As there are some errors in the hardware manuals for the above products, we would like to announce the corrections.

1. The description under (3) Programming Execution in 22.4.3, Programming/Erasing Interface Parameters, of Section 22, Flash Memory, is amended as follows.

[Before amendment]
2. The program data for the user MAT must be prepared in the consecutive area.
   The program data must be in the consecutive space, which can be accessed by using the MOV.B instruction of the CPU, and is not the flash memory space.

[After amendment]
2. The program data for the user MAT must be prepared in the consecutive area.
   The program data must be in the consecutive space, which can be accessed by using the MOV.L instruction of the CPU, and is not the flash memory space.

2. The description in Figure 22.9, Programming/Erasing Overview Flow, in Section 22, Flash Memory, was amended as follows.

[Before amendment]
4. Programming/erasing is executed only in the on-chip RAM.
   However, if the program data is in a consecutive area and can be accessed by the MOV.B instruction of the CPU like SRAM/ROM, the program data can be in an external space.

[After amendment]
4. Programming/erasing is executed only in the on-chip RAM.
   However, if the program data is in a consecutive area and can be accessed by the MOV.L instruction of the CPU like SRAM/ROM, the program data can be in an external space.