RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU			Document No.	TN-RX*-A0216A/E	Rev.	1.00
Title	Addition of New Package (0.8-mm pitch 64-pin LQFP) Products to RX24T Group			Information Category	Technical Notification		
Applicable Product	RX24T Group,	Lot No.		Reference Document	RX24T Group User's Manual: Hardware Rev.2.00 (R01UH0576EJ0200)		
		All					

New package products (0.8-mm pitch 64-pin LQFP package) are added to the RX24T group MCU lineup.

This document contains a list, a pin assignment, and package dimensions of products to be added.

1. List of New Products

Four products listed in Table 1 are added to RX24T group.

Table 1.   List of RX24T Group New Package Products

Group	Part No.	Part No. (for Orders)	Package	ROM Capacity	RAM Capacity	E2 DataFlash	Operating Frequency (max.)	Operating Temperature
RX24T (D-version)	R5F524TAADFK	R5F524TAADFK#31	PLQP0064GA-A	256 Kbytes	16 Kbytes	8 Kbytes	80 MHz	–40 to 85°C
	R5F524T8ADFK	R5F524T8ADFK#31	PLQP0064GA-A	128 Kbytes				
RX24T (G-version)	R5F524TAAGFK	R5F524TAAGFK#31	PLQP0064GA-A	256 Kbytes				–40 to 105°C
	R5F524T8AGFK	R5F524T8AGFK#31	PLQP0064GA-A	128 Kbytes				

2. Pin Assignment

Pin assignment for products listed above is the same as the products with 64-pin LFQFP package.

3.   Package Dimensions

The package dimensions for the 0.8-mm pitch 64-pin LQFP package (PLQP0064GA-A) are as follows.

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-LQFP64-14x14-0.80	PLQP0064GA-A	64P6U-A/ —	0.7g

NOTE)
1. DIMENSIONS "*1" AND "*2" DO NOT INCLUDE MOLD FLASH.
2. DIMENSION "*3" DOES NOT INCLUDE TRIM OFFSET.

Terminal cross section

Detail F

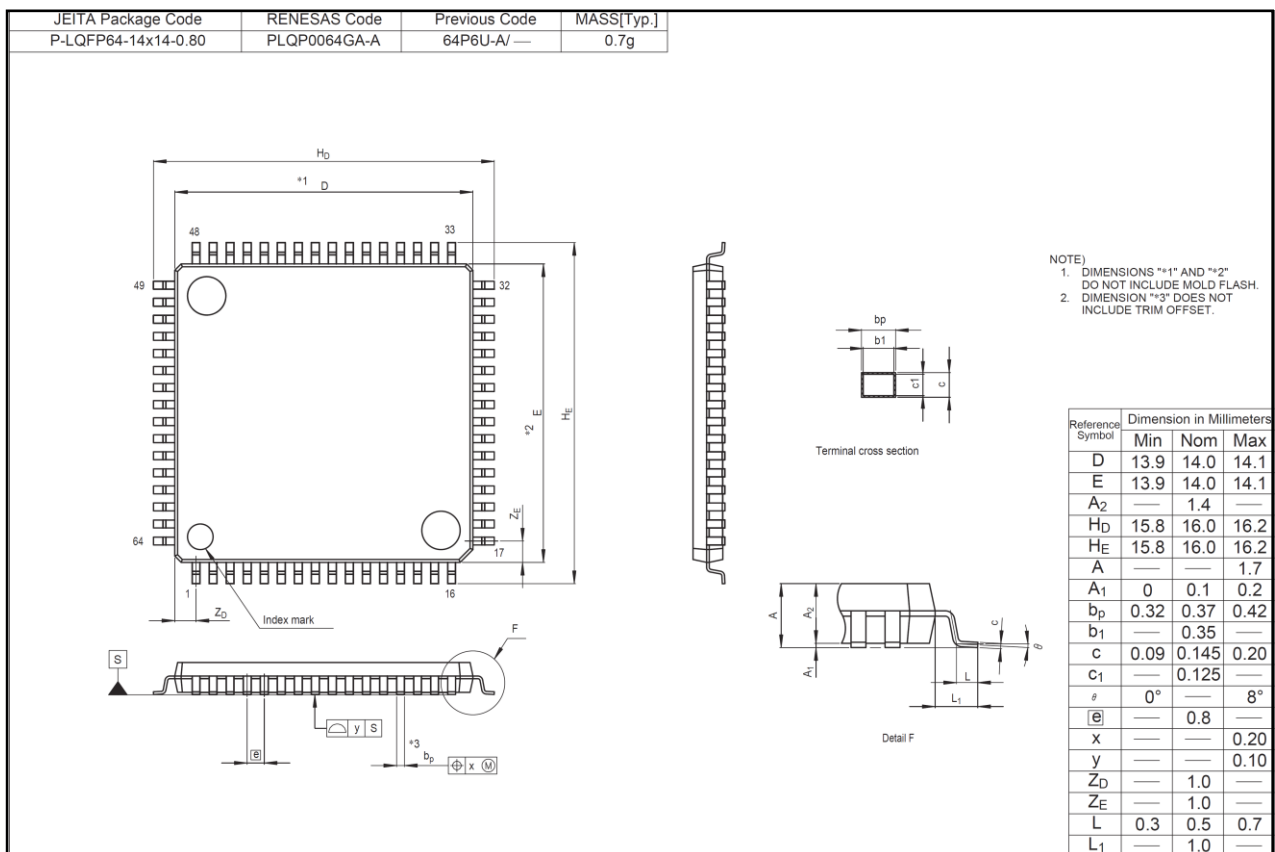| Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	13.9	14.0	14.1
E	13.9	14.0	14.1
A_2	—	1.4	—
H_D	15.8	16.0	16.2
H_E	15.8	16.0	16.2
A	—	—	1.7
A_1	0	0.1	0.2
b_p	0.32	0.37	0.42
b_1	—	0.35	—
c	0.09	0.145	0.20
c_1	—	0.125	—
θ	0°	—	8°
e	—	0.8	—
x	—	—	0.20
y	—	—	0.10
Z_D	—	1.0	—
Z_E	—	1.0	—
L	0.3	0.5	0.7
L_1	—	1.0	—

Figure 1.   64-Pin LQFP (PLQP0064GA-A)