To our customers,

**Old Company Name in Catalogs and Other Documents**

On April 1\(^{st}\), 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: [http://www.renesas.com](http://www.renesas.com)

April 1\(^{st}\), 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation ([http://www.renesas.com](http://www.renesas.com))

We would like to inform you of the addition to and corrections in the above listed hardware manuals. Please refer to the following for details.

< Addition to and correction of errors regarding GSR3 judgment >

1. **SH7280 Group Hardware Manual**
   - Section 22, Controller Area Network (RCAN-ET) (SH7286 Only)
   - Page 1062, notes of figure 22.8, Reset Sequence

2. **SH7216 Group Hardware Manual**
   - Section 21, Controller Area Network (RCAN-ET)
   - Page 1082, notes of figure 21.8, Reset Sequence
[Before Change]

### Reset Sequence

**Configuration Mode**

1. Power On/SW Reset*
2. MCR[0] = 1 (automatically in hardware reset only)
3. IRR[0] = 1, GSR[3] = 1 (automatically)
4. Clear IRR[0] Bit
5. Configure MCR[15]
6. Clear Required IMR Bits
7. Mailbox Setting (STD-ID, EXT-ID, LAFM, DLC, RTR, IDE, MBC, MBIMR, DART, ATX, NMC, Message-Data)*
8. Set Bit Timing (BCR)
9. Clear MCR[0]
10. GSR[3] = 0?
11. Yes
   - RCAN-ET is in Tx_Rx Mode
   - Set TXPR to start transmission or stay idle to receive
   - Transmission_Reception (Tx_Rx) Mode
   - Detect 11 recessive bits and join the CAN bus activity
   - Receive*3
   - Transmit*3
12. No

**Notes:**
1. SW reset could be performed at any time by setting MCR[0] = 1.
2. Mailboxes are comprised of RAMs, therefore, please initialise all the mailboxes enabled by MBC.
3. If there is no TXPR set, RCAN-ET will receive the next incoming message.
   - If there is a TXPR(s) set, RCAN-ET will start transmission of the message and will be arbitrated by the CAN bus.
   - If it loses the arbitration, it will become a receiver.
   - If it loses the arbitration, it will become a receiver.
[After Change]

* Addition of a note 3

Notes:
1. SW reset could be performed at any time by setting MCR[0] = 1.
2. Mailboxes are comprised of RAMs, therefore, please initialise all the mailboxes enabled by MBC.
3. Judgment of the acknowledgement 0 takes about 1 bit period.
4. If there is no TXPR set, RCAN-ET will receive the next incoming message.
   If there is a TXPR(s) set, RCAN-ET will start transmission of the message and will be arbitrated by the CAN bus.
   If it loses the arbitration, it will become a receiver.