RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SY*-A0077A/E	Rev.	1.00
Title	Add a restriction on exclusive access		Information Category	Technical Notification		
Applicable Product	Synergy S1 Series S1JA Group	Lot No.	Reference Document	-Renesas S1JA Group User's Manual: Hardware R01UM0008EU0140 Rev.1.40		
		All lots				

Add a below restriction to above mentioned applicable products.

14.2.5 Restriction on Exclusive Access

The main bus does not support exclusive transfers and there is no global monitor in the MCU.

The main bus always deasserts the HEXOKAY signal (a signal in the AHB-Lite protocol) to the CPU.

This means that a store exclusive instruction such as STREX instruction always gets a failed status.

When an exclusive write operation is performed by the CPU, the main bus always writes the data successfully.