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Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU	Document No.	TN-H8*-A327A/E	Rev.	1.00
Title	About event counter PWM operation	Information Category	Technical Notification		
Applicable Product	H8/38086R Group H8/38076R Group	Lot No.	Reference Document	H8/38086R Group Hardware manual (REJ09B0182-0200 Rev.2.00) H8/38076R Group Hardware manual (REJ09B0093-0300 Rev.3.00)	
		All			

The error correction of the hardware manual of event counter PWM operation of H8/38086R group and H8/38076R group, Please refer to the following for details.

< Before change >

H8/38086R Group Hardware Manual (Page 292 of 644)

H8/38076R Group Hardware Manual (Page 290 of 620)

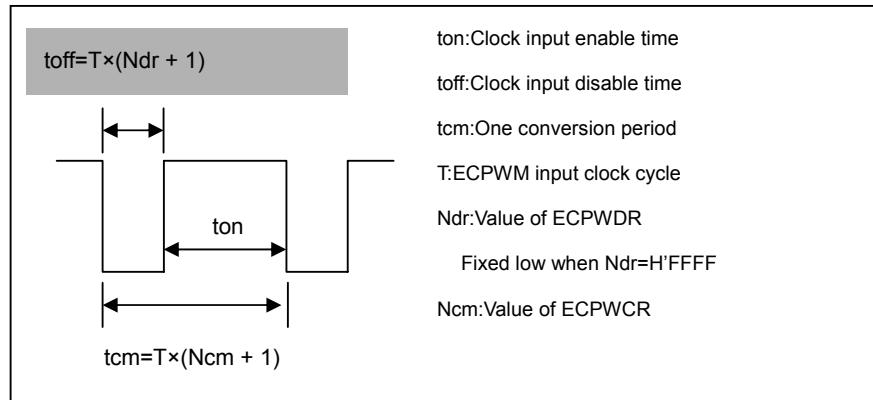


Figure 13.4 Event Counter Operation Waveform

< After change >

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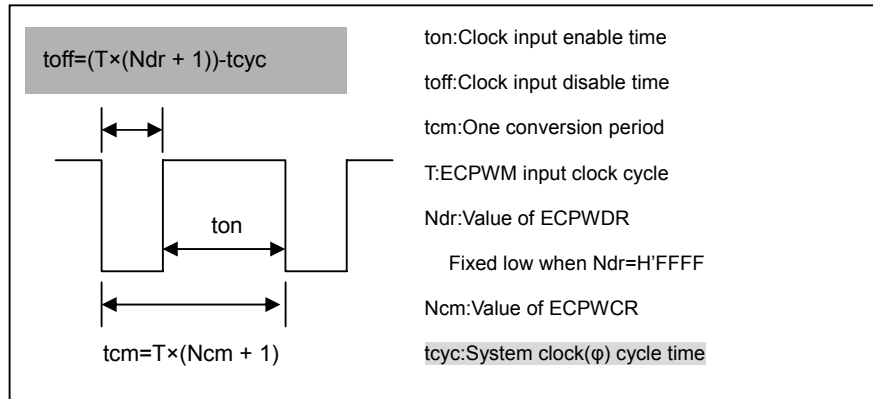


Figure 13.4 Event Counter Operation Waveform

< Before change >

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Table 13.2 Examples of Event Counter PWM Operation

Conditions: fosc = 4MHz, f ϕ = 4MHz, high-speed active mode, ECPWCR value (Ncm) = H'7A11,

ECPWDR value (Ndr) = H'16E3

Clock Source Selection	Clock Source Cycle (T)*	ECPWCR Value (Ncm)	ECPWDR Value (Ndr)	toff = T × (Ndr+1)	tcm = T × (Ncm + 1)	ton = tcm – toff
$\phi/2$	0.5 μ s	H'7A11	H'16E3	2.93ms	15.625ms	12.695ms
$\phi/4$	1 μ s	D'31249	D'5859	5.86ms	31.25ms	25.39ms
$\phi/8$	2 μ s			11.72ms	62.5ms	50.78ms
$\phi/16$	4 μ s			23.44ms	125.0ms	101.56ms
$\phi/32$	8 μ s			46.88ms	250.0ms	203.12ms
$\phi/64$	16 μ s			93.76ms	500.0ms	406.24ms

< After change >

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Table 13.2 Examples of Event Counter PWM Operation

Conditions: fosc = 4MHz, fφ = 4MHz, high-speed active mode, ECPWCR value (Ncm) = H'7A11,

ECPWDR value (Ndr) = H'16E3

Clock Source Selection	Clock Source Cycle (T)*	ECPWCR Value (Ncm)	ECPWDR Value (Ndr)	toff = (T × (Ndr+1)) - tcyc	tcm = T × (Ncm + 1)	ton = tcm – toff
φ/2	0.5us	H'7A11	H'16E3	2.92975ms	15.625ms	12.69525ms
φ/4	1us	D'31249	D'5859	5.85975ms	31.25ms	25.39025ms
φ/8	2us			11.71975ms	62.5ms	50.78025ms
φ/16	4us			23.43975ms	125.0ms	101.56025ms
φ/32	8us			46.87975ms	250.0ms	203.12025ms
φ/64	16us			93.75975ms	500.0ms	406.24025ms