

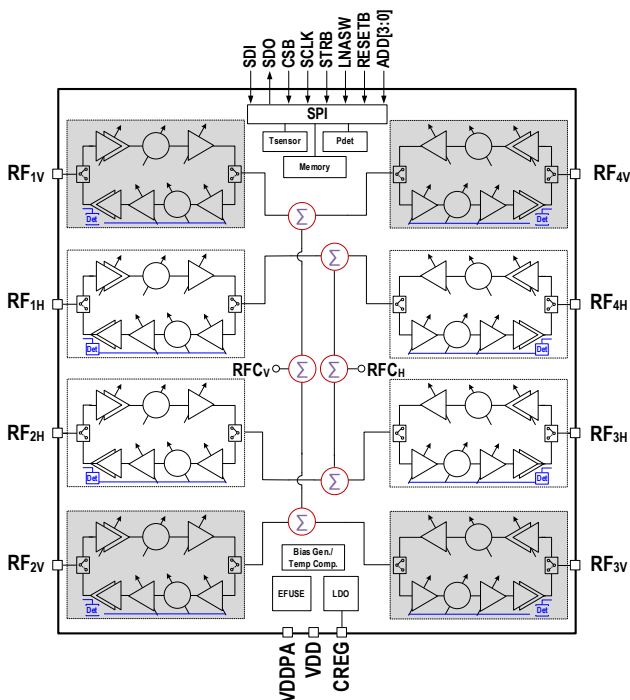
F5268

26GHz 8-Channel, Half-Duplex Transceiver IC

The F5268 is an 8-channel half-duplex transceiver (TRX) silicon integrated circuit (IC). The device is designed using a SiGe BiCMOS process for dual-polarization 5G phased-array applications at n258 and n261 bands. The core IC has highly flexible gain and phase control on each channel to achieve fine beam steering and gain compensation between radiating channels.

The F5268 includes a standard SPI protocol that operates up to 65MHz with fast beam switching and fast beam-state loading. Up to 16 devices can be supported on the same SPI bus using dedicated address pins. Each device uses a 2.5V analog supply and a selectable 2.5V/3.3V PA supply to provide dual TX power modes. The digital core and SPI use a 1.8V supply generated by an on-chip LDO.

Block Diagram



Features

- 24.25 - 27.5GHz operation (5G NR n258/n261)
- 8 radiation channels
- 100ns typical TX/RX mode switching time
- 20ns typical gain and phase settling time
- 1.4° typical RMS phase error
- 0.2dB typical RMS gain error
- 30.5dB gain attenuation range
- Integrated PTAT, PTAT2, and Bandgap generator
- Internal temperature sensor and power detector
- 4-bit chip address (hard-wired/programmable)
- Up to 65MHz SPI control
- 2048 on-chip programmable beam states
- Analog supply voltage: +2.4V to +2.6V
- Dedicated PA supply voltage: selectable between +2.4V to +2.6V and +3.0V to +3.3V
- 5.1 × 5.1 × 0.8 mm, 118-FCCSP package
- -40°C to +95°C operating temperature range

Applications

- 5G Single/Dual-Polarization Phased-Array Antenna System
- Beam Steering

Ordering Information

Part Number	Package (mm)	Carrier Type
RA81F5268STGBX#BC0	5.1 × 5.1 × 0.8 118-FCCSP	Tray
RA81F5268STGBX#KC0	5.1 × 5.1 × 0.8 118-FCCSP	Reel
RTKA81F5268ST000RU	Eval. Board	-

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