

Description

The 8V97003 is a high-performance wideband microwave Synthesizer / Phase Lock Loop (PLL) that generates output frequencies up to 18GHz from an integrated Voltage Controlled Oscillator (VCO) offering an octave of frequency tuning range. The device also offers a high-performance 32-bit fractional feedback divider and an output divider to allow users to fully benefit from the wideband characteristics of the VCO.

The device's figure of merit (FOM) of -237dBc/Hz and the excellent VCO performance allow for very low phase noise and RMS phase jitter.

The 8V97003 offers a very low output-to-output and input-to-output phase skew drift in temperature of $< 7^\circ$ across the entire ambient temperature range ($\ll 1^\circ$ of phase skew drift per degree Celsius of temperature variation, on average), reducing radio path recalibration occurrences in beamforming applications, such as 5G radio card massive MIMO systems.

The output drivers have programmable output power settings and can deliver high single-ended output power up to +13dBm at 6GHz, up to +10dBm at 12GHz, and up to +4dBm at 18GHz when using inductively loaded output terminations, and with the outputs being double terminated. When the outputs are resistively loaded, the output drivers can deliver single-ended output power up to +8dBm at 6GHz, and up to +6dBm at 12GHz. The output power can be further increased when using differential outputs and measuring the output power differentially.

The 8V97003 relies on a single 3.3V power supply and offers low noise integrated LDOs for excellent power supply noise immunity.

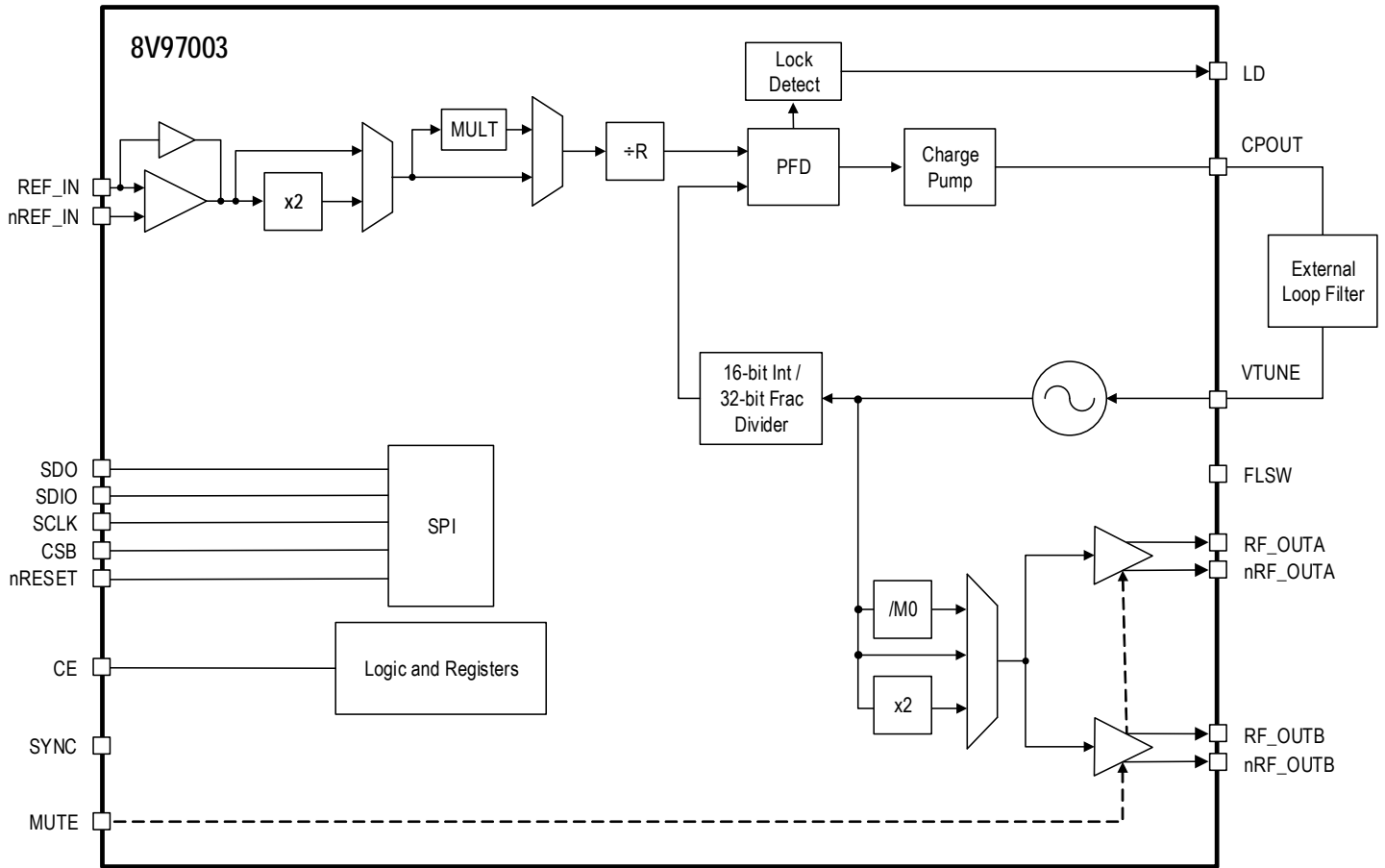
Typical Applications

- 5G millimeter wave wireless infrastructure
- Massive MIMO
- Phase Array Antennas and beam forming
- Wireless backhaul
- Point-to-point and point-to-multipoint microwave links
- Satellites / VSATs
- Test equipment/instrumentation
- Clock generation
- High-speed RF converters sampling clocks
- Radar

Features

- Output frequency range: 187.5MHz to 18GHz
- Ultra-low phase noise VCO
 - -60dBc integrated phase jitter (34fs rms jitter) from 20kHz to 100MHz at 6GHz
- Figure of Merit: -237dBc/Hz
- Input reference frequency:
 - 10MHz to 1GHz (LVPECL, LVDS)
 - 10MHz to 250MHz (LVCMOS)
- Fractional-N synthesizer and integer-N synthesizer
- 32-bit of fractional and modulus resolution
- Phase frequency detector (PFD) operation up to 500MHz (Integer mode) or 250MHz (Fractional mode)
- Programmable RF output power levels
- RF output power $< \text{TBDdBm}$ when in MUTE
- Programmable input multiplier (MULT) to increase PFD frequency when using a low input frequency
- -40°C to $+95^\circ\text{C}$ ambient temperature range; and up to $+105^\circ\text{C}$ case temperature
- 3.3V single power supply operation
- 7×7 mm 48-VFQFPN package
- SPI interface is compatible with 1.8V logic and tolerant to 3.3V
- Supported in the Timing Commander™ design tool

Block Diagram



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