

Bootup Configuration

Device 8-bit I2C address	Default Configuration
0xf8	EEPROM (8-bit EEPROM address 0xa0)

Inputs

Crystal Frequency	Input CLK0	Input CLK1
38.88MHz	—	—

Outputs

Output	Frequency (MHz)	Type	Enable	Polarity	Voltage	Source	Divider	Err
Q0	—	High-Z	Disabled	Normal	3.3	PLL	—	—
Q1	—	High-Z	Disabled	Normal	3.3	PLL	—	—
Q2	—	High-Z	Disabled	Normal	3.3	PLL	—	—
Q3	—	High-Z	Disabled	Normal	3.3	PLL	—	—

PLL

Frequency	Mode	Acquire BW	Lock BW	CLK0 PFD	CLK1 PFD
—	Synthesizer	25Hz	25Hz	—	—

Power

Typical Power	Max Power
—	—

GPIOs

GPIO0	GPIO1	GPIO2	GPIO3
In – OE0	In – OE1	In – CSEL	In – GPI

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