

Tsi382A (BGA) Evaluation Board Schematics

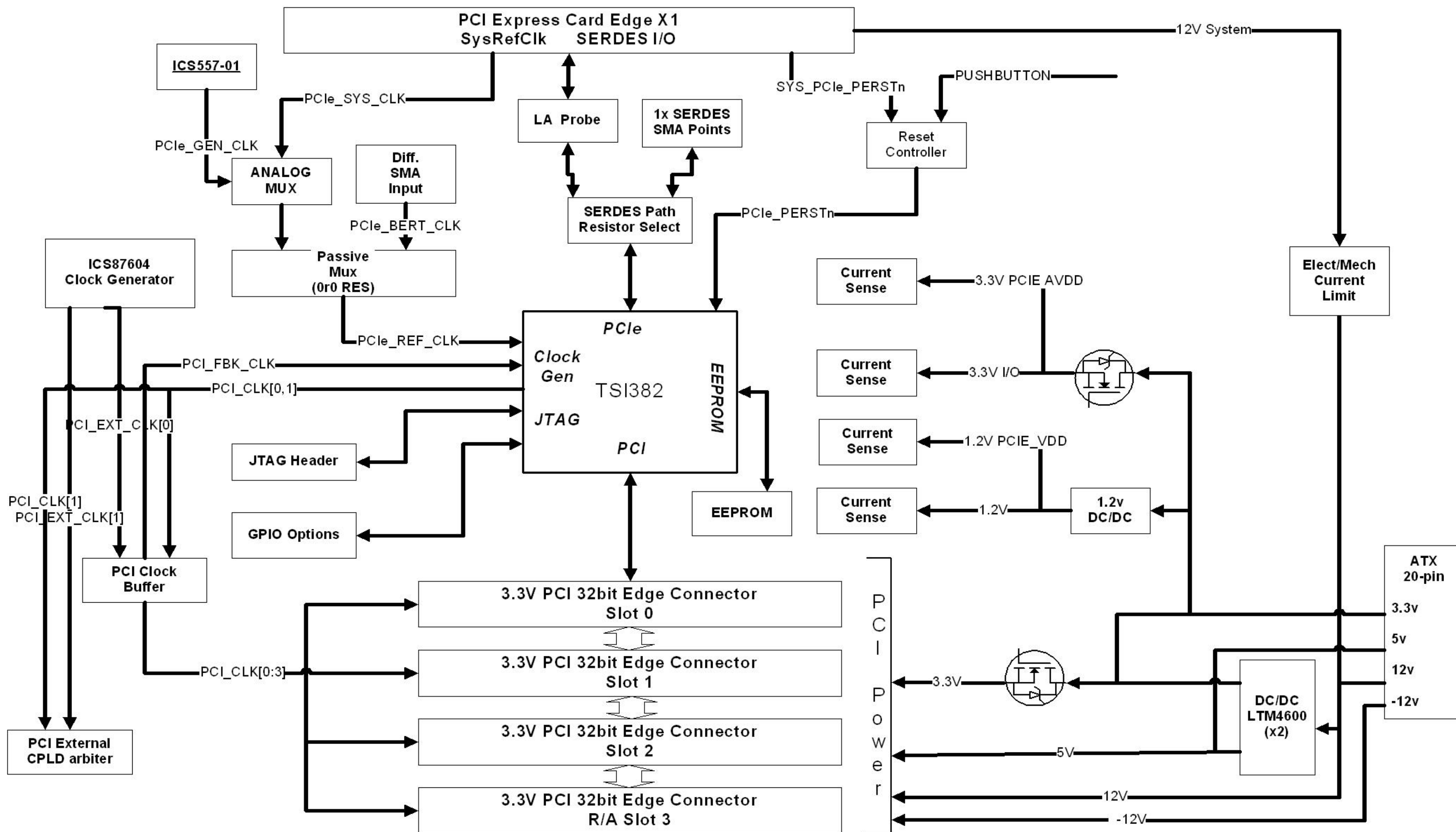
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C	60E204A_SC001_01	1.0	PHILIP LAUZON
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BLOCK DIAGRAM

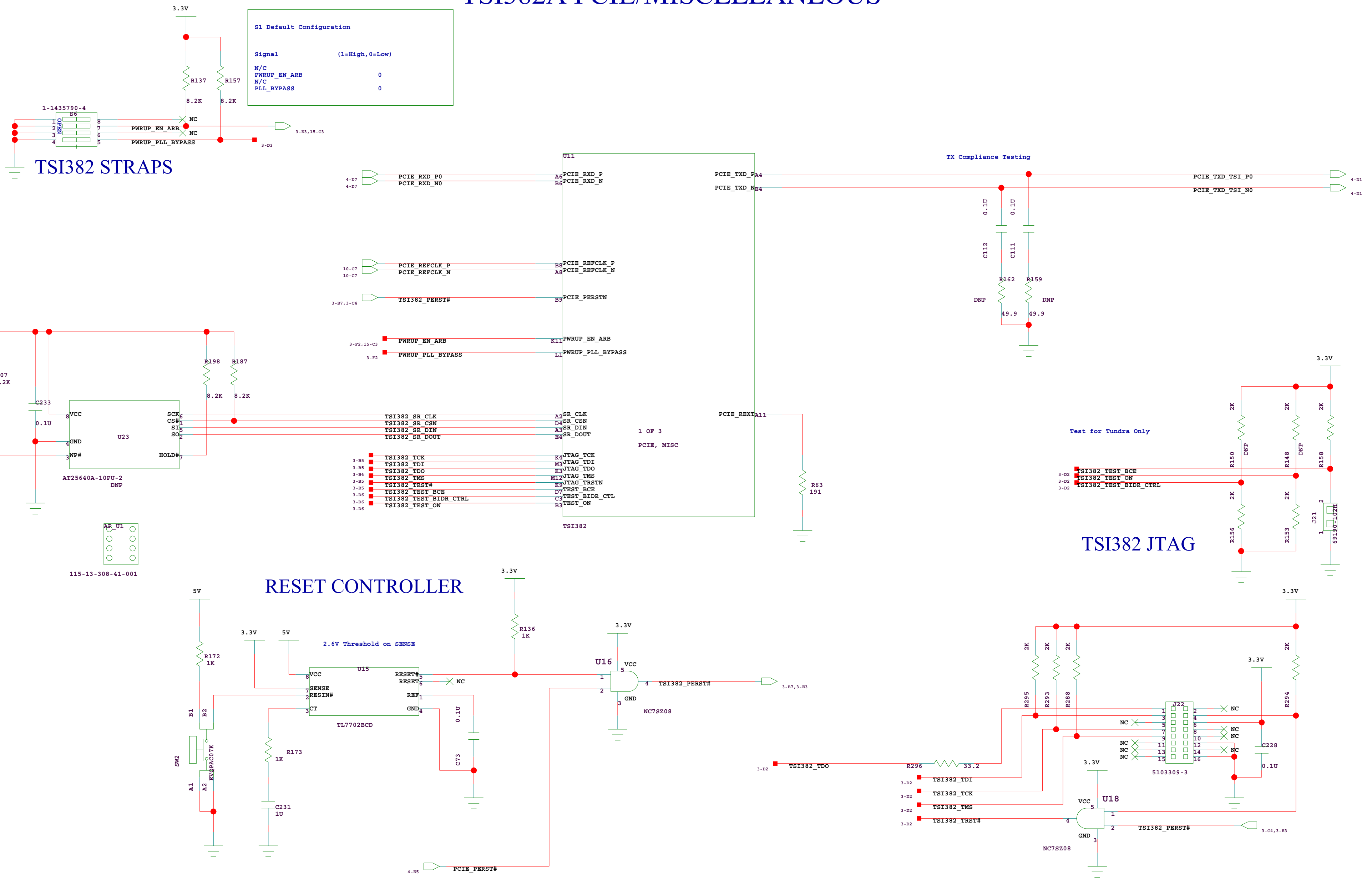


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TSI382A PCIE/MISCELLANEOUS



S1 Default Configuration

Signal	(1=High,0=Low)
N/C	0
PWRUP_EN_ARB	0
N/C	0
PLL_BYPASS	0

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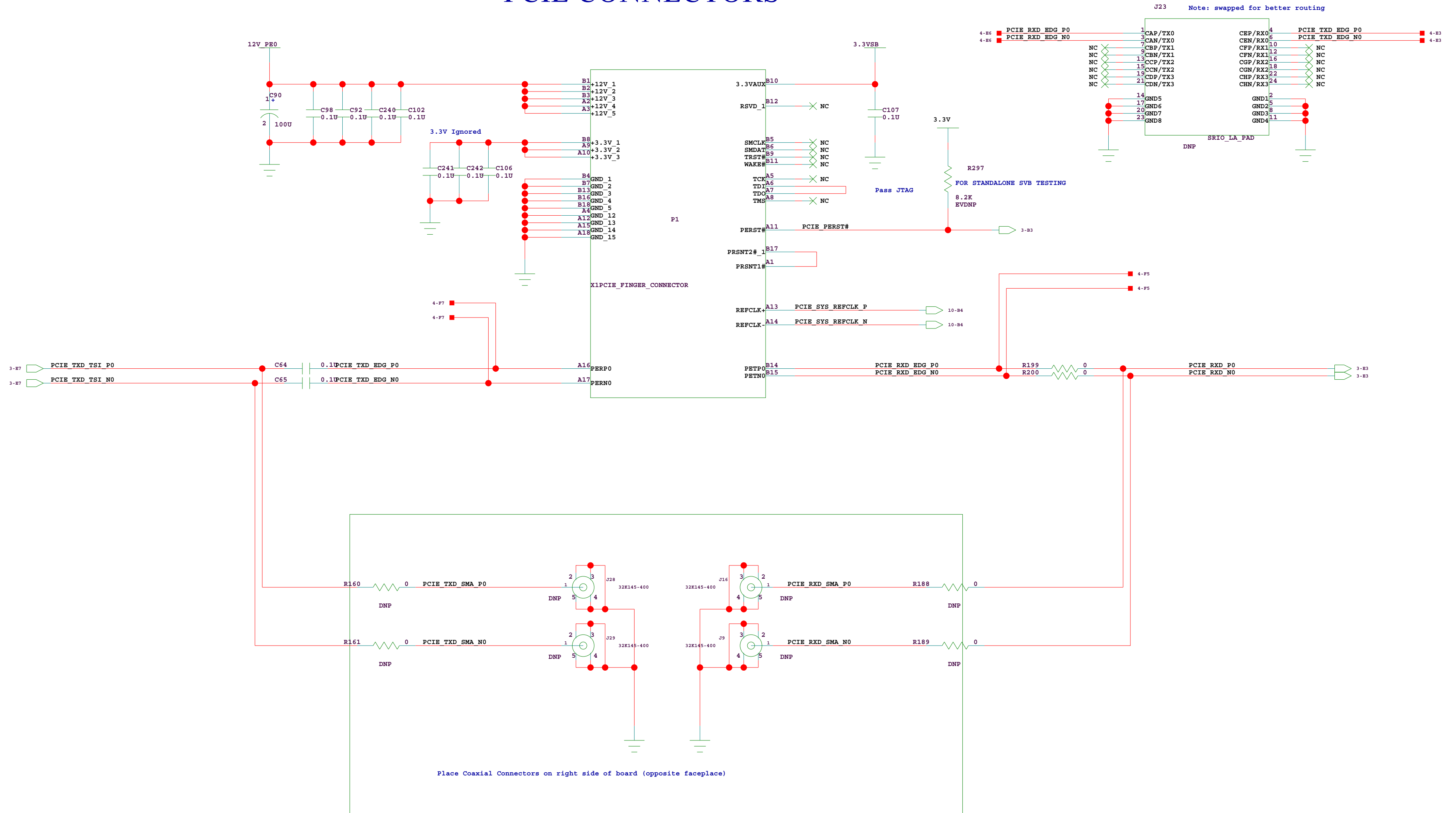
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TSI382A PCIE/MISC

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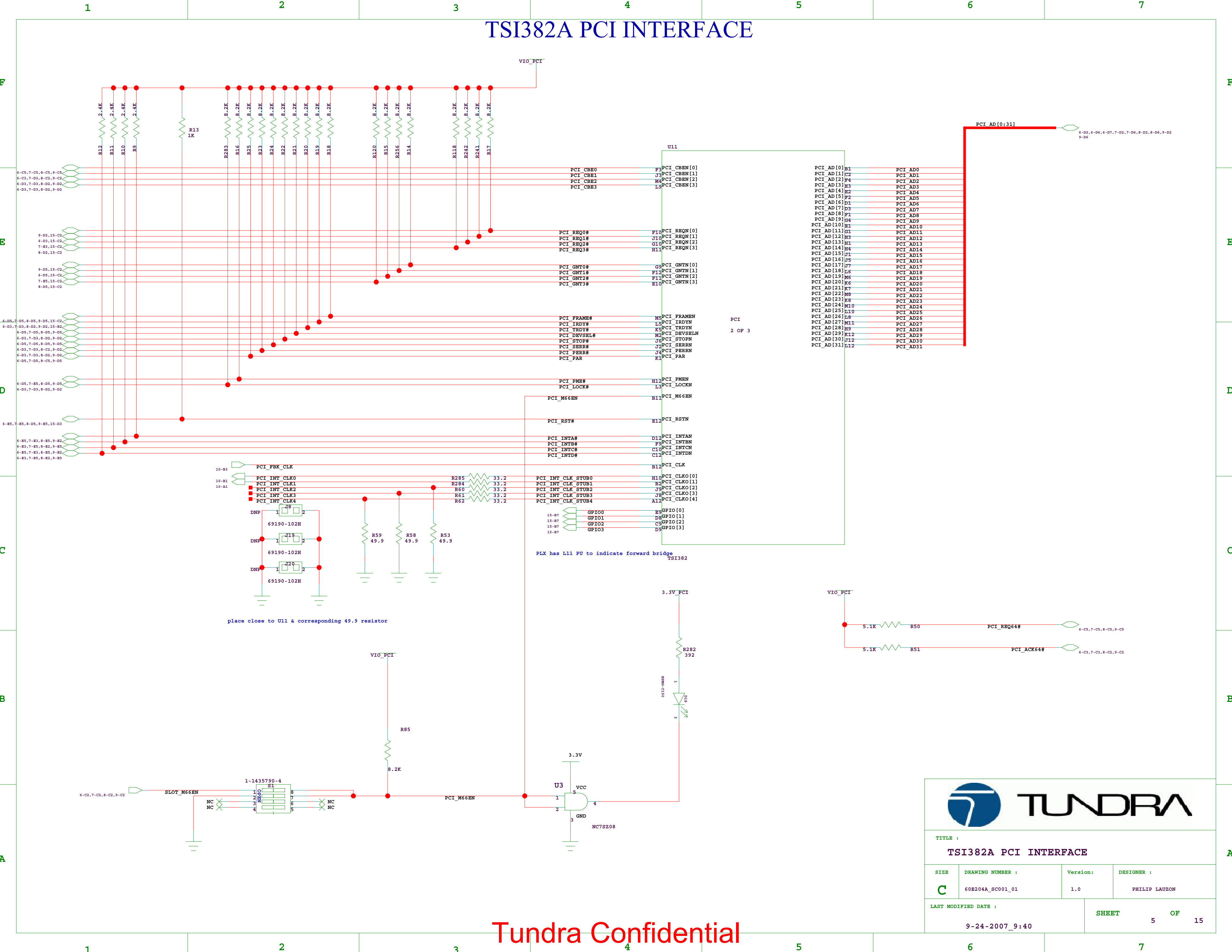
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PCIE CONNECTORS



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PCIE CONNECTORS			
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TSI382A PCI INTERFACE



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PLX has L11 PU to indicate forward bridge TSI382

place close to U11 & corresponding 49.9 resistor

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TITLE :
TSI382A PCI INTERFACE

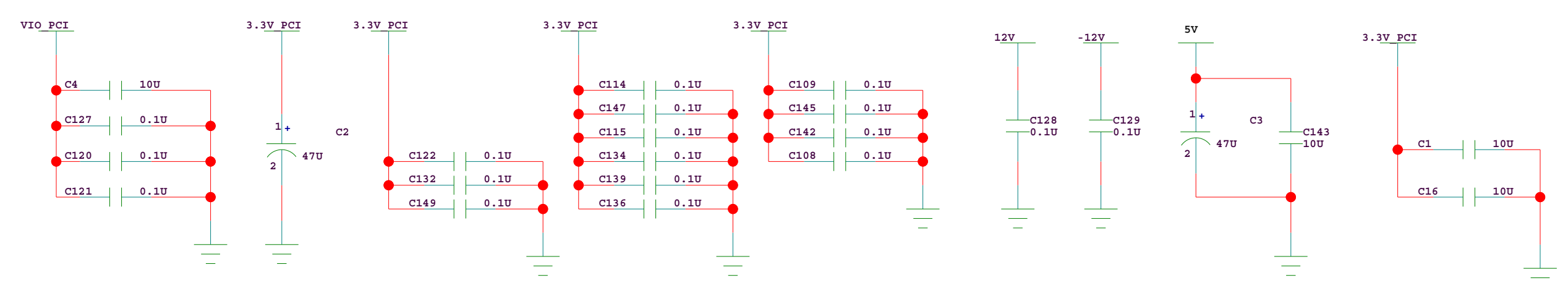
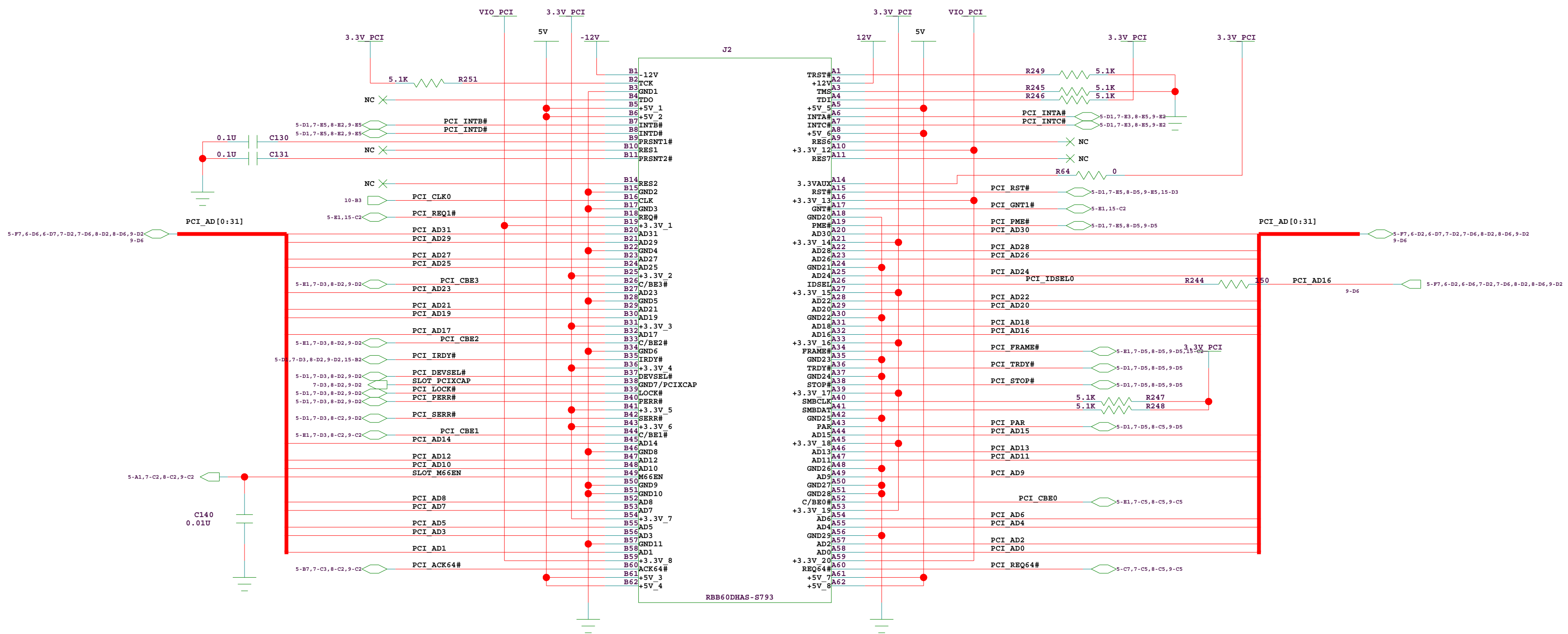
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PCI SLOT 0 (R/A)

IDSEL AD16
 - Required Interrupt Routing
 SLOT : BRIDGE
 INTA -> INTA
 INTB -> INTB
 INTC -> INTC
 INTD -> INTD
 - PCI Clock PCI_CLK0
 - PCI Arbitration PCI_GNT#1/PCI_REQ#1





TITLE :
PCI SLOT 0

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PCI SLOT 1 (Vertical)

IDSEL AD17
 - Required Interrupt Routing
 SLOT : BRIDGE
 INTA -> INTB
 INTB -> INTC
 INTC -> INTD
 INTD -> INTA

 - PCI Clock PCI_CLK1

 - PCI Arbitration PCI_GNT#2/PCI_REQ#2



Tundra Confidential

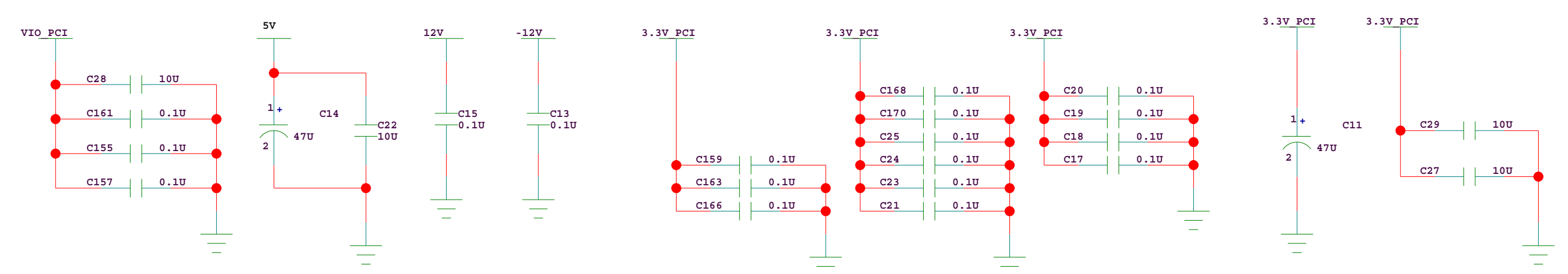
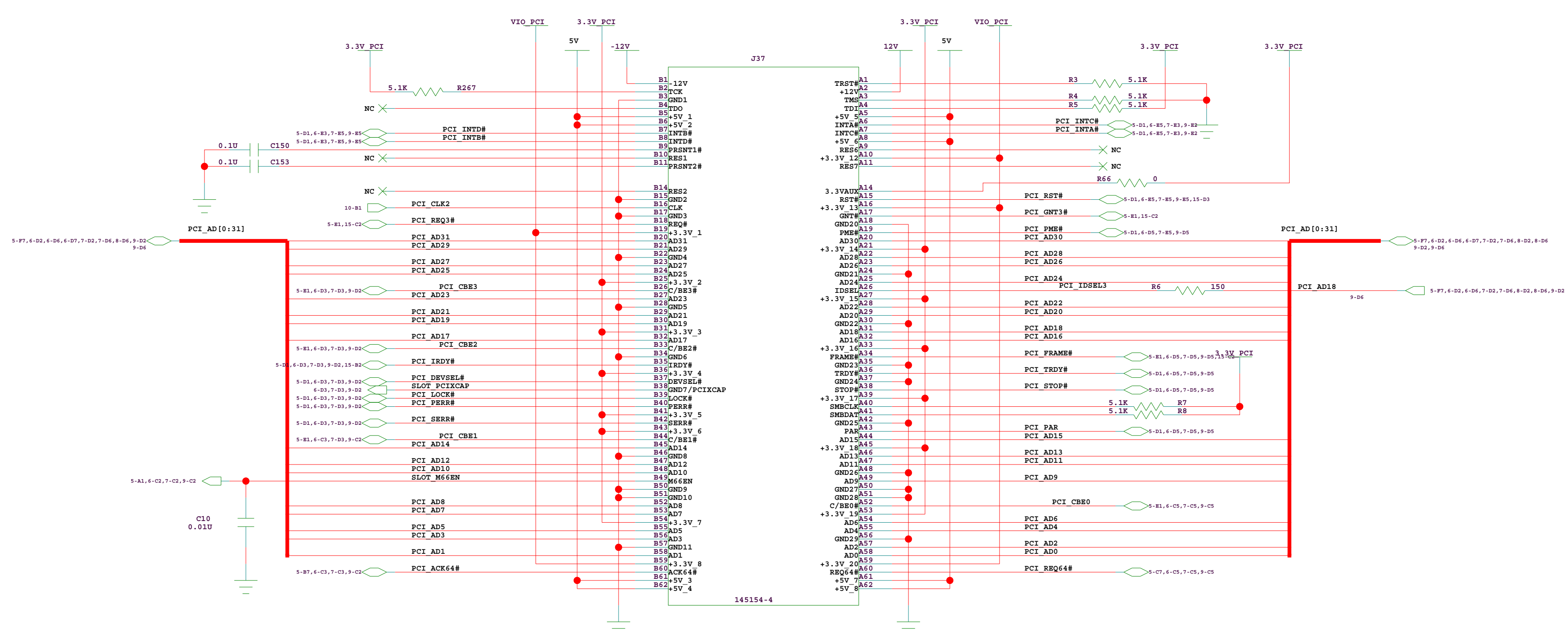
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PCI SLOT 1
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PCI SLOT 3 (Vertical)

IDSEL AD18
 - Required Interrupt Routing
 SLOT : BRIDGE
 INTA -> INTC
 INTB -> INTD
 INTC -> INTA
 INTD -> INTB

 - PCI Clock PCI_CLK2

 - PCI Arbitration PCI_GNT#3/PCI_REQ#3

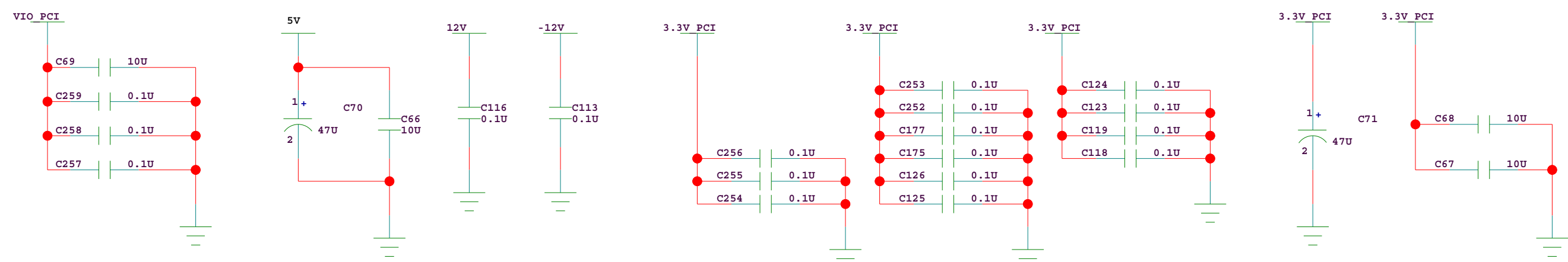
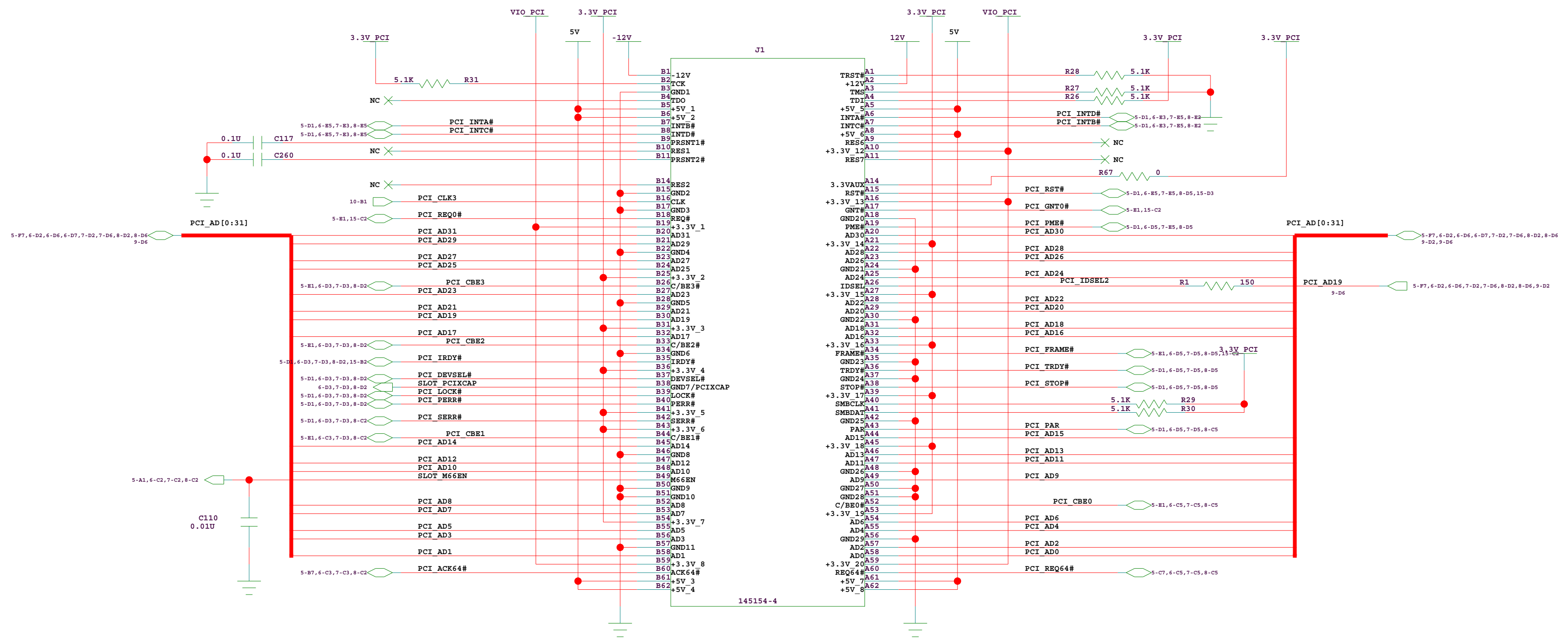




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PCI SLOT 2 (Vertical)

IDSEL AD19
 - Required Interrupt Routing
 SLOT : BRIDGE
 INTA -> INTD
 INTB -> INTA
 INTC -> INTB
 INTD -> INTC
 - PCI Clock PCI_CLK3
 - PCI Arbitration PCI_GNT#0/PCI_REQ#0



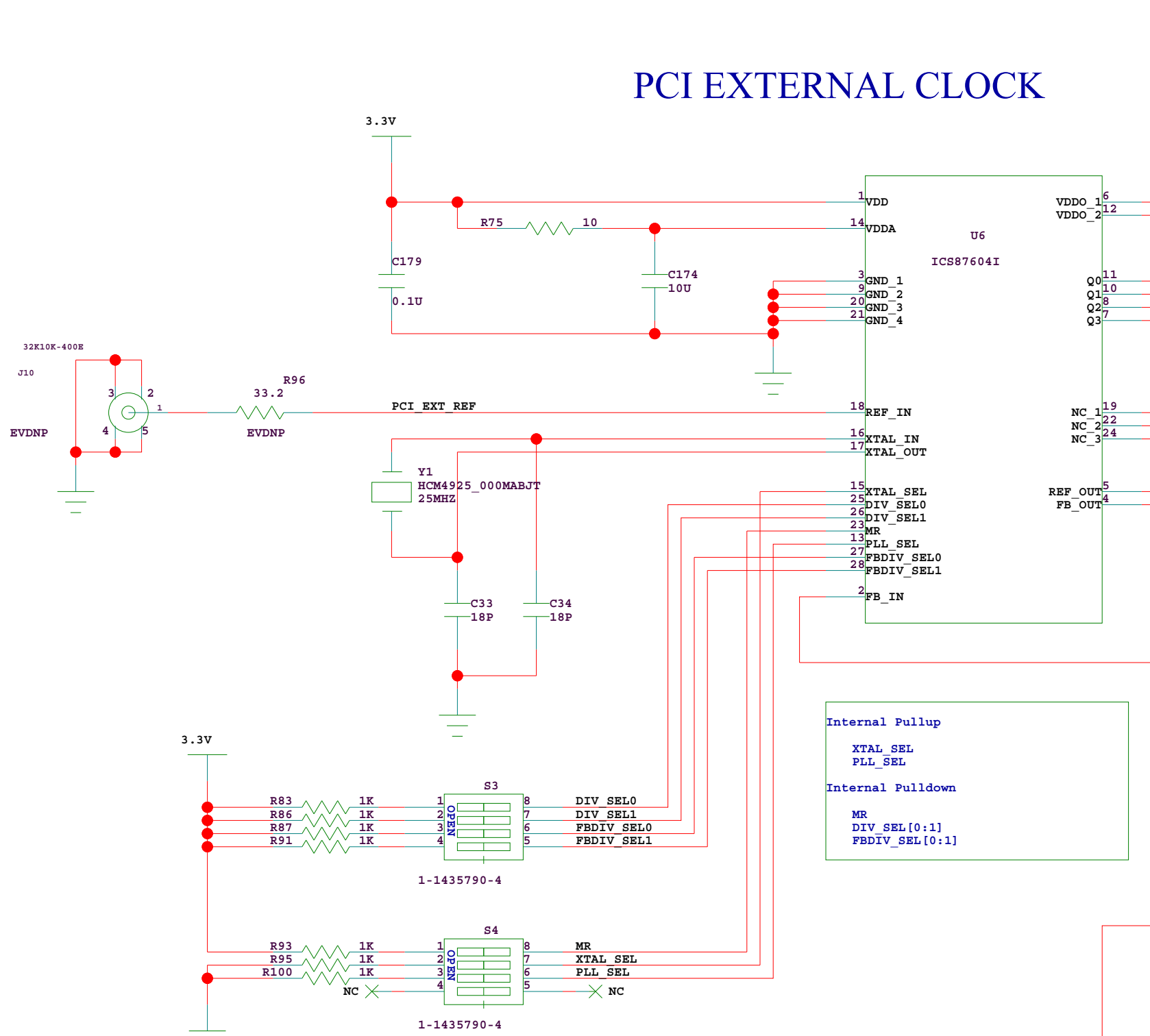
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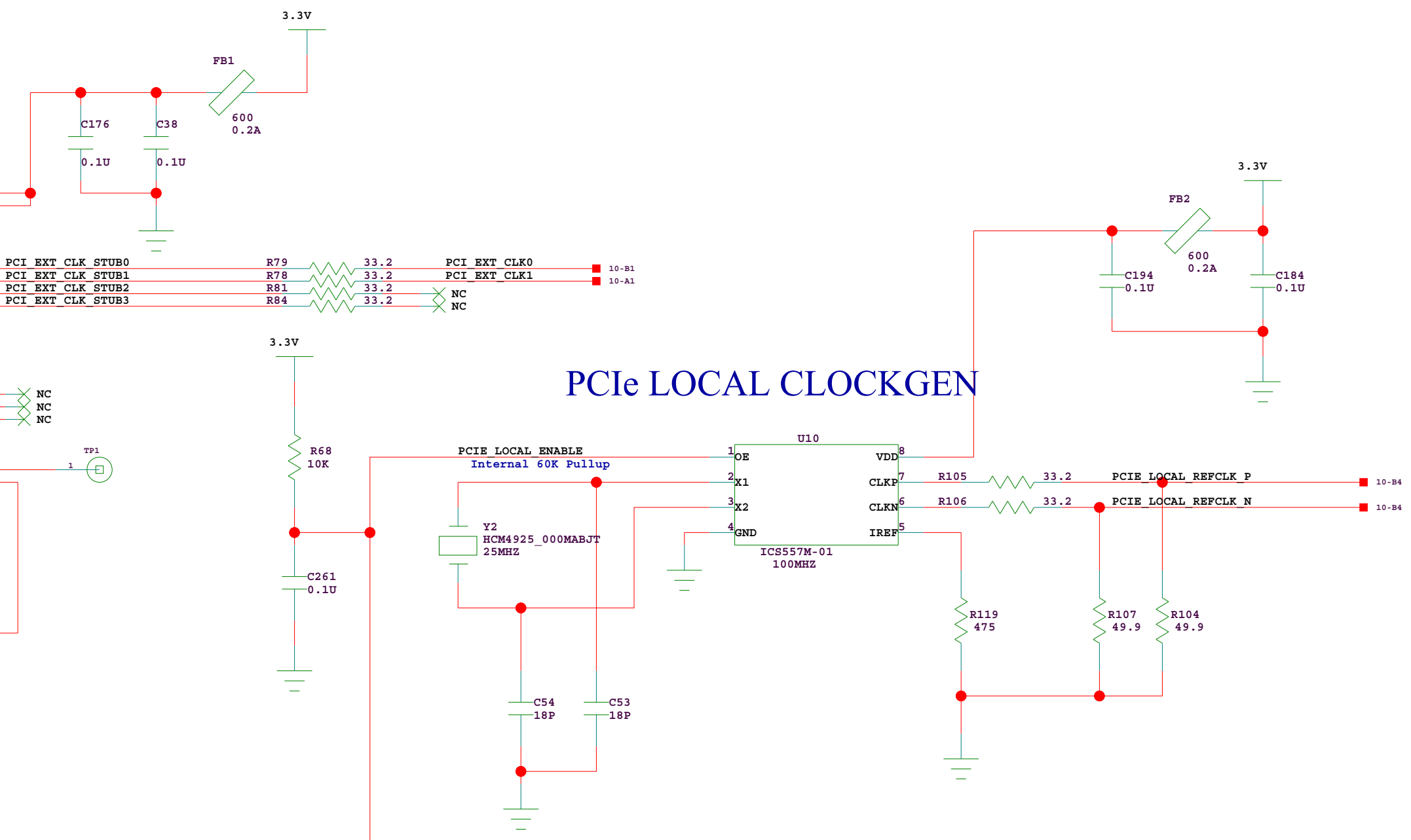
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CLOCK DISTRIBUTION

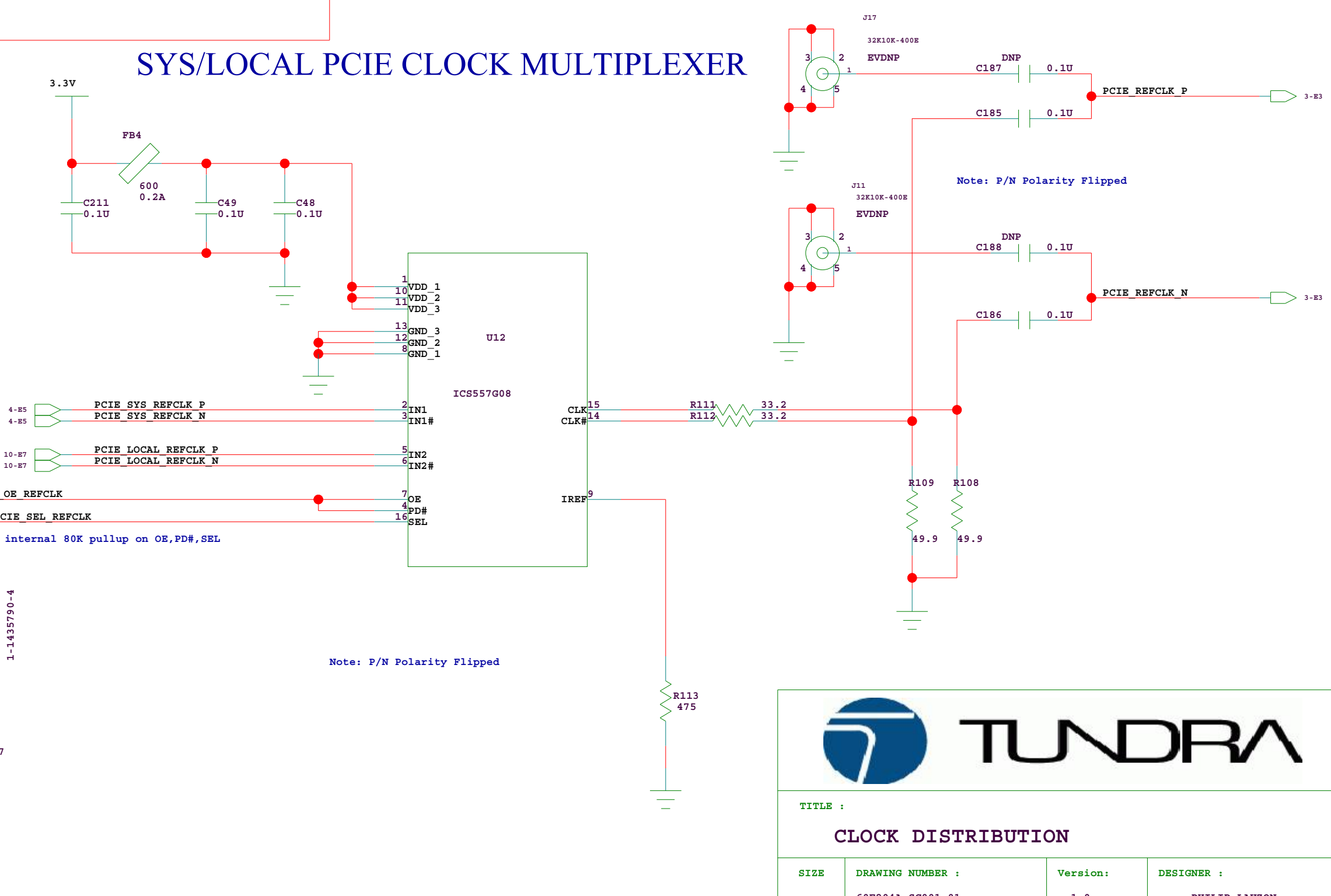
PCI EXTERNAL CLOCK



PCIe LOCAL CLOCKGEN



SYS/LOCAL PCIe CLOCK MULTIPLEXER



Frequency Select (ICS87604I)

FBDIV_SEL[0:1]	DIV_SEL[0:1]	OUTPUT
0:0	0:0	100Mhz
0:0	1:0	50Mhz
0:0	1:1	25Mhz
0:1	0:0	133Mhz
0:1	1:0	66Mhz
0:1	1:1	33Mhz

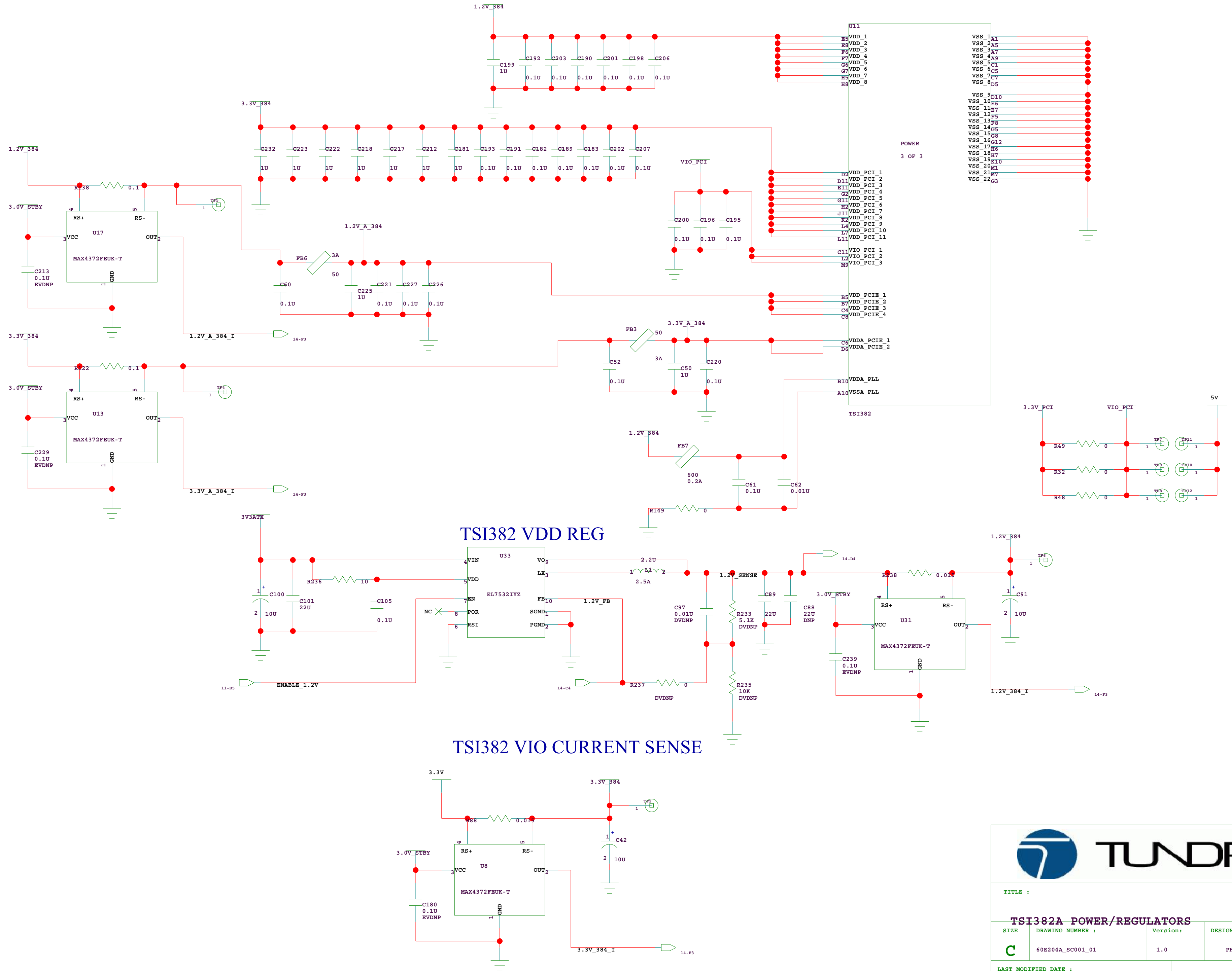
Place resistors near CY2305

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CLOCK DISTRIBUTION

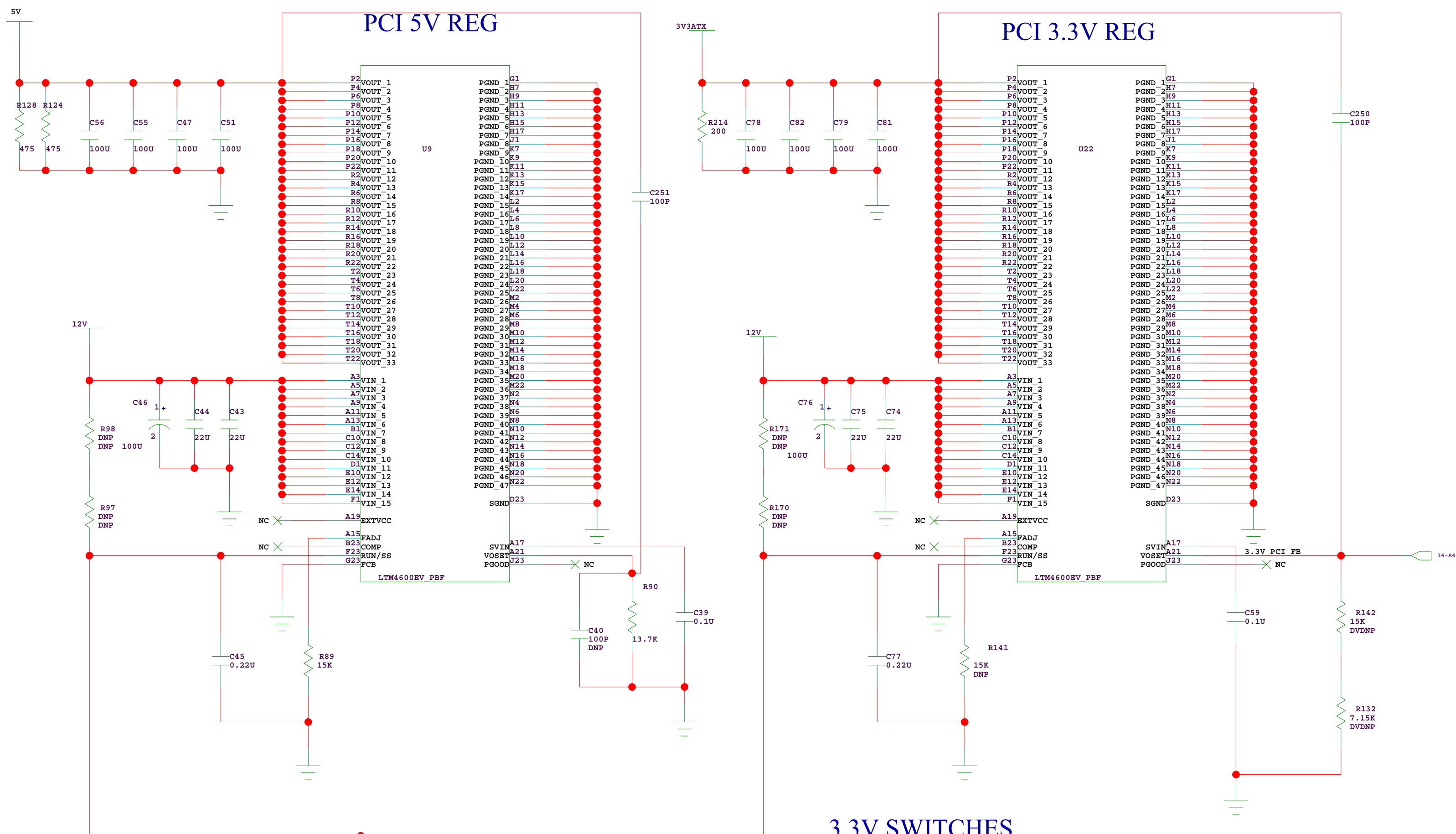
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TSI382A POWER/REGULATORS

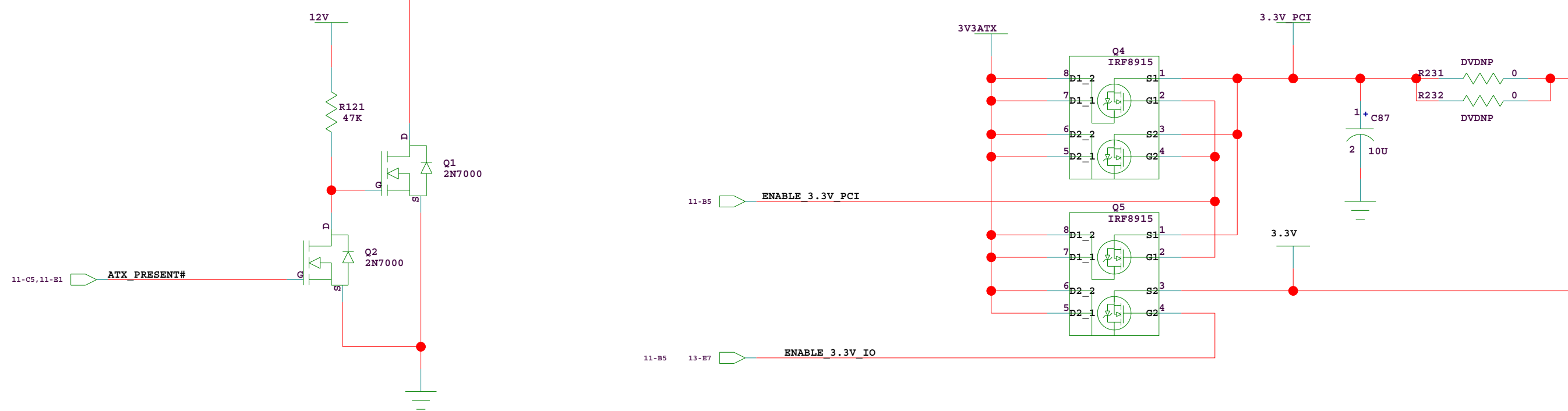


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TSI382A POWER/REGULATORS			
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PCI POWER REGULATORS



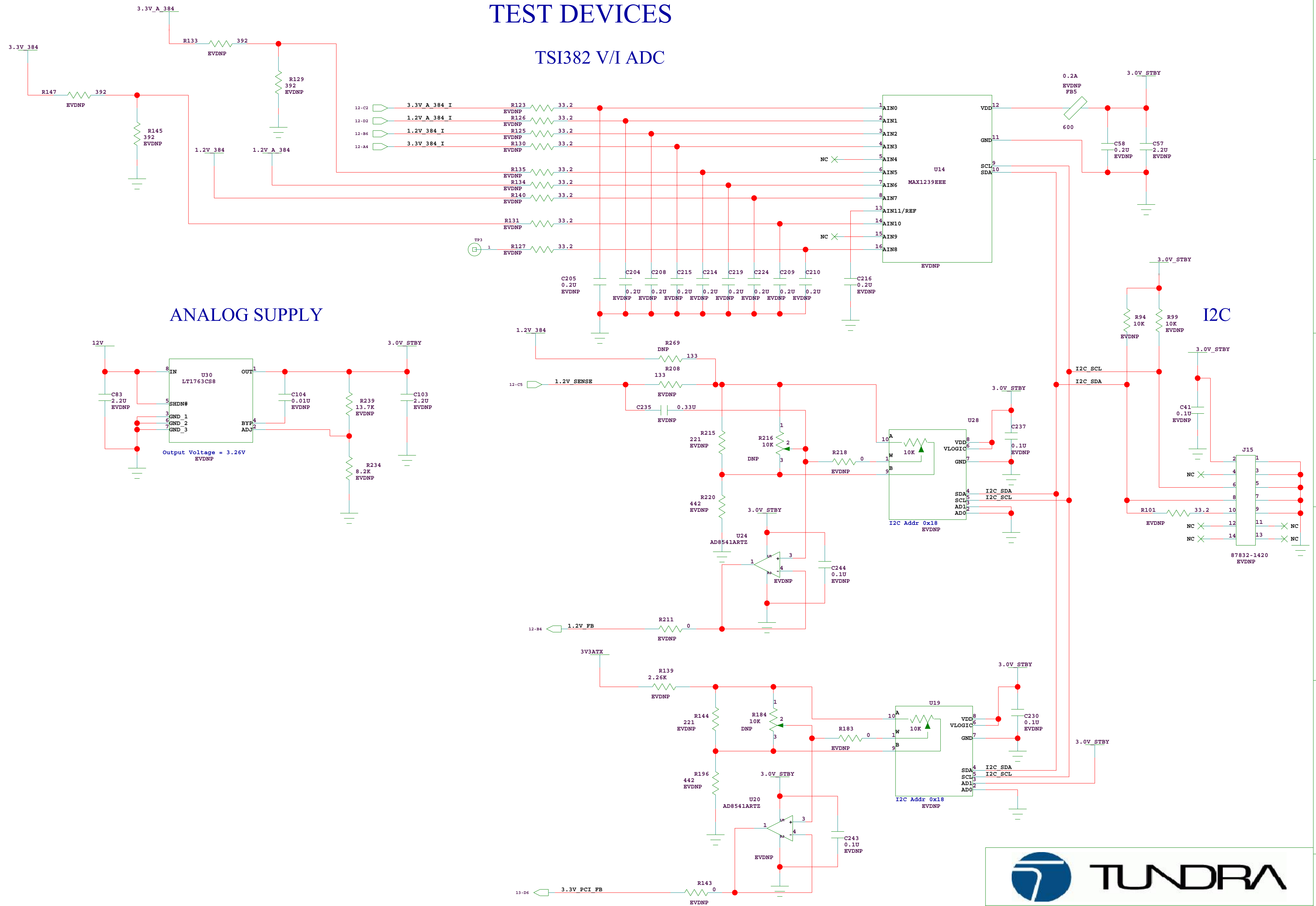
3.3V SWITCHES



TITLE : PCI POWER REGULATORS			
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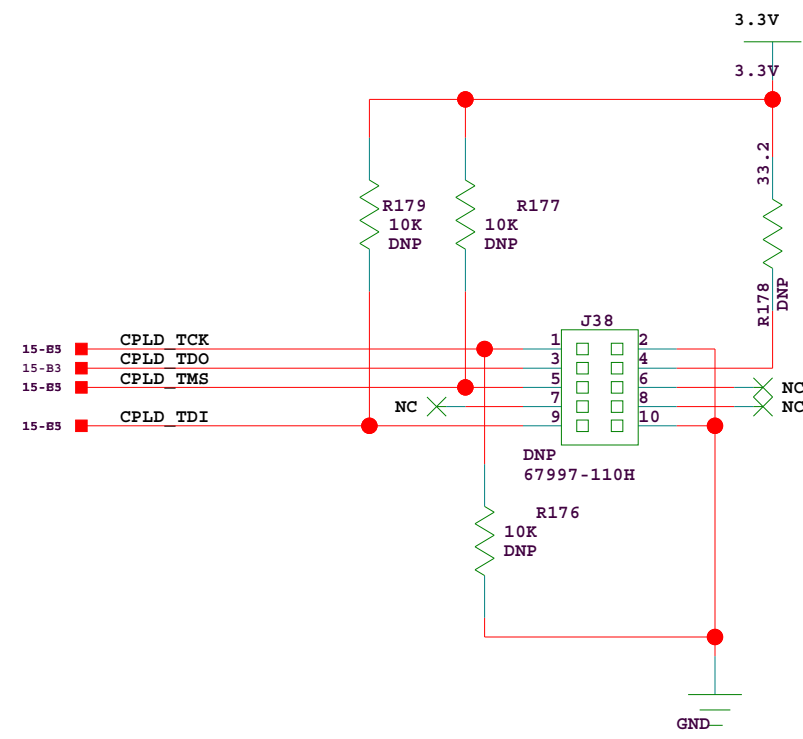
TEST DEVICES

TSI382 V/I ADC

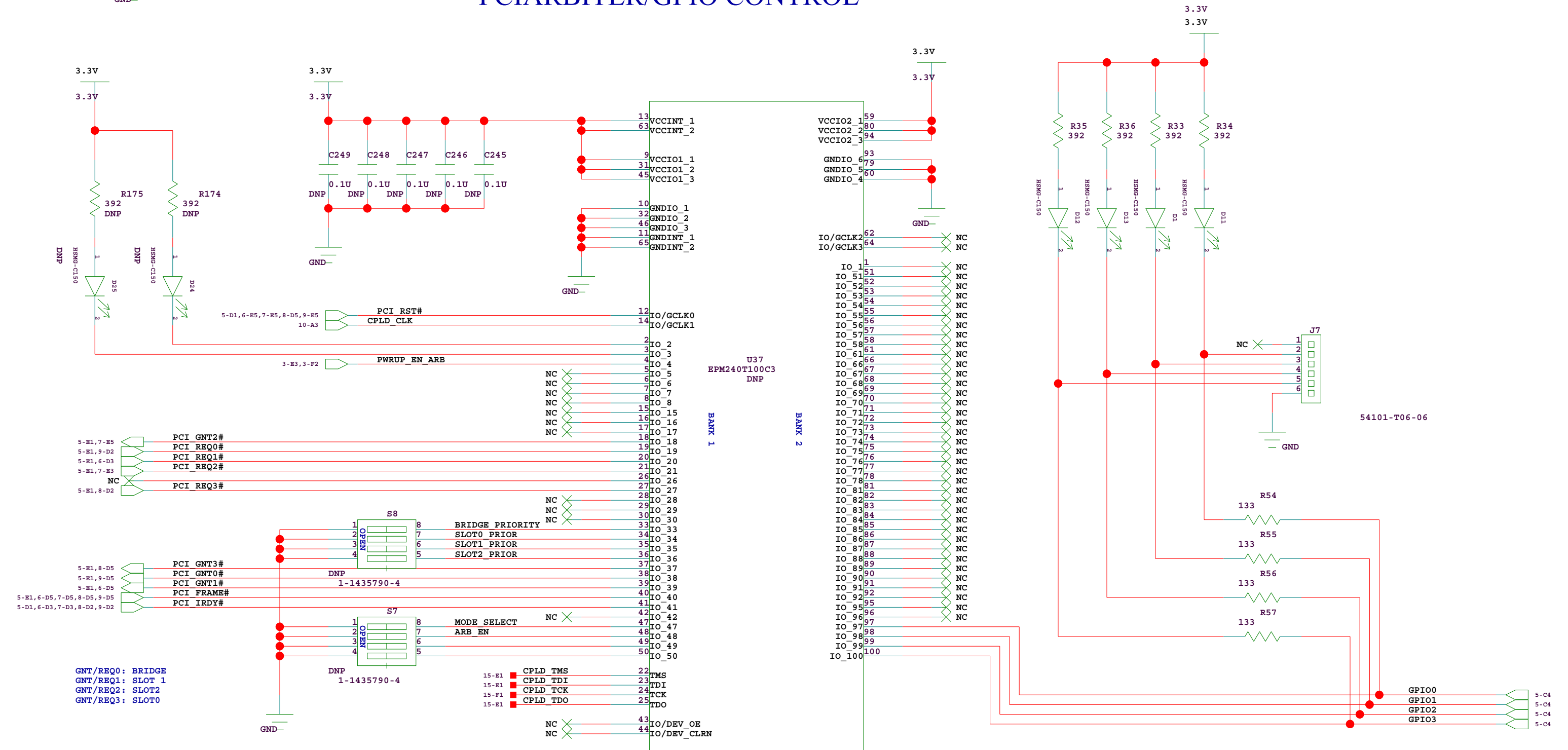


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TEST DEVICES			
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TEST DEVICES CONT'D



PCIARBITER/GPIO CONTROL



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TEST DEVICES CONT'D			
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