



ESD protection diodes are recommended for voltage levels over 20kV.
E.g. using 150pF / 3300hm ESD gun for test

Place close to IPS pins

Place close to IPS pins

Place close to Header pins

$$f_{TX} = \frac{1}{2\pi\sqrt{L_{TX}C_{TX}}} \rightarrow C_{TX} = \frac{1}{(2\pi f_{TX})^2 L_{TX}}$$

C_ADJ = To be adjusted according to coil inductivity. Calculate C_TX with the given formula.
C_ADJ = 2 x C_TX
It's recommended to use C0G or NP0 ceramic capacitors
Use dielectric strength Vr >= 50V capacitors for C7 + (C10 .. C22)

LEGEND
 Component Size 0603
 Component Size 0402
 Net Name
 Component Placement Suggestion
 Optional Component

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