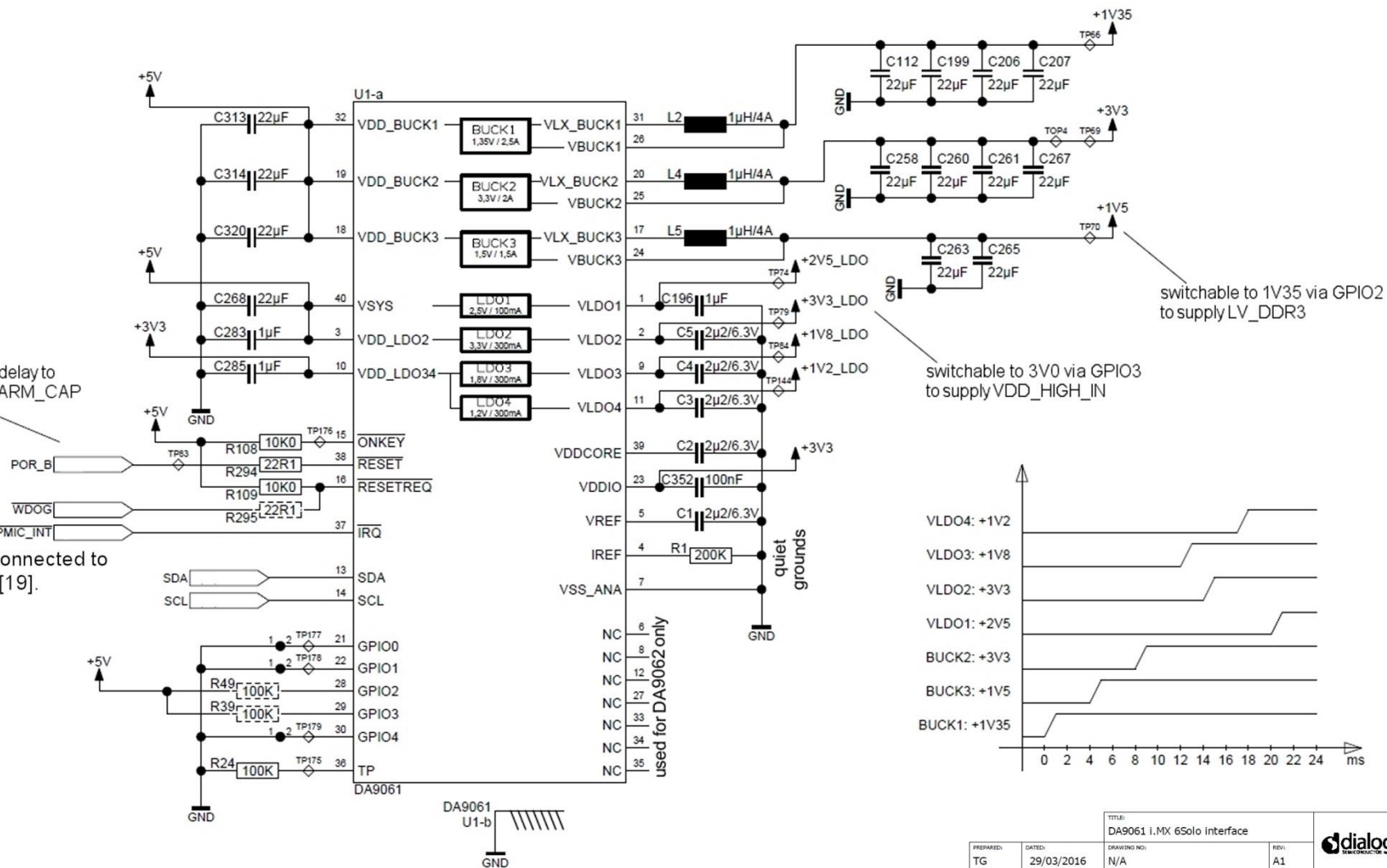
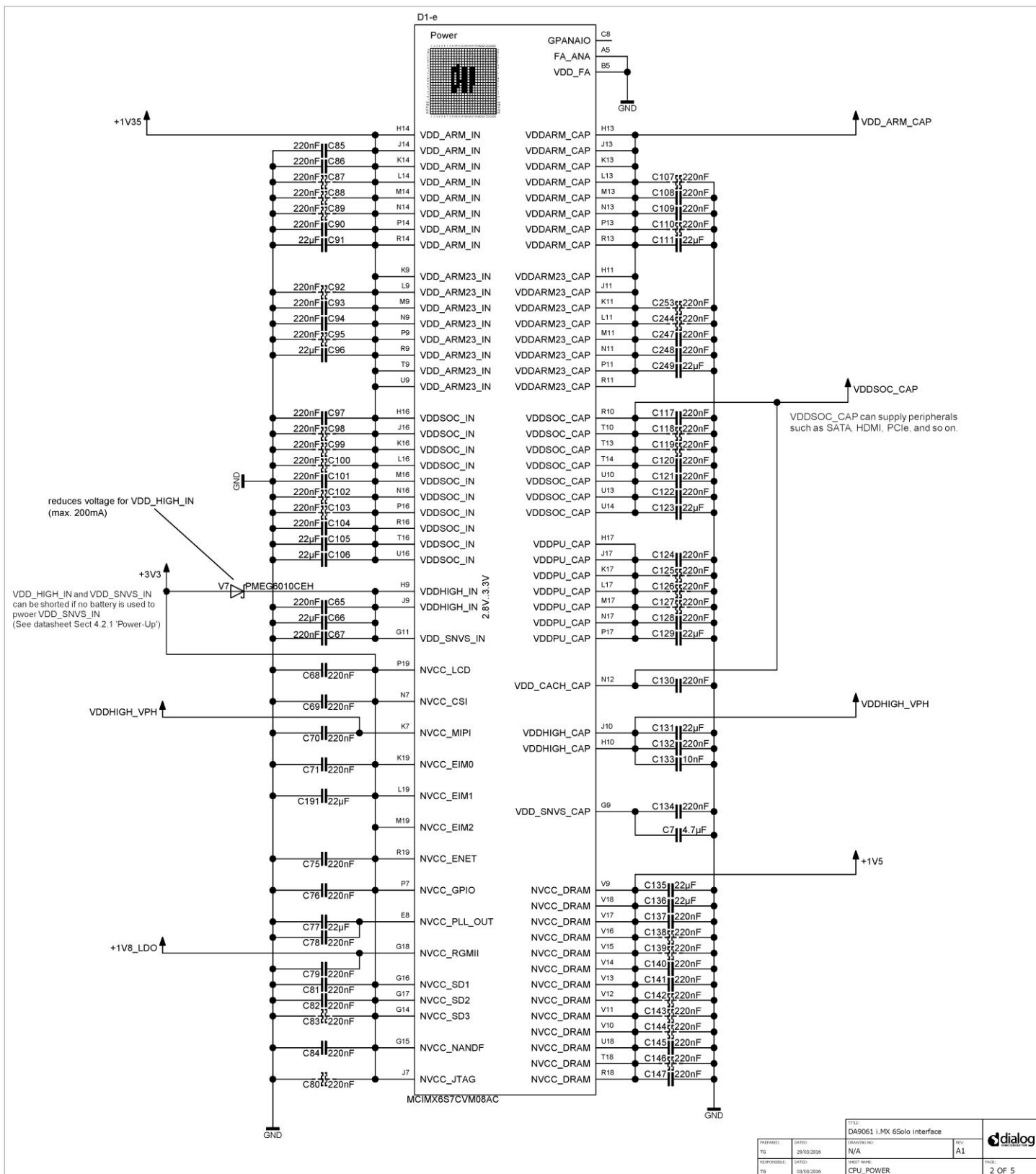


nRESET (open drain) has a 100 ms delay to ensure VDD\_SOC\_CAP and VDD\_ARM\_CAP are stable

nPMIC\_INT connected to i.MX 6S pin A[19].



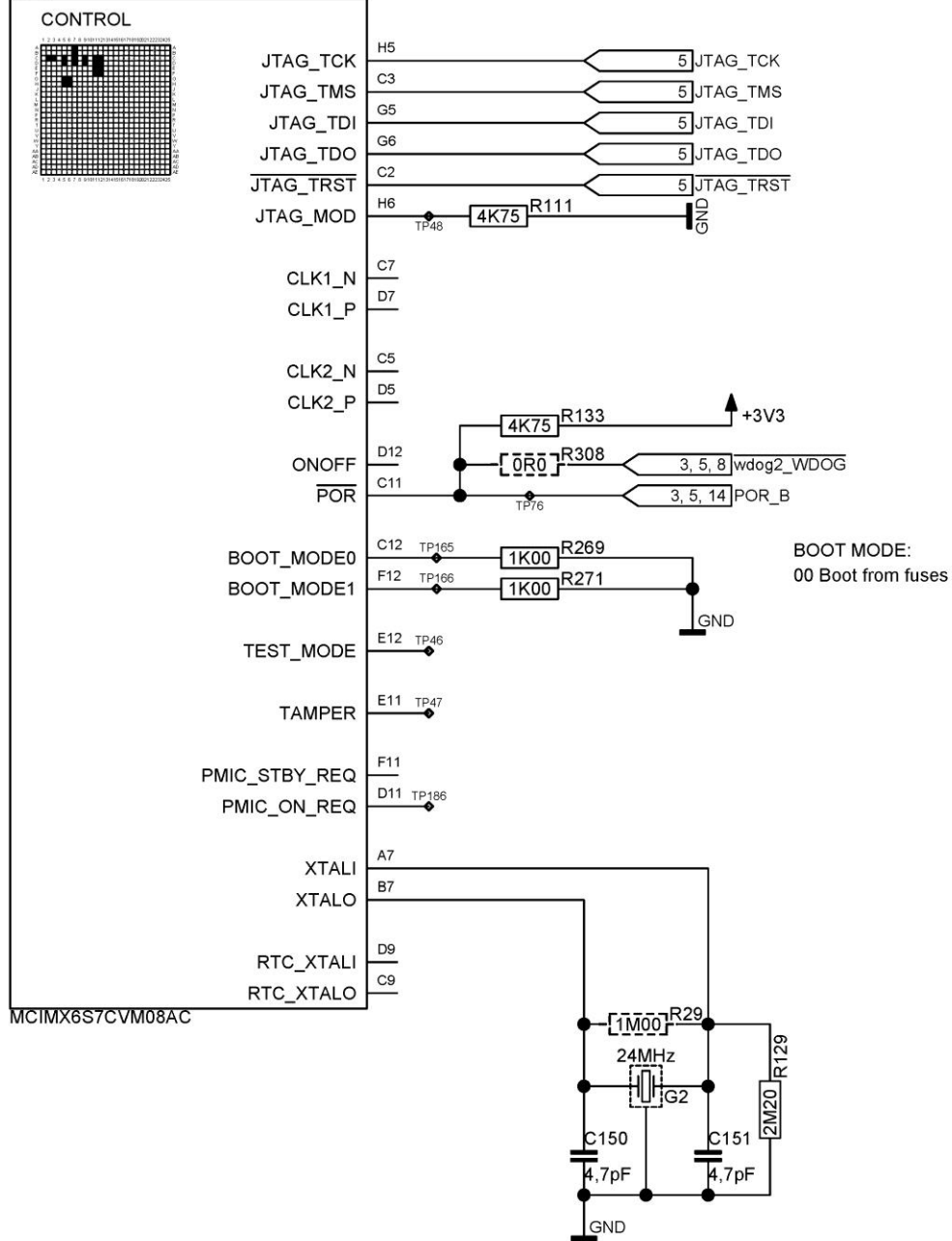
|              |            |                             |        |
|--------------|------------|-----------------------------|--------|
| TITLE:       |            | DA9061 i.MX 6Solo Interface |        |
| PREPARED:    | DATED:     | DRAWING NO:                 | REV:   |
| TG           | 29/03/2016 | N/A                         | A1     |
| RESPONSIBLE: | DATED:     | SHEET NAME:                 | PAGE:  |
| TG           | 03/03/2016 | DA9061                      | 1 OF 5 |



|              |  |            |  |             |  |        |  |
|--------------|--|------------|--|-------------|--|--------|--|
| PREPARED:    |  | DATE:      |  | DRAWING NO: |  | REV:   |  |
| TG           |  | 26/03/2016 |  | N/A         |  | A1     |  |
| RESPONSIBLE: |  | DATE:      |  | PART NAME:  |  | PAGE:  |  |
| TG           |  | 03/03/2016 |  | CPU_POWER   |  | 2 OF 5 |  |

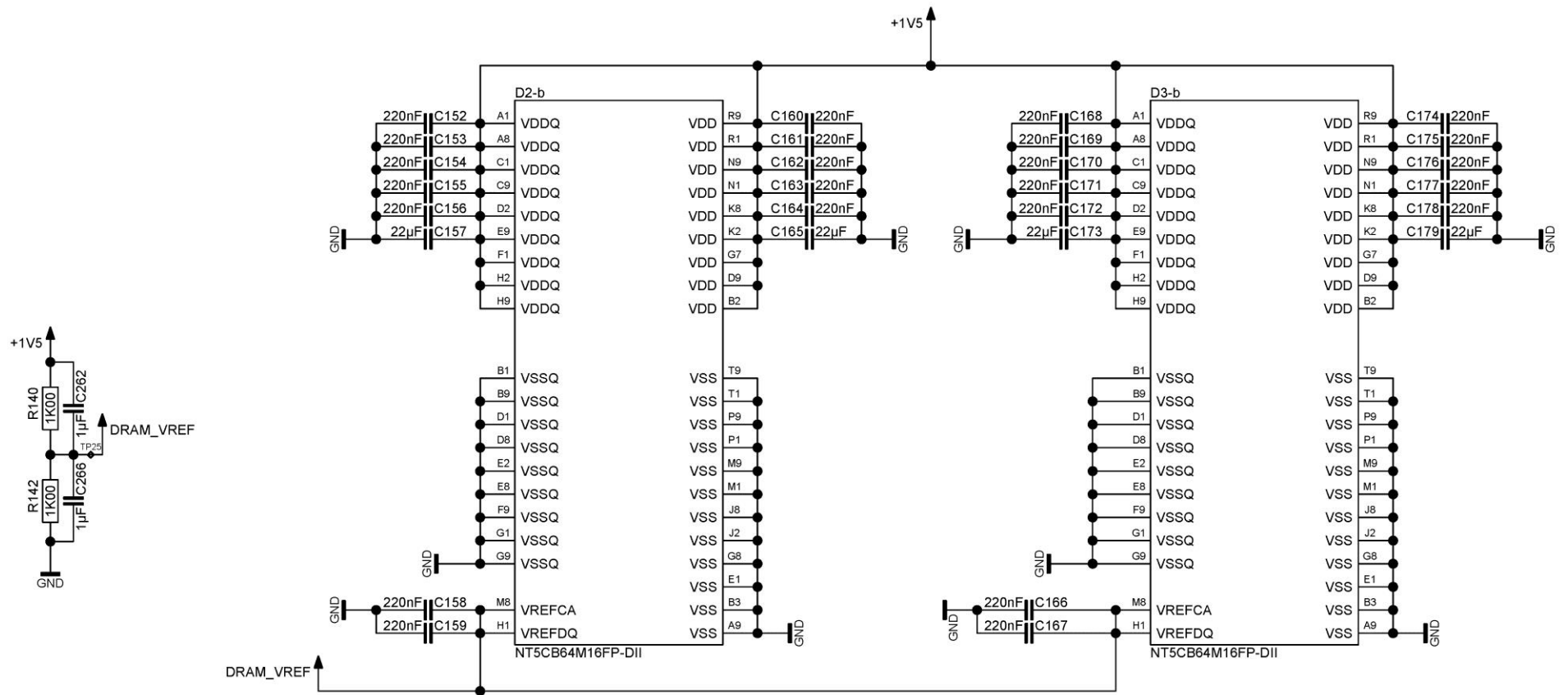


D1-g



|              |            |                             |        |
|--------------|------------|-----------------------------|--------|
| TITLE:       |            | DA9061 i.MX 6Solo interface |        |
| PREPARED:    | DATED:     | DRAWING NO:                 | REV:   |
| TG           | 29/03/2016 | N/A                         | A1     |
| RESPONSIBLE: | DATED:     | SHEET NAME:                 | PAGE:  |
| TG           | 03/03/2016 | CPU_CONTROLS                | 3 OF 5 |





|              |            |                             |        |
|--------------|------------|-----------------------------|--------|
| TITLE:       |            | DA9061 i.MX 6Solo interface |        |
| PREPARED:    | DATE:      | DRAWING NO:                 | REV:   |
| TG           | 29/03/2016 | N/A                         | A1     |
| RESPONSIBLE: | DATE:      | SHEET NAME:                 | PAGE:  |
| TG           | 03/03/2016 | DRAM_POWER                  | 4 OF 5 |



| REV | DATE       | ENGINEER | NOTES           |
|-----|------------|----------|-----------------|
| A1  | 29/03/2016 | TG       | First revision. |

SCIENTIFIC TEMPLATE REV F42 (November 30, 2011) | DIALOG DOCUMENT NUMBER: 31-403-03

|                    |                      |                                       |                 |
|--------------------|----------------------|---------------------------------------|-----------------|
|                    |                      | TITLE:<br>DA9061 i.MX 6Solo interface |                 |
| PREPARED:<br>TG    | DATED:<br>29/03/2016 | DRAWING NO:<br>N/A                    | REV:<br>A1      |
| RESPONSIBLE:<br>TG | DATED:<br>03/03/2016 | SHEET NAME:<br>REVISION/MECHANICAL    | PAGE:<br>5 OF 5 |

