

e² studio V7.4.0 Linux Host Public Beta for RZ

Release Note

Introduction

This document outlines the supported OS and device support in e² studio V7.4.0 for Linux Host.

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1. Product Information

1.1 Supported Operating Systems

These operating systems are officially supported by e² studio:

• Ubuntu 18.04 LTS 64-bit version

1.2 Product version

• 7.4.0 – (7.4.0.R20190325-1350)

1.3 Supported Toolchains

The following toolchains are supported in e² studio V7.4.0:

• Linaro GCC – tested version 7.3.1-201805



Product Information Device Support

1.4 Project Generator Support

Family	Group	Devices
		R7S721000, R7S721000_DualSPI, R7S721001, R7S721001_DualSPI,
		R7S721010, R7S721010_DualSPI, R7S721011, R7S721011_DualSPI,
		R7S721020, R7S721020_DualSPI, R7S721021, R7S721021_DualSPI,
		R7S721030, R7S721030_DualSPI, R7S721031, R7S721031_DualSPI,
	_A1	R7S721034, R7S721034_DualSPI
		R7S921040, R7S921041, R7S921042, R7S921043, R7S921045,
		R7S921046, R7S921047, R7S921048, R7S921051, R7S921052,
	A2	R7S921053, R7S921056, R7S921057, R7S921058
	G1C	R8A77470
		R8A77450
	G1E	R8A77450_Core1, (Debug Support Only)
RZ	G1H	R8A77420
112		R8A77430
	G1M	R8A77430 Core1, (Debug Support Only)
	G1N	R8A77440
	OIN	
		R8A774D, R8A774C, R8A7748, R8A774A, R8A774B
		R7S910001, R7S910002, R7S910006, R7S910007, R7S910011, R7S910013, R7S910015, R7S910015 M3, R7S910016, R7S910016 M3,
		R7S910015, R7S910015, R7S910015_MS, R7S910016, R7S910016_MS, R7S910017, R7S910017 M3, R7S910018, R7S910018 M3, R7S910025,
		R7S910017, R7S910017_MS, R7S910018, R7S910018_MS, R7S910023,
		R7S910026, R7S910027, R7S910028, R7S910035, R7S910036, R7S910101, R7S910102, R7S910106, R7S910107, R7S910111,
		R7S910101, R7S910102, R7S910100, R7S910107, R7S910111,
		R7S910113, R7S910113, R7S910113_M3, R7S910110, R7S910110_M3, R7S910117, R7S910117_M3, R7S910118, R7S910118_M3, R7S910125,
	T1	R7S910117, K73510117_M3, K73510118, K73510118_M3, K73510123,
	1 1	17.5510126, 17.5510127, 17.5510126, 17.5510135, 17.5510150

2. What is new in e² studio V7.4.0 Linux Host?

Component	Device	Description	
RZ debugging	RZ Family devices	Linux Host support is provided for e ² studio to support the RZ devic family.	
		This version has expanded device support and an updated Segger debugger.	
		The use cases allow the following cases:	
		Linux target OS debugging for RZ/G and RZ/A1.Segger J-link debugging for RZ on Linux Host	

2.1 Overview

The public beta version e² studio is based on e² studio V7.4.0 for windows. Therefore, documents of e² studio will be helpful for common usages. This part describes mainly how to install public beta version e² studio for Linux and set configurations of debug functions have dependencies of Linux environment. These operating systems are officially supported by e² studio:

2.2 How to install

Installation steps of public beta version e² studio for Linux

- A. Download archived file from download page. You can find the download page by visiting the e² studio product information web page (https://www.renesas.com/e2_studio) and then click the 'Download the installer' button. Once completed enter the text 'e² studio V7.4.0 for Linux, public beta edition' in the search text box and press "Search". Then you can select the download from the found items list.
- B. Extract the downloaded archived file (extension *.7z) into local storage.
- C. Please install JRE 1.8 (Java 8) 64bit version.
 Ubuntu install command example (The internet connection is required.):
 sudo apt-get install openjdk-8-jre

Error Message in below will appear, if JRE (Java Runtime Environment) is not installed and try to run e² studio.

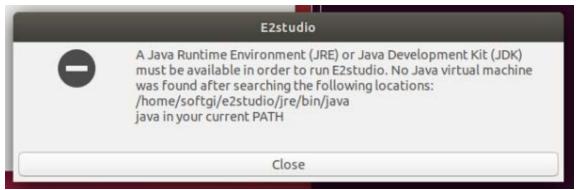


Figure 1. Error Message: No installed JRE or JDK

2.3 How to run

- A. Administrator privilege is required for debugging or some other features which demand file access authentication.
- B. Run 'terminal' application of Linux.
- C. Move installed directory and Run 'e² studio' binary file.

 Example of executing command with administrator privilege at installed directory: sudo ./e2studio

2.4 Register toolchain to e² studio

- A. Download and extract a toolchain package file to arbitrary directory.
- B. Run 'e2 studio' and select 'Help Add Renesas Toolchains'
- C. Select 'Toolchain Type' and 'Add' Location of toolchain.

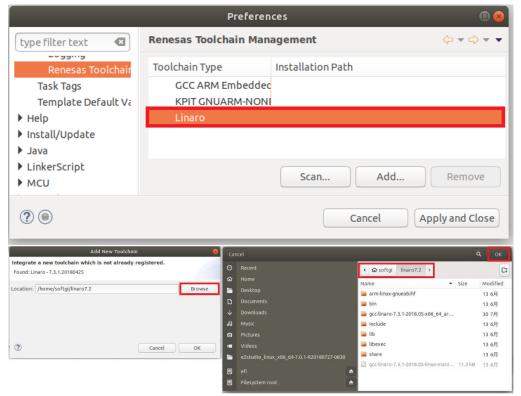


Figure 2. Register Toolchain: Browse toolchain location

D. Click checkbox of added toolchain and restart e² studio.

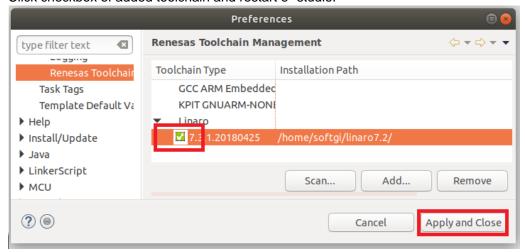


Figure 3. Register Toolchain: ex) Linaro

2.5 How to build and debug Linux application Overview

Public beta version e² studio for Linux supports building and debugging Linux applications for devices of RZ/A Group and RZ/G Group. For debugging by GDB (the GNU Project Debugger), please add Linux programs gdb-server program to Linux file system of devices and run as background process automatically. (ssh-server, tcf-agent will be needed for connection between host system and target device.) For detail about building Linux image for RZ family devices, refer to embedded Linux wiki pages (https://elinux.org) or Renesas Rulz web pages about RZ family (https://renesasrulz.com/rz). Descriptions in below is based on RZ/A1H case.

2.5.1 How to add gdb-server to RZ/A Linux root file system

- A. Build root file system of RZ/A1 Linux-4.9 BSP. (path example: ~/rza_linux-4.9_bsp/, command example: ./build.sh buildroot)
- B. Move to 'buildroot-***' directory in 'output'. (path example: ~/rza_linux-4.9_bsp/output/buildroot-2017.02)
- C. Run menuconfig (make menuconfig) and add gdb-server. (Select 'Toolchain—Copy gdb server to the Target' menu)

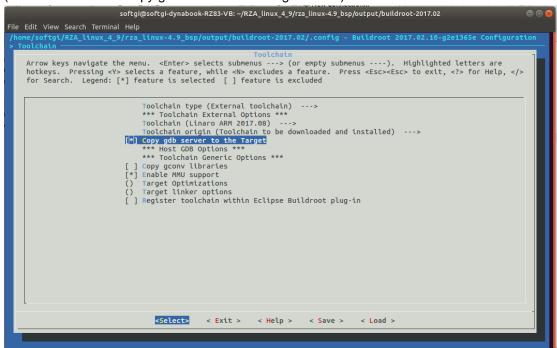


Figure 4. Menuconfig: set 'copy gdb server to the target'

- D. Move to 'target' directory in 'output' of 'buildroot-****'.

 (path example: ~/rza_linux-4.9_bsp/output/buildroot-2017.02/output/target)
- E. Add new file with a line as command at '/etc/init.d' directory

```
File name: S51gdbserver

Command: /usr/bin/gdbserver --multi --remote-debug /dev/ttySC0
```

F. Delete or disable below contents from etc/inittab.

```
# Put a getty on the serial port
# ttySC0::respawn:/sbin/getty -L ttySC0 115200 vt100 # GENERIC_SERIAL
```

G. Move 'Linux-4.9 BSP root' (path example: ~/rza_linux-4.9_bsp/) and build root file system again. Download root file system at target device.

button.

2.5.2 Linux C/C++ Project generation and build

- A. Connect target device which is run as Linux, via Serial port.
- B. Select 'File New RZ Linux C/C++ project' menu and make new RZ/A1H Linux C/C++ project. In phase of 'RZ Linux connection settings', the serial port which is used for connecting target device, will be selected automatically.

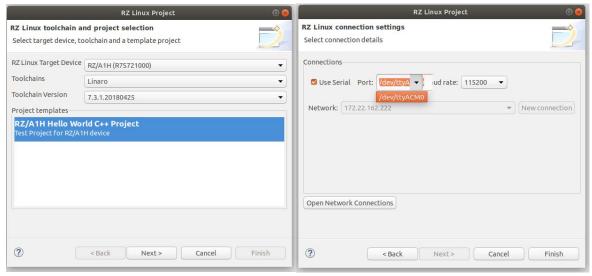


Figure 5. New RZ Linux project & connection setting: Serial port

C. After editing codes, build by selecting 'Build Project' in right-click menu or push

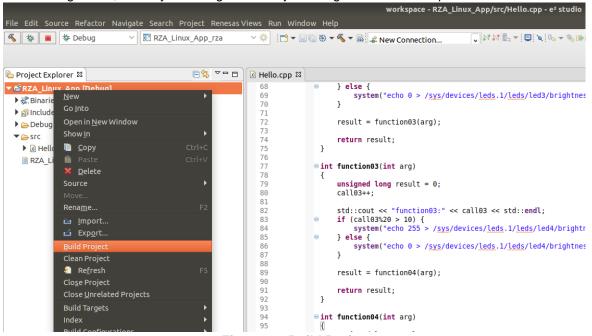


Figure 6. Build Project

2.5.3 GDB debug by using serial port communication

- A. Terminate all processes use serial port communication such as Minicom.
- B. Open 'Configuration' and check 'Serial' is selected as 'Connection'.

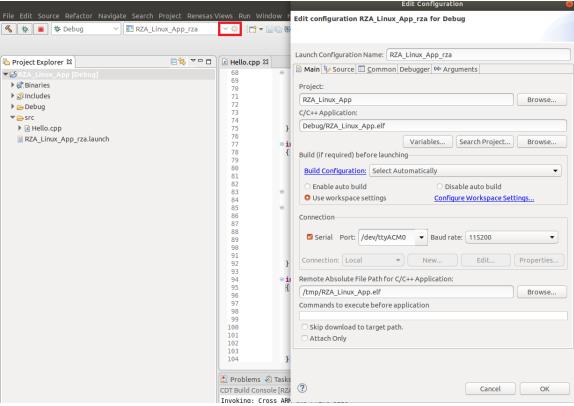


Figure 7. Connection configuration: Serial

C. Run debug by push button . It takes 10 or more seconds for transferring binary files to target device. Pop up message for switching to debug perspective will be shown after transferring binary files.

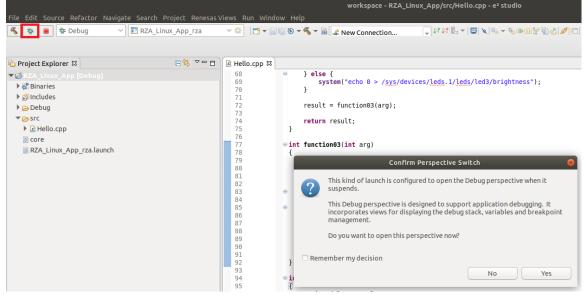


Figure 8. Debug: Perspective Switch

D. 'Debug Perspective' provide ways for flow controls and configurations. This public beta version e² studio for Linux doesn't have console view for showing result of the program.
 (Under development) For more detail, please see user manuals of e² studio Windows edition.

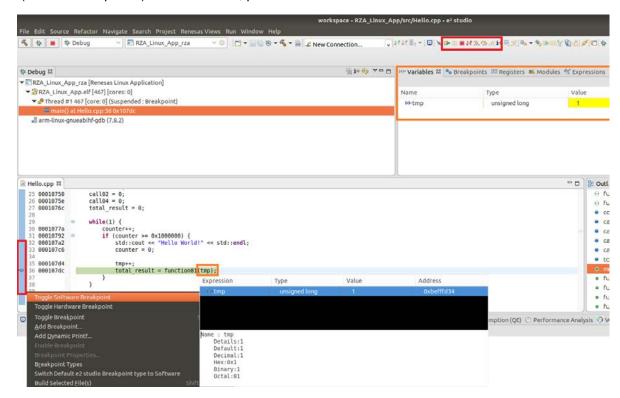


Figure 9. Debug: Control buttons, views, setting break point

3. Appendix

3.1 Website and Support

Renesas Electronics Website http://www.renesas.com/

Inquiries

http://www.renesas.com/contact/





Revision History

		Descriptio	n
Rev.	Date	Page	Summary
1.00	2019. 06. 05.	-	First edition

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

- 6. Voltage application waveform at input pin
 - Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).
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