

## TOYAMA MURATA MANUFACTURING CO., LTD.

Product Engineering Service Section VI
Piezoelectric Components Department I
Piezoelectric Components Division
Device Business Unit

| Approved by | Checked by | Issued by | Issued Date | Data No. |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |
| K. Maruno |  |  |  |  |
| K.Maruno | R.Miyamae | T.Morita | Nov 22, 2005 | TCD-05-1678 |

## Contents

1. Test Circuit 1
2. Temperature Characteristics of Oscillating Frequency, Oscillating Voltage 2
3. Rise Time, Oscillating Frequency, Oscillating Voltage vs Vcc Characteristics 3

Note : Rise Time
"Rise time" is defined as the time when oscillation voltage reaches $90 \%$ of full voltage swing after Vdd(Vset) is supplied.


In the case that rising time of $\operatorname{Vdd}(V \operatorname{set})$ is slow comparing to resonator's rise time due to the bypass capacitor, resonator's rise time is also slow because it depends on rising time of Vdd(Vset).

Also, in the case that the time supplying voltage to the oscillator circuit takes a certain time by reset time etc after $\operatorname{Vdd}(V$ set) is applied, resonator's rise time is also slow.

In these case, we will describe "Unable to measure" in rise time data, because we can not measure resonator's rise time correctly.

## Test Circuit



| SEL | RESET <br> 8pin | P1_7/TRAIO/ㅅNT1 <br> 21pin | P4_5/INTO <br> 27pin |
| :---: | :---: | :---: | :---: |
| SW | $\mathrm{L} \rightarrow \mathrm{H}$ | H | $\mathrm{H} \rightarrow \mathrm{L}$ |

Xin: 11
Xout: 9
H : 5, 12
L : 10

Recommended Value
CERALOCK ${ }^{\circledR}$ : CSTLS8M00G56-B0
$\mathrm{Vcc}=2.6$ to $5.5[\mathrm{~V}]$
C1 $=47[\mathrm{pF}]$ (Typ.)
C2 $=47$ [pF] (Typ.)
$\mathrm{Ta}=-40$ to $85\left[{ }^{\circ} \mathrm{C}\right]$




Oscillating Voltage vs Vcc Characteristics
MODEL : CSTLS8M00G56-B0 with R5F21256SNFP(High)


## Appendixes

4. Comparison Table

## Comparison Table

| IC : No | V1H [V] | V1L [V] | V1p-p [V] | V2H [V] | V2L [V] | V2p-p [V] Fosc [kHz] | Trise [ms] | Vstart [V] |  |
| :--- | ---: | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  |  |  |  |  |  |  |  |  |
| WS | 5.41 | -0.47 | 5.88 | 5.34 | -0.31 | 5.65 | 7995.949 | Unable to | 1.62 |
| LL | 5.35 | -0.47 | 5.82 | 5.34 | -0.24 | 5.58 | 7995.728 | Measure | 1.49 |
| LH | 5.35 | -0.47 | 5.82 | 5.38 | -0.24 | 5.62 | 7995.321 | 1.61 |  |
| HH | 5.41 | -0.47 | 5.88 | 5.34 | -0.31 | 5.65 | 7995.145 | 1.63 |  |
| HL | 5.35 | -0.47 | 5.82 | 5.28 | -0.31 | 5.59 | 7995.763 | 1.64 |  |

Ref.
Performance described page 2 to 3 were measured with IC No. WS

## Frequency Correlation Data

| Sample | R5F21256SNFP(High) | TC74HCU04 |  |
| ---: | ---: | ---: | ---: |
| No. | Fosc $[k H z]$ | Fosc $[k H z]$ | Shift [\%] |
|  |  |  |  |
| 1 | 7999.709 | 7987.452 | 0.1535 |
| 2 | 8023.405 | 8009.837 | 0.1694 |
| 3 | 8005.207 | 7993.276 | 0.1493 |
| 4 | 7992.524 | 7980.035 | 0.1565 |
| 5 | 8024.336 | 8010.346 | 0.1746 |
| $\bar{X}$ | 8009.036 | 7996.189 | 0.1607 |

## muRata Standard Circuit



CERALOCK $\begin{aligned} & : \text { CSTLS8M00G56-B0 } \\ \mathrm{C} 1 & =47[\mathrm{pF}] \\ \mathrm{C} 2 & =47[\mathrm{pF}] \\ \mathrm{Rf} & =1[\mathrm{Mohm}] \\ \mathrm{Rd} & =680[\mathrm{ohm}]\end{aligned}$

