Technical Data of Ceramic Resonator

MURATA Part No.: CSTLS8M00G56-B0

Applied to R5F21256SNFP(High)

# TOYAMA MURATA MANUFACTURING CO., LTD.

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#### Note : Rise Time

"Rise time" is defined as the time when oscillation voltage reaches 90% of full voltage swing after Vdd(Vset) is supplied.



In the case that rising time of Vdd(Vset) is slow comparing to resonator's rise time due to the bypass capacitor, resonator's rise time is also slow because it depends on rising time of Vdd(Vset).

Also, in the case that the time supplying voltage to the oscillator circuit takes a certain time by reset time etc after Vdd(Vset) is applied, resonator's rise time is also slow.

In these case, we will describe "Unable to measure" in rise time data, because we can not measure resonator's rise time correctly.

## Test Circuit



SEL	RESET	P1_7/TRAIO/INT1	P4_5/INTO
	8pin	21pin	27pin
SW	L→H	H	H→L

Xin	:	11	
Xout	:	9	
Н	:	5,	12
L		10	)

Recommended Value





2



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Appendixes

4. Comparison Table

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## Comparison Table

IC : No	V1H [V]	V1L [V]	V1p-p [V]	V2H [V]	V2L [V]	V2p-p [V]	Fosc [kHz]	Trise [ms]	Vstart [V]
WS	5.41	-0.47	5.88	5.34	-0.31	5.65	7995.949	Unable to	1.62
LL	5.35	-0.47	5.82	5.34	-0.24	5.58	7995.728	Measure	1.49
LH	5.35	-0.47	5.82	5.38	-0.24	5.62	7995.321		1.61
НН	5.41	-0.47	5.88	5.34	-0.31	5.65	7995.145		1.63
HL	5.35	-0.47	5.82	5.28	-0.31	5.59	7995.763		1.64

Ref.

Performance described page 2 to 3 were measured with IC No. WS

### **Frequency Correlation Data**

Sample	R5F21256SNFP(High)	TC74HCU04	
No.	Fosc [kHz]	Fosc [kHz]	Shift [%]
1	7999.709	7987.452	0.1535
2	8023.405	8009.837	0.1694
3	8005.207	7993.276	0.1493
4	7992.524	7980.035	0.1565
5	8024.336	8010.346	0.1746
x	8009.036	7996.189	0.1607

muRata Standard Circuit



CERALOCK<sup>®</sup> : CSTLS8M00G56-B0 C1 = 47 [pF]

C2 = 47 [pF] Rf = 1 [Mohm]

Rd = 680 [ohm]