Technical Data of Ceramic Resonator

MURATA Part No.: CSTLS6M00G56-B0

Applied to R5F21256SNFP(High)

TOYAMA MURATA MANUFACTURING CO., LTD.

Product Engineering Service Section VI
Piezoelectric Components Department I
Piezoelectric Components Division
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Approved by	Checked by	Issued by	Issued Date	Data No.
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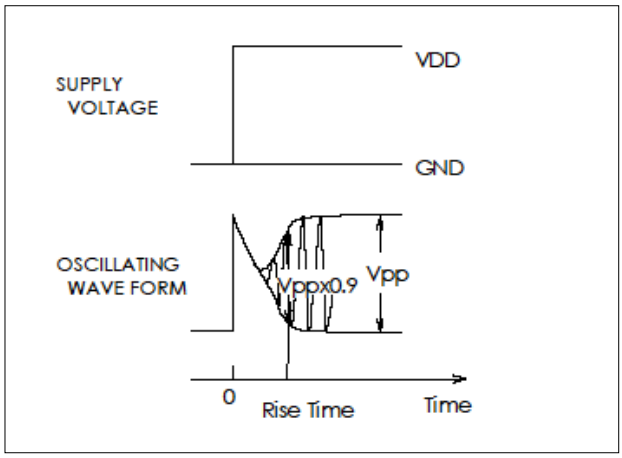
Contents

Note : Rise Time

"Rise time" is defined as the time when oscillation voltage reaches 90% of full voltage swing after Vdd(Vset) is supplied.

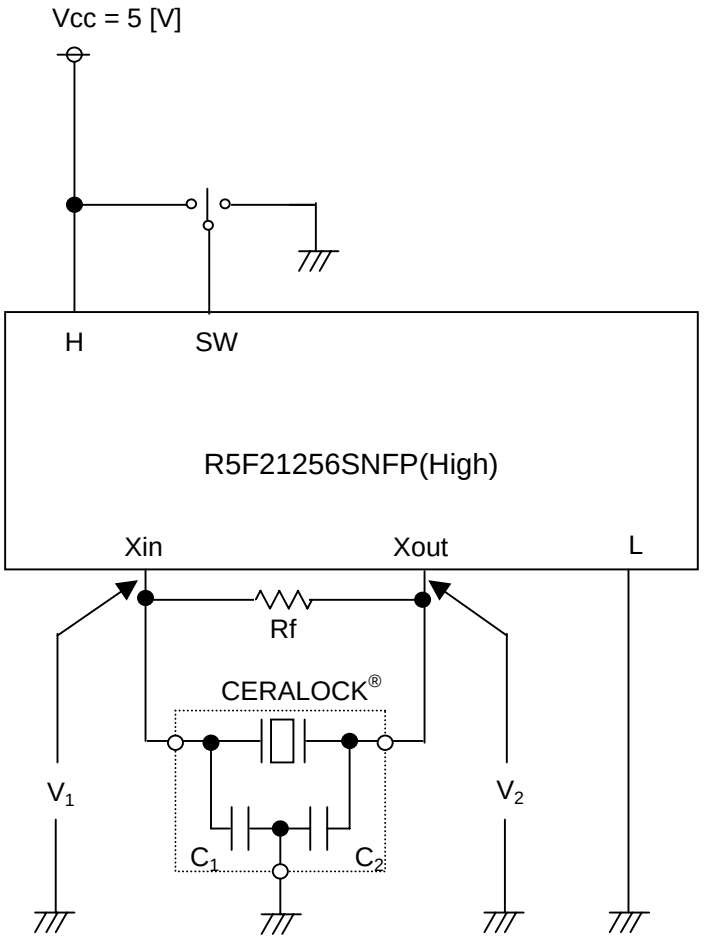


In the case that rising time of Vdd(Vset) is slow comparing to resonator's rise time due to the bypass capacitor, resonator's rise time is also slow because it depends on rising time of Vdd(Vset).

Also, in the case that the time supplying voltage to the oscillator circuit takes a certain time by reset time etc after Vdd(Vset) is applied, resonator's rise time is also slow.

In these case, we will describe "Unable to measure" in rise time data, because we can not measure resonator's rise time correctly.

Test Circuit



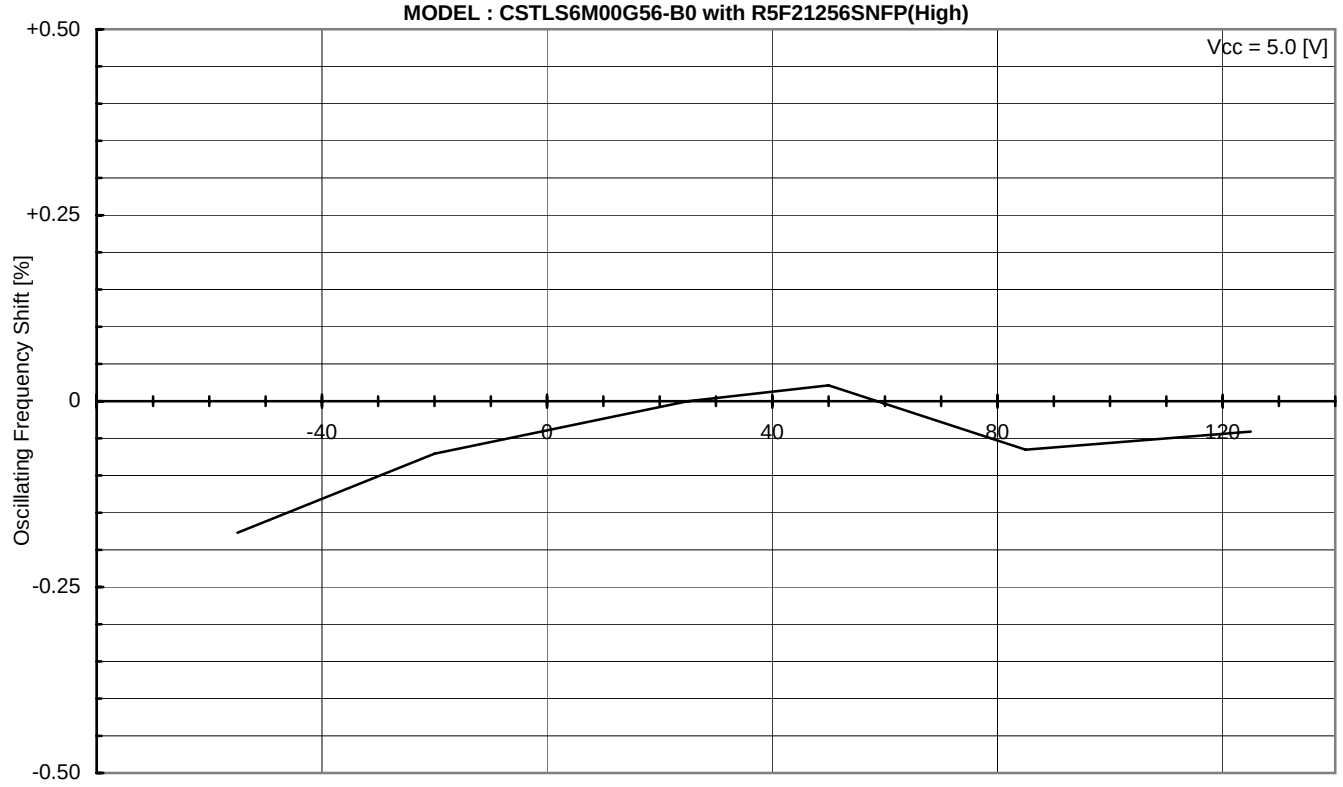
SEL	RESET 8pin	P1_7/TRAIO/INT1 21pin	P4_5/INTO 27pin
SW	L→H	H	H→L

Xin : 11
Xout: 9
H : 5, 12
L : 10

Recommended Value
CERALOCK® : CSTLS6M00G56-B0
Vcc = 2.2 to 5.5 [V]
C1 = 47 [pF] (Typ.)
C2 = 47 [pF] (Typ.)
Rf = 1 [Mohm]
Ta = -40 to 85 [°C]

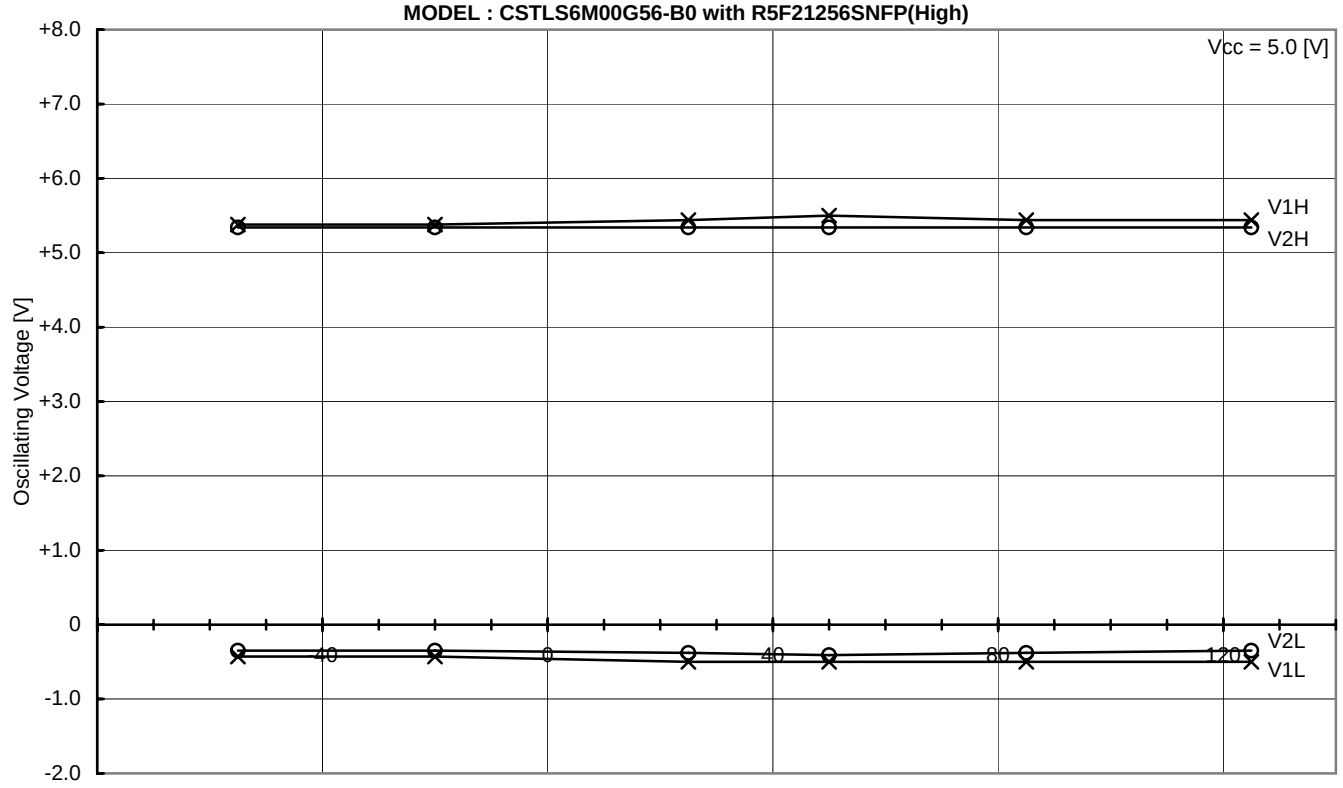
Temperature Characteristics of Oscillating Frequency

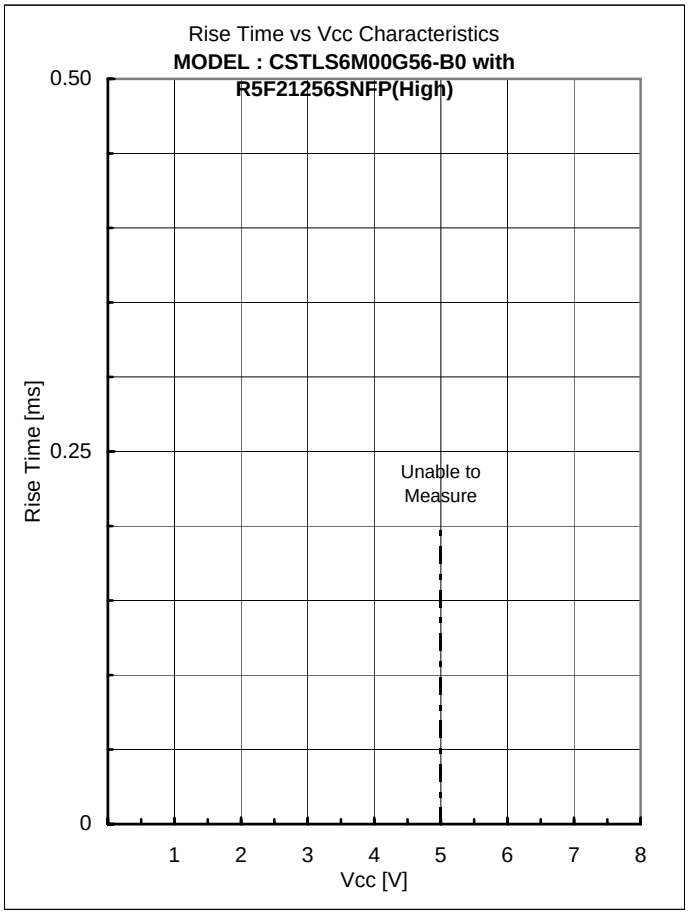
MODEL : CSTLS6M00G56-B0 with R5F21256SNFP(High)

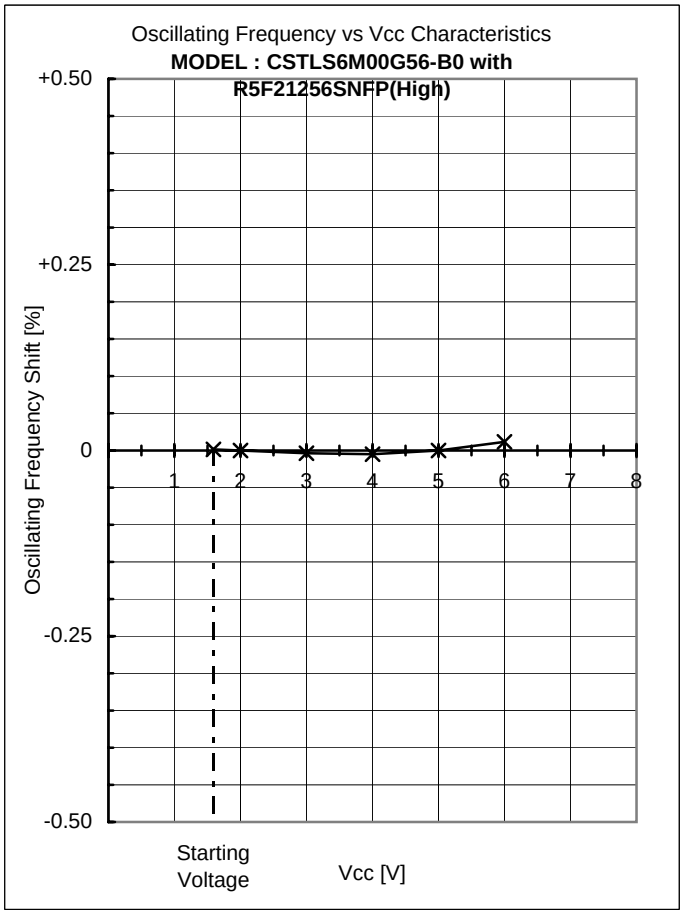


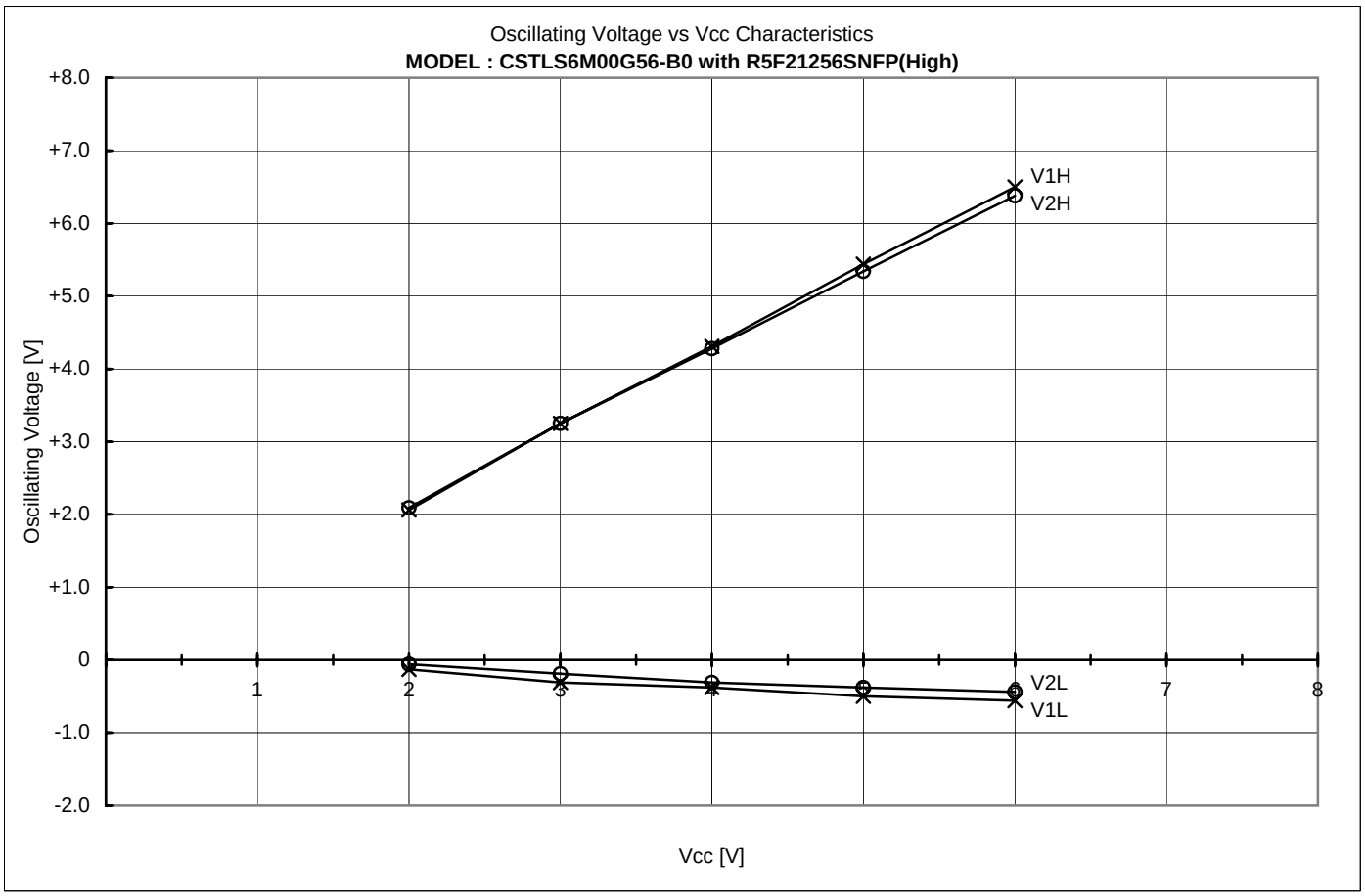
Temperature Characteristics of Oscillating Voltage

MODEL : CSTLS6M00G56-B0 with R5F21256SNFP(High)




Rise Time vs Vcc Characteristics
MODEL : CSTLS6M00G56-B0 with
R5F21256SNFP(High)
0.50
0.25
0
Rise Time [ms]
Unable to
Measure
1
2
3
4
5
6
7
8
Vcc [V]


Oscillating Frequency vs Vcc Characteristics
MODEL : CSTLS6M00G56-B0 with
R5F21256SNFP(High)
+0.50
+0.25
0
-0.25
-0.50
Oscillating Frequency Shift [%]
1
2
3
4
5
6
7
8
Starting
Voltage
Vcc [V]


Oscillating Voltage vs Vcc Characteristics
MODEL : CSTLS6M00G56-B0 with R5F21256SNFP(High)
+8.0
+7.0
+6.0
+5.0
+4.0
+3.0
+2.0
+1.0
0
-1.0
-2.0
Oscillating Voltage [V]
V1H
V2H
V2L
V1L
1
2
3
7
8
Vcc [V]

Appendixes

Comparison Table

IC : No	V1H [V]	V1L [V]	V1p-p [V]	V2H [V]	V2L [V]	V2p-p [V]	Fosc [kHz]	Trise [ms]	Vstart [V]
WS	5.44	-0.50	5.94	5.34	-0.38	5.72	5991.443	Unable to	1.59
LL	5.40	-0.44	5.84	5.28	-0.44	5.72	5992.625	Measure	1.58
LH	5.44	-0.50	5.94	5.41	-0.31	5.72	5991.974		1.58
HH	5.44	-0.50	5.94	5.34	-0.38	5.72	5991.803		1.56
HL	5.44	-0.44	5.88	5.28	-0.44	5.72	5991.888		1.57

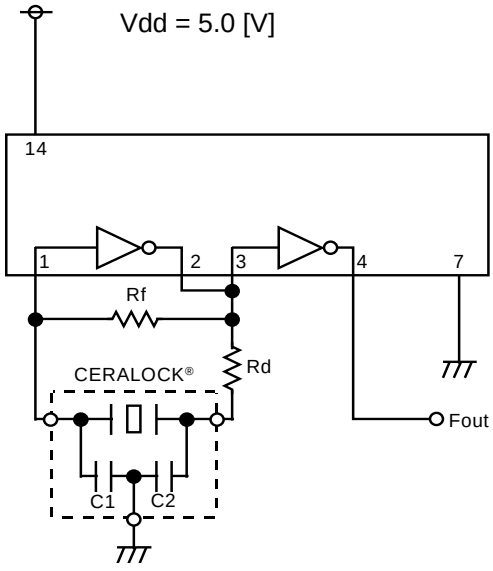
Ref.

Performance described page 2 to 3 were measured with IC No. WS

Frequency Correlation Data

Sample No.	R5F21256SNFP(High) Fosc [kHz]	TC74HCU04 Fosc [kHz]	Shift [%]
1	5993.600	5987.545	0.1011
2	5992.423	5986.293	0.1024
3	5992.415	5987.021	0.0901
4	5993.546	5987.407	0.1025
5	5992.365	5987.276	0.0850
$\bar{X}$	5992.870	5987.108	0.0962

muRata Standard Circuit



CERALOCK® : CSTLS6M00G56-B0
C1 = 47 [pF]
C2 = 47 [pF]
Rf = 1 [Mohm]
Rd = 680 [ohm]