




Technical Data of Ceramic Resonator

MURATA Part No.: CSTCE8M00G55-R0

Applied to R5F21194SP(High)

***TOYAMA MURATA MANUFACTURING CO., LTD.***

Product Engineering Service Section VI  
Piezoelectric Components Department I  
Piezoelectric Components Division  
Device Business Unit

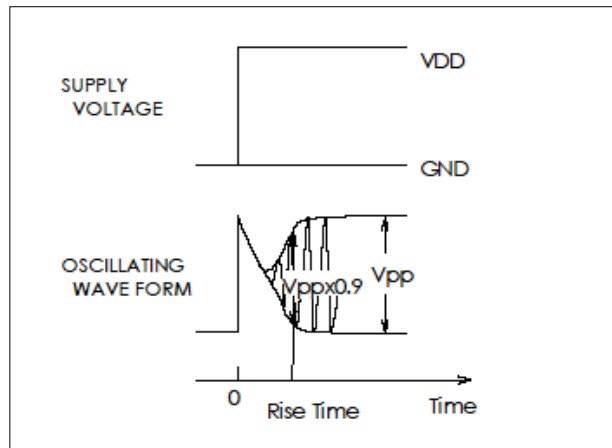
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### Note : Rise Time

"Rise time" is defined as the time when oscillation voltage reaches 90% of full voltage swing after Vdd(Vset) is supplied.



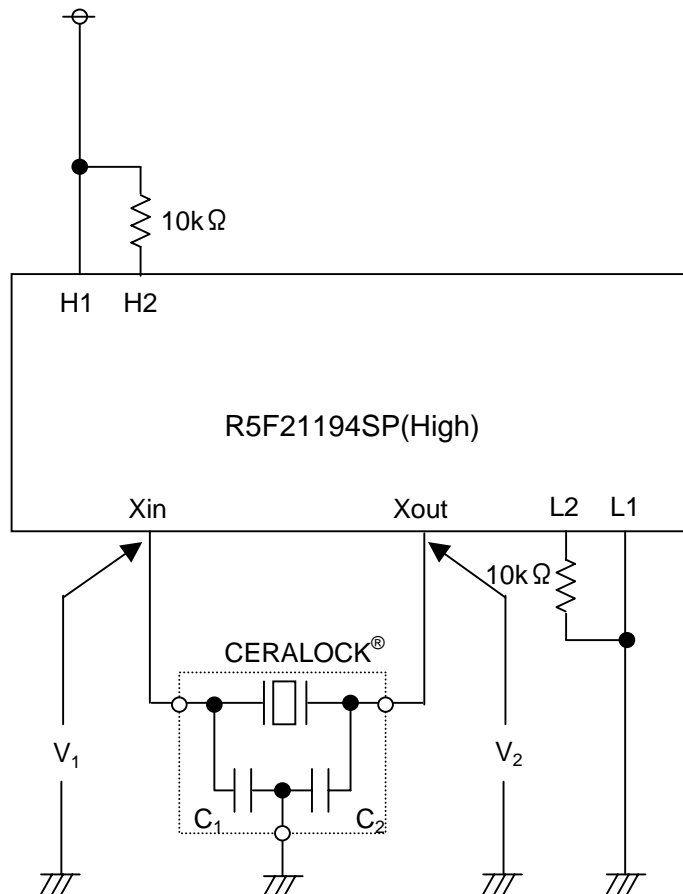
In the case that rising time of Vdd(Vset) is slow comparing to resonator's rise time due to the bypass capacitor, resonator's rise time is also slow because it depends on rising time of Vdd(Vset).

Also, in the case that the time supplying voltage to the oscillator circuit takes a certain time by reset time etc after Vdd(Vset) is applied, resonator's rise time is also slow.

In these case, we will describe "Unable to measure" in rise time data, because we can not measure resonator's rise time correctly.

## Test Circuit

Vcc = 5.0 [V]



Xin : 6

Xout: 4

H1 : 3,7

L1 : 5

H2 : 8,10

L2 : 2

Recommended Value

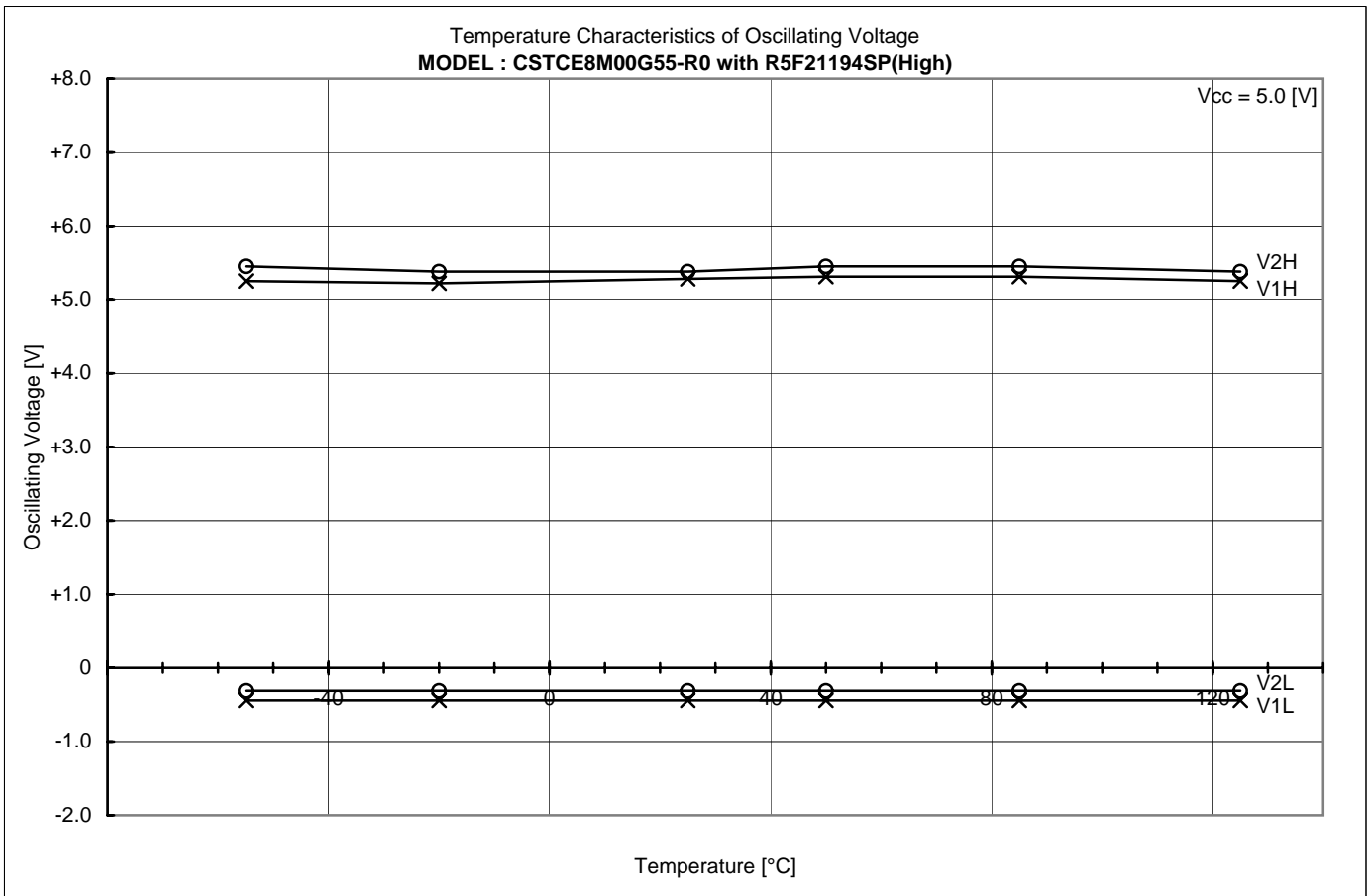
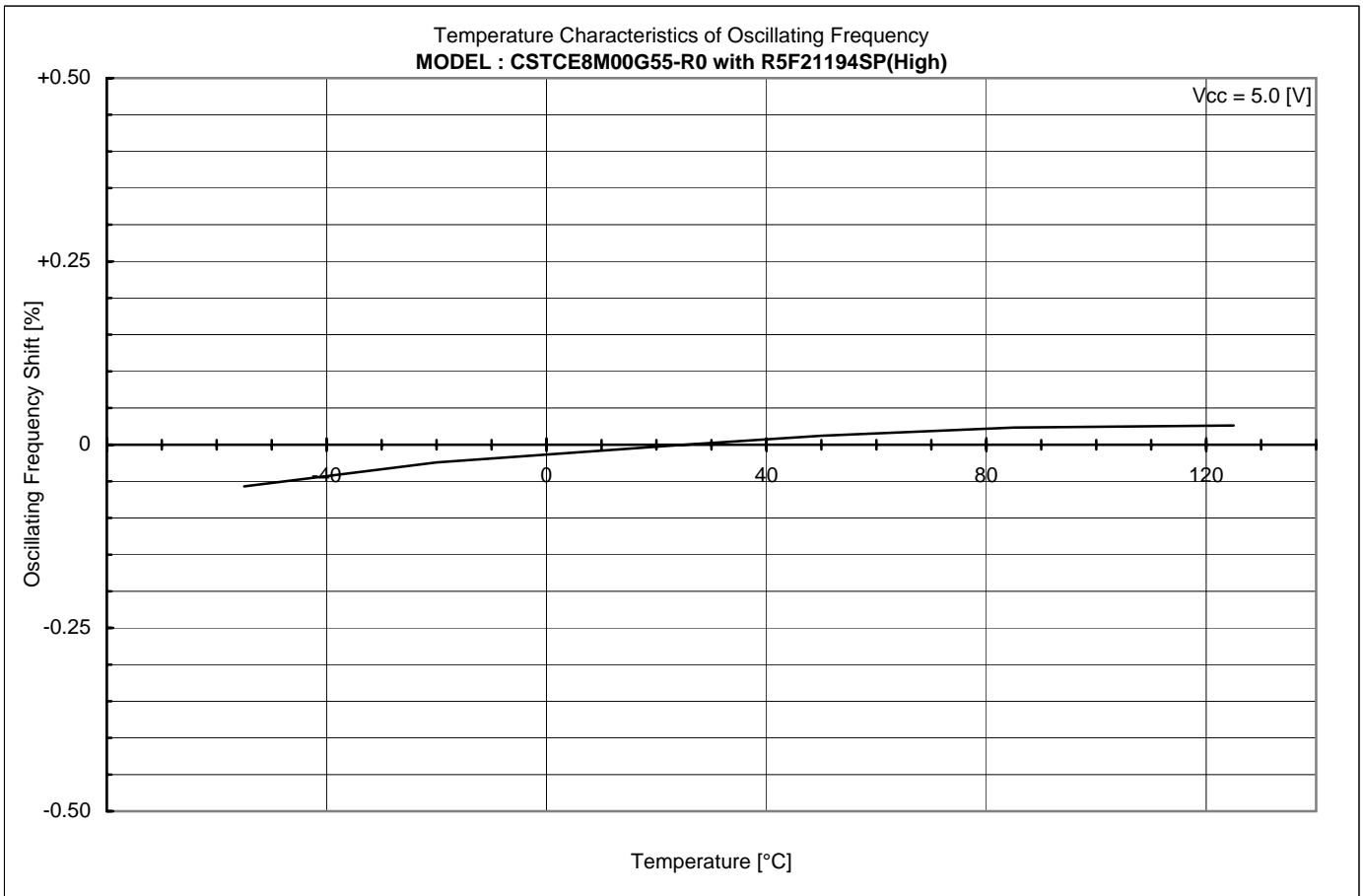
CERALOCK<sup>®</sup> : CSTCE8M00G55-R0

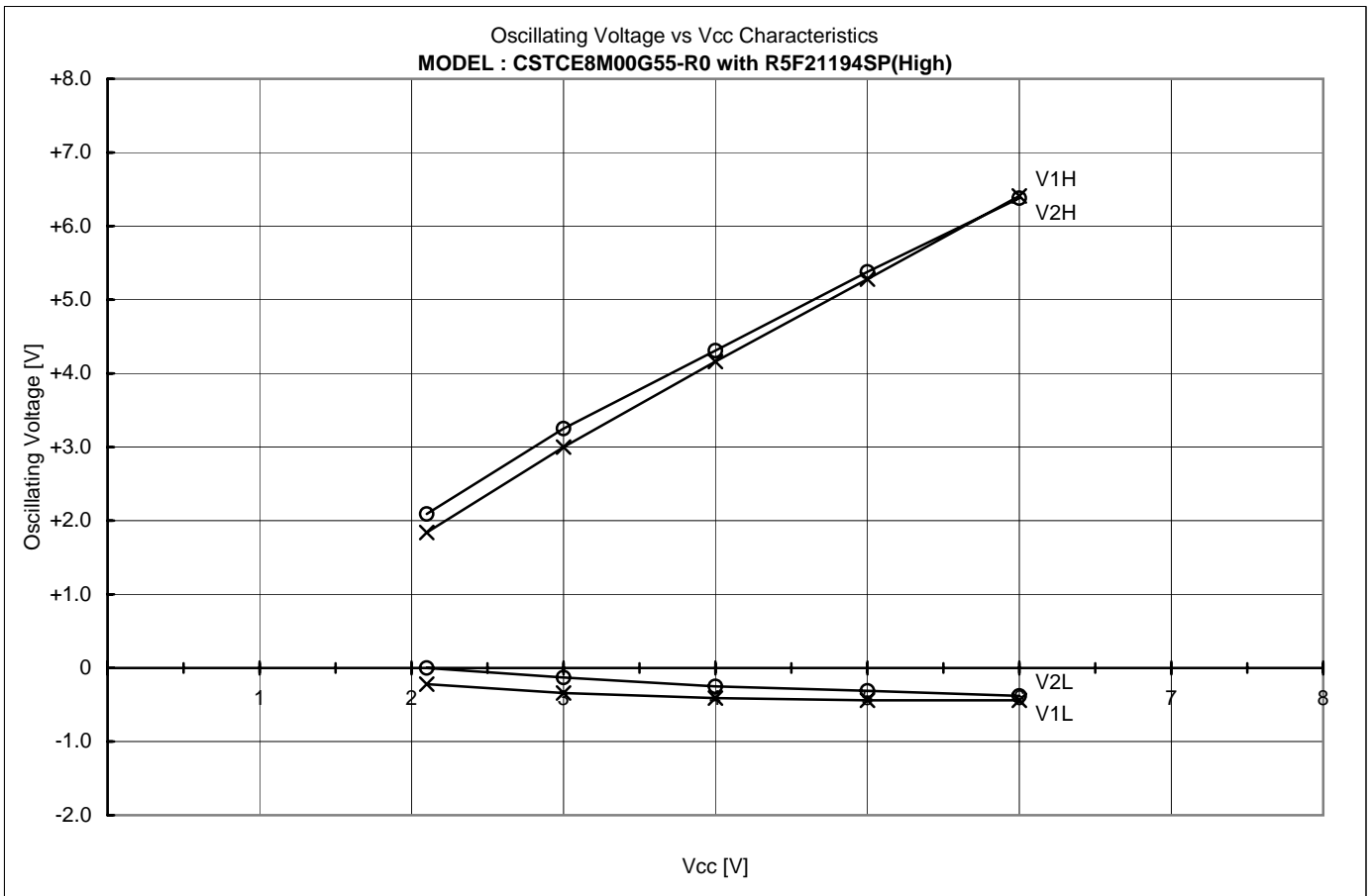
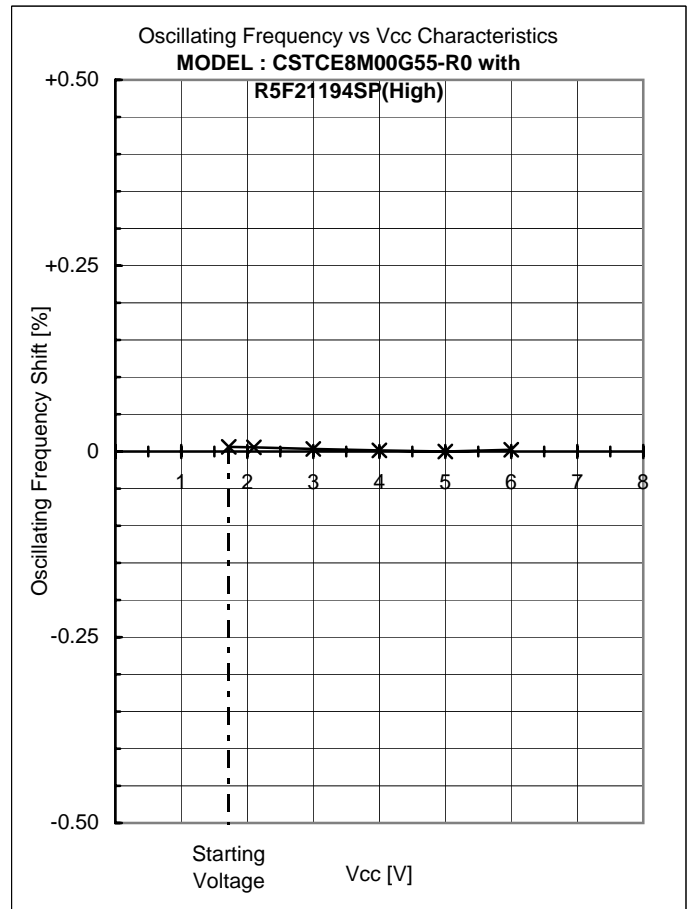
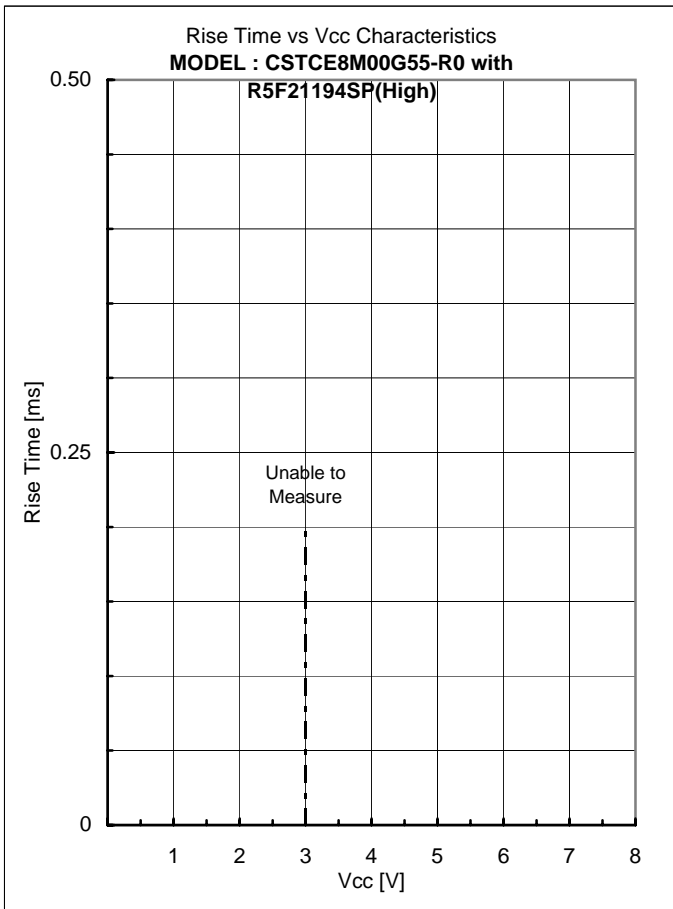
Vcc = 2.7 to 5.5 [V]

C1 = 33 [pF] (Typ.)

C2 = 33 [pF] (Typ.)

Ta = -40 to 85 [°C]







## Appendixes

### 4. Comparison Table

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### Comparison Table

IC : No	V1H [V]	V1L [V]	V1p-p [V]	V2H [V]	V2L [V]	V2p-p [V]	Fosc [kHz]	Trise [ms]	Vstart [V]
TYP	5.28	-0.44	5.72	5.38	-0.31	5.69	8007.179	Unable to	1.72
LL	5.34	-0.41	5.75	5.38	-0.38	5.76	8007.100	Measure	1.66
LH	5.28	-0.44	5.72	5.38	-0.31	5.69	8006.518		1.67
HL	5.34	-0.41	5.75	5.31	-0.38	5.69	8006.877		1.66
HH	5.34	-0.44	5.78	5.38	-0.38	5.76	8007.000		1.75

Ref.

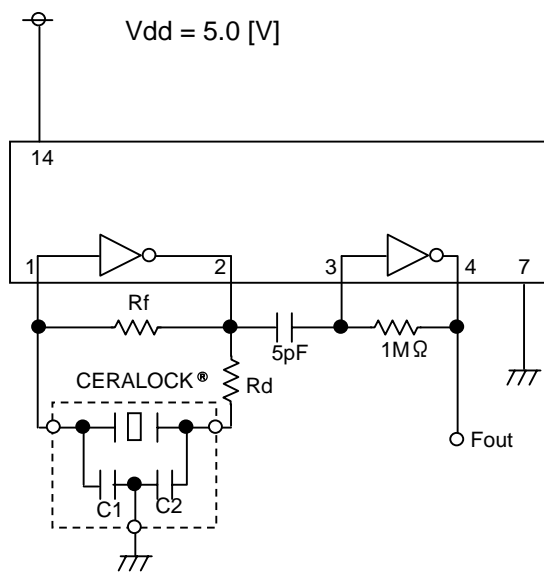
Performance described page 2 to 3 were measured with IC No. TYP



### Frequency Correlation Data

Sample No.	R5F21194SP(High) Fosc [kHz]	TC74HCU04AP Fosc [kHz]	Shift [%]
1	8010.266	8009.400	0.0108
2	8008.723	8008.700	0.0003
3	8008.032	8008.400	-0.0046
4	8008.559	8008.900	-0.0043
5	7993.088	7992.600	0.0061
$\bar{X}$	8005.733	8005.600	0.0017

#### muRata Standard Circuit



CERALOCK® : CSTCE8M00G55-R0

C1 = 33 [pF]

C2 = 33 [pF]

Rf = 1 [Mohm]

Rd = 330 [ohm]