

ISL70020SEHSEE Test Report

Introduction

The intense proton and heavy ion environment encountered in space applications can cause a variety of Single Event Effects (SEE) in electronic circuitry, including Single Event Upset (SEU), Single Event Transient (SET), Single Event Functional Interrupt (SEFI), Single Event Gate Rupture (SEGR), and Single Event Burnout (SEB). SEE can lead to system-level performance issues including disruption, degradation, and destruction. For predictable and reliable space system operation, individual electronic components should be characterized to determine their SEE response. This report discusses the results of destructive SEE testing performed on the constituent die of the [ISL70020SEH](#) GaN transistor.

Product Description

The ISL70020SEH is a 40V N-channel enhancement mode GaN power transistor packaged in hermetic Ceramic Leadless Chip Carriers (CLCC). The die packaged into the CLCC by Renesas are manufactured by EPC (Efficient Power Conversion Company). The EPC parts are bare die solder bumped to be flip-chip mounted. The die used by Renesas in the CLCC, the EPC2924, have high temperature solder bumps to allow soldering of the CLCC without de-mounting the die already mounted inside the package. The commercial equivalent of EPC product is the EPC2024 (40V, 90A).

Related Literature

For a full list of related documents, visit our website:

- [ISL70020SEH](#) device page

1. Single Event Effects Test

1.1 Objective

The testing described here was intended to characterize the constituent die of the ISL70020SEH transistor for energetic heavy ion irradiation impact on I_{DSS} (two terminal blocking current) when the parts were irradiated in the blocking mode. The primary concern was SEB typified by a sudden large increase in I_{DSS} during irradiation. The secondary interest was the gradual increase in I_{DSS} with irradiation fluence noted during testing of other GaN FET parts. The testing was intended to provide a safe operating area (for both V_{DSS} and irradiation Linear Energy Transfer (LET) for SEB) and to quantify the rate of the gradual increase of I_{DSS} with fluence, V_{DSS} , and LET.

1.2 Facility

The testing was done at the Texas A&M University (TAMU) Radiation Effects Facility of the Cyclotron Institute. This facility is coupled to a K500 superconducting cyclotron that is capable of generating a wide range of particle beams with various energy and flux levels needed for advanced single event testing. The ion species used in the testing reported here and the approximate ion parameters are as listed in [Table 1](#). The testing was done on December 5, 2018.

Table 1. Ion Species and Approximate Parameters Used in Testing the EPC2024

Species	Initial Total Energy (GeV)	Surface LET after window and air path (MeV·cm ² /mg)	Range to Bragg Peak in Si (μm)
Kr	1.259	28	115
Ag	1.634	43	91
Pr	2.114	60	85
Au	2.954	86	63

Note: Taken from TAMU Cyclotron Institute on-line beam characteristics information.

1.3 Setup and Method

To make the ISL70020SEH's device side accessible for ion irradiation, the flip-chip devices were mounted with the solder bumped side exposed away from the Printed Circuit Board (PCB) to which the parts were physically attached. The connections from the devices to the PCB traces were made by soldering fine wires from the PCB traces to the device solder bumps. The parts were wired for testing in a two terminal configuration with drain biased against the gate, source, and substrate (wired together at the device). "[Appendix A](#)" on [page 7](#) provides diagrams of how the wire mounting was done.

For irradiation testing, four devices mounted on a PCB inside the ion beam diameter of one inch were biased with a single voltage supply (V_{DSS}) through four separate current meters, one for each Device Under Test (DUT). This allowed the current (I_{DSS}) to be monitored on each DUT. One set of four DUTs was used for each combination of irradiation species (4), and test voltage (3) resulting in twelve separate irradiation runs.

Before and after each irradiation the current was logged for irradiation V_{DSS} biasing without the ion beam. The I_{DSS} current was also measured for the absolute maximum voltage ratings (40V) before and after each irradiation. The measurements and irradiations were carried out at ambient temperature (~25°C) to a fluence of 2.5×10^6 ion/cm² at a flux of approximately 1×10^4 ion/(cm²-s). This brings the total fluence for the device type at each species and V_{DSS} combination to 1×10^7 ion/cm².

Each combination of ion species (4) and V_{DSS} (3) was tested on four fresh DUTs with the sequence of events outlined in [Table 2 on page 3](#). The I_{DSS} current of each DUT was monitored and logged during each row entry in [Table 2](#). The V_{DSS} during irradiation for the EPC2024 took values of 24V, 32V, and 40V. In the case of irradiation with $V_{DSS} = 40V$, the first and last rows of [Table 2](#) became redundant and were dropped so the resulting sequence had only three rows.

Table 2. Sequence of Events for I_{DSS} Logging Using the First V_{DSS} Voltage for the EPC2024

Flux (ion/(cm ² -s))	Fluence (ion/cm ²)	V_{DSS} (V)	Time (s)
0	0	40	30
0	0	24	30
1.0E+04	2.50E+06	24	250
0	0	24	30
0	0	40	30

1.4 Pre-Irradiation Characterization

Prior to irradiation each part had its I_{DSS} measured at 40V V_{DSS} for approximately 30 seconds at a sampling time of about 0.78 seconds. The measurements produced for each part were then used to characterize the parts' I_{DSS} as represented in [Figure 1](#). Because the SEB testing plan was to use a different set of DUTs (4) for each condition it is important that the parts for testing represent a homogeneous population. The total population registered a mean I_{DSS} value of 5.51 μ A between extremes of 1.45 μ A and 12.54 μ A and an overall standard deviation of 2.50 μ A.

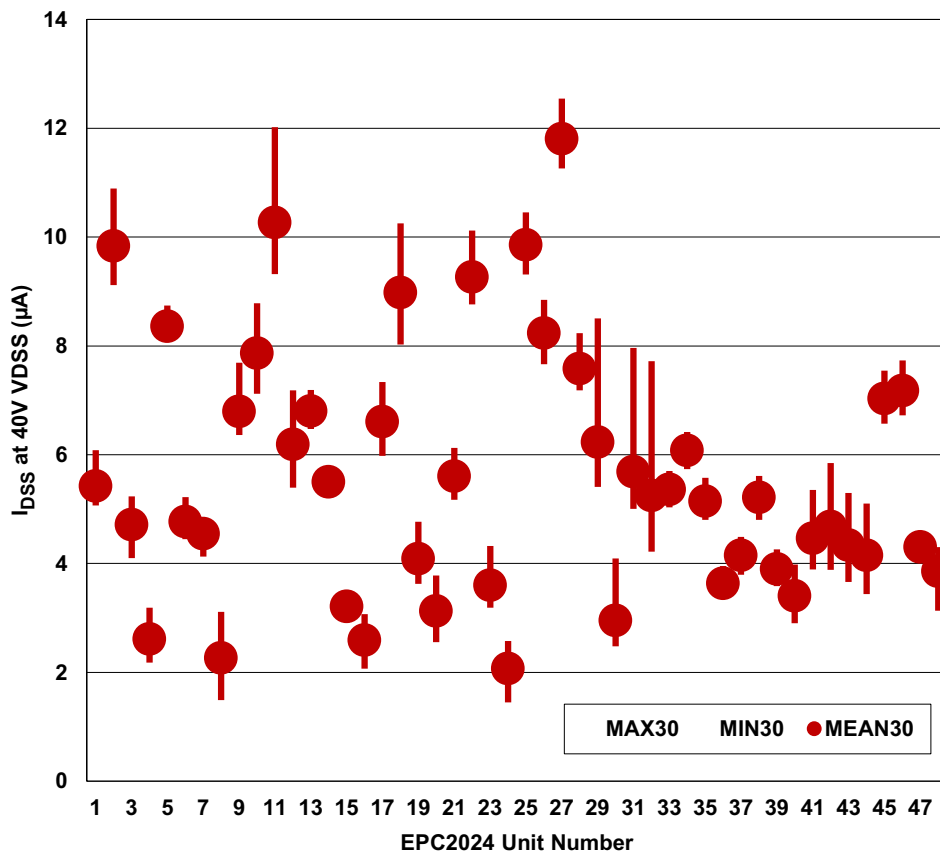


Figure 1. Initial I_{DSS} at 40V Characterization Data on the EPC2024 Devices Taken Before Irradiation Testing

The logged I_{DSS} data was then used to calculate the sequential changes in the I_{DSS} measurements. These sequential changes give a representation of the nominal error associated with the measurements. A histogram of the measurement changes is presented in [Figure 2 on page 4](#). The standard deviation for all the measurement changes was 82nA. The minimum and maximum changes were -502nA and 310nA, respectively. This sets the bounds on the normal variation in the I_{DSS} measurements.

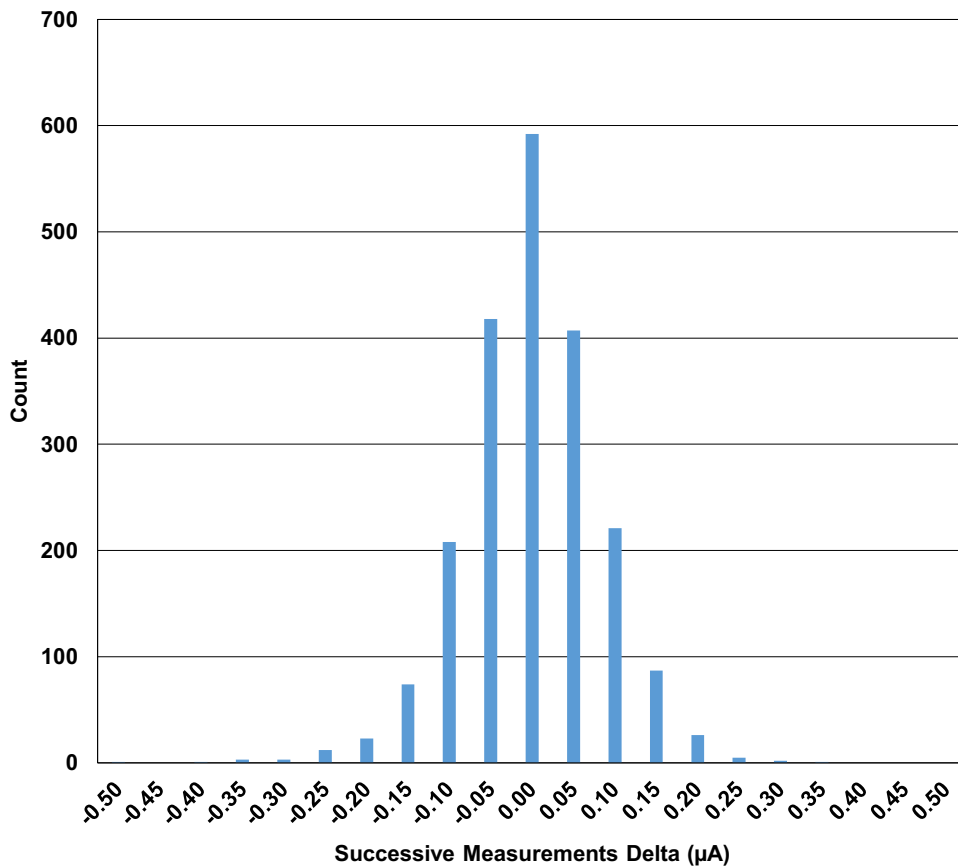


Figure 2. 30-Second Sequential Changes in I_{DSS} Measurements at 40V for the Pre-Irradiation Characterization of the Part Set Going to SEB Testing

2. Results

The I_{DSS} for each EPC2024 part was logged for 30s at 40V and for 30s at the irradiation V_{DSS} (24V or 32V) immediately before irradiation. Next, I_{DSS} was logged during irradiation at the selected V_{DSS}. After irradiation, I_{DSS} was again logged for 30s at the irradiation V_{DSS} and for 30s at 40V.

The I_{DSS} data collected during the irradiation was used to calculate the step changes in I_{DSS} between measurements. The minimum, maximum, and average steps were found for each grouping of four units irradiated together. This I_{DSS} step data by irradiation condition appears in [Table 3](#). Only one irradiation treatment yielded a positive step in excess of 1μA at 1.44μA. The irradiation yielding this result was V_{DSS} = 40V at LET = 86MeV·cm²/mg. Although these events are statistically significant, they represent small perturbations in the blocking current. Note, the irradiation condition also provided the largest negative step in I_{DSS} at -0.81. The adjacent irradiation of 32V at 86MeV·cm²/mg had the second highest maximum step at 0.63μA.

Table 3. EPC2024 IDSS Step Statistics During Irradiation by Irradiation Treatment

EPC2024 I _{DSS} Irradiation Step Statistics in μA				
LET (MeV·cm ² /mg)	Step	V _{DSS} During Irradiation		
		24V	32V	40V
28	Mean	0.0013	-0.0013	-0.0045
	Min	-0.34	-0.36	-0.31
	Max	0.31	0.26	0.53

Table 3. EPC2024 IDSS Step Statistics During Irradiation by Irradiation Treatment (Continued)

EPC2024 I_{DSS} Irradiation Step Statistics in μA				
LET (MeV·cm ² /mg)	Step	V_{DSS} During Irradiation		
		24V	32V	40V
43	Mean	0.0003	0.0011	0.0003
	Min	-0.43	-0.38	-0.39
	Max	0.34	0.33	0.30
60	Mean	0.0028	0.0010	0.0021
	Min	-0.26	-0.25	-0.42
	Max	0.37	0.31	0.48
86	Mean	0.0023	0.0302	0.0476
	Min	-0.36	-0.50	-0.81
	Max	0.35	0.63	1.44

Note: Each irradiation was done on four devices to 2.5×10^6 ion/cm² and the statistics on the I_{DSS} steps of the group are reported here.

The EPC2024 parts did exhibit a gradual growth of I_{DSS} , measured at $V_{DSS} = 40V$ for irradiation conditions with LET at $86MeV \cdot cm^2/mg$ and $60MeV \cdot cm^2/mg$. The minimum I_{DSS} measured at 40V before irradiation was subtracted from the maximum I_{DSS} registered after irradiation for each part to establish the I_{DSS} deltas over the irradiation. The change in μA was then divided by 2.5 to yield an I_{DSS} rise per 1×10^6 ion/cm². These numbers are reported in [Table 4](#). It is worth noting that the time in orbit to accumulate 1×10^6 ion/cm² of ions with LET greater than or equal to $28MeV \cdot cm^2/mg$ is greater than one hundred thousand years. Therefore, the current increases given in [Table 4](#) really have no practical application in normal usage as the fluence encountered in a mission is much too small to yield meaningful increases.

Table 4. EPC2024 Change in I_{DSS} (μA) at $V_{DSS} = 40V$ per Irradiation with 1×10^6 ion/cm² by DUT

LET	Change in I_{DSS} in μA at $V_{DSS} = 40V$ per Irradiation to 1×10^6 ion/cm ²											
	$V_{DSS} = 24$				$V_{DSS} = 32$				$V_{DSS} = 40$			
	DUT1	DUT2	DUT3	DUT4	DUT1	DUT2	DUT3	DUT4	DUT1	DUT2	DUT3	DUT4
28	-0.30	-0.26	-0.28	0.11	-0.61	-0.76	-0.96	-0.73	-0.34	-0.82	-0.49	-0.59
43	-0.46	-0.80	-0.36	-0.40	-0.45	-0.49	-0.24	0.04	-0.12	-0.06	-0.10	0.11
60	-0.21	0.29	-0.39	-0.38	-0.14	-0.23	0.03	-0.06	0.10	0.04	-0.72	0.40
86	-0.13	-0.30	-0.08	-0.09	4.76	5.36	2.32	1.92	5.48	8.97	1.97	5.97

Note: The blue cells indicate groupings that did not show a significant increase in I_{DSS} over the irradiations. The yellow cells indicate cases that did exhibit I_{DSS} growth with irradiation. Negative changes are indicated in bold text.

With the exception of the three higher stresses ($86MeV \cdot cm^2/mg$ at 32V and 40V, and $60MeV \cdot cm^2/mg$ at 40V) the changes in I_{DSS} show a negative trend. The three higher stresses do show a positive change trend. The I_{DSS} evolution for the highest stress case is presented in [Figure 3 on page 6](#) (Note: Changes in test conditions are marked by spikes in data to $30\mu A$). Clearly the irradiation caused a gradual rise in I_{DSS} composed of many small increments. The largest positive step in I_{DSS} ($1.44\mu A$) occurred in DUT2 which performed somewhat worse than the other three DUTs. A three sigma bound on the increase is $14.2\mu A$ over 1×10^6 ions/cm² at $86MeV \cdot cm^2/mg$ and a bias of 40V. The three sigma bound on the case of a 32V bias at $86MeV \cdot cm^2/mg$ to 1×10^6 ion/cm² is $8.8\mu A$. At the 40V bias at $60MeV \cdot cm^2/mg$ the three sigma bound on the increase drops to $1.4\mu A$.

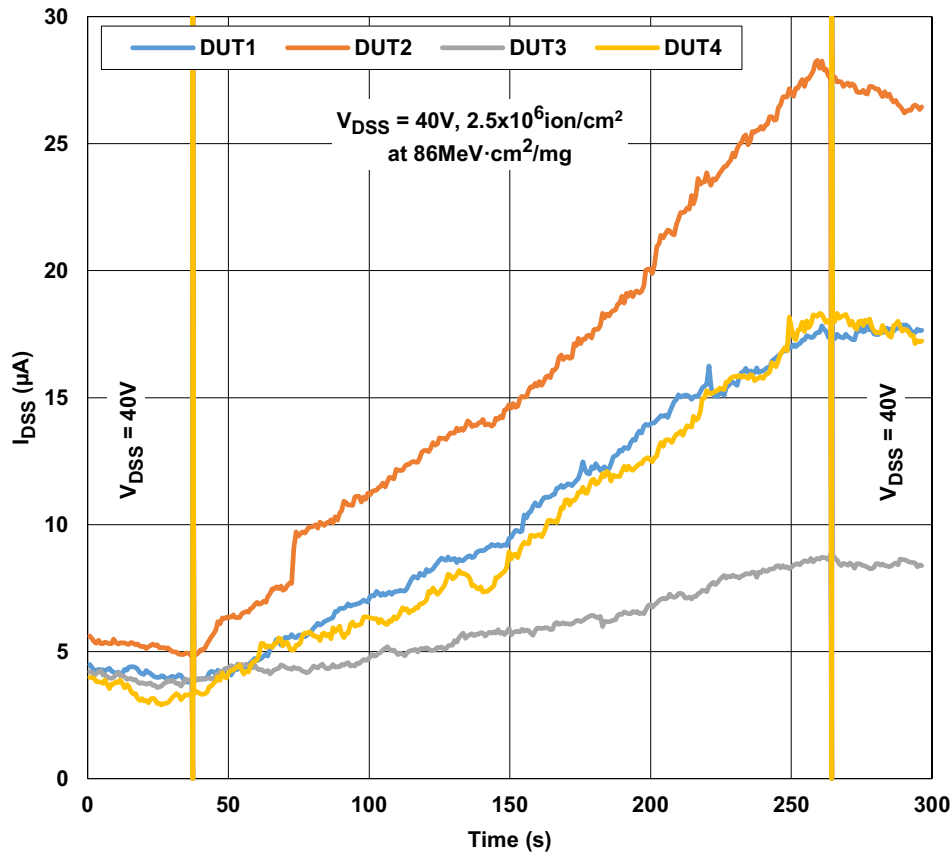


Figure 3. I_{DSS} Behavior for the EPC2024 40V GaN FET at $V_{DSS} = 40V$ and $LET = 86MeV \cdot cm^2/mg$ to $2.5 \times 10^6 ion/cm^2$

3. Discussion and Conclusions

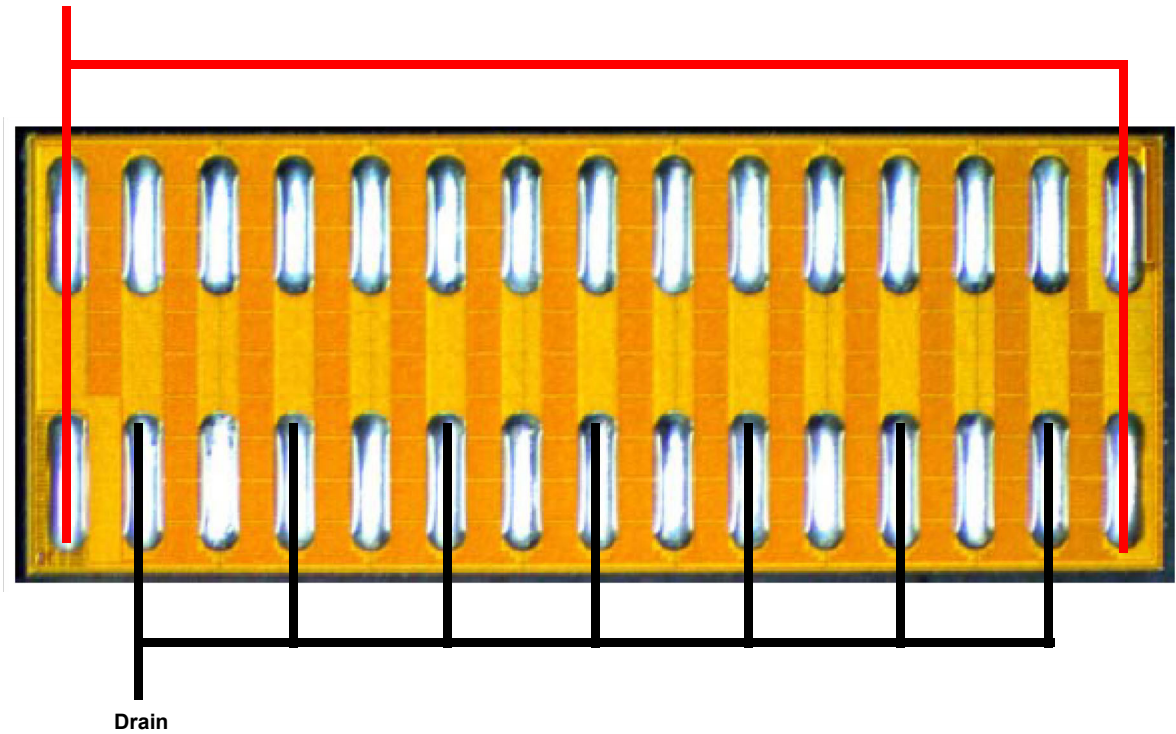
The ISL70020SEH (EPC2024) devices exhibited two forms of I_{DSS} behavior over the range of twelve irradiation conditions tested ($V_{DSS} = 24V, 32V,$ and $40V$ with $LET = 28, 43, 60,$ and $86MeV \cdot cm^2/mg$ at $25^\circ C$). For the two lower LET, no apparent changes in I_{DSS} during irradiation at $V_{DSS} = 40V$ were found. The conclusion is that these irradiation conditions ($V_{DSS} \leq 40V$ and $LET \leq 43MeV \cdot cm^2/mg$) define an unconditional Safe Operating Area (SOA). At $LET = 60MeV \cdot cm^2/mg$, the two lower voltages (24V and 32V) did not show any apparent increase in I_{DSS} at 40V. However, at a 40V bias and $LET=60MeV \cdot cm^2/mg$, there did appear to be some gradual I_{DSS} increase during the irradiation. At $LET=86MeV \cdot cm^2/mg$ the lowest voltages (24V) did not show any apparent increase in I_{DSS} when measured at 40V; however, at 32V and 40V biases at this LET, there appeared to be significant I_{DSS} increase. No current steps greater than $1.44\mu A$ were registered even for these evolving I_{DSS} conditions. These higher conditions can be interpreted as conditional SOA. It is important to note that even in this conditional SOA the fluences needed to cause any significant I_{DSS} increase are more than fifty thousand times that expected in a twenty year earth's orbit mission.

No occurrences of catastrophic I_{DSS} increase were registered for any irradiation conditions. Therefore, even at a 40V bias and irradiation with $86MeV \cdot cm^2/mg$ gold ions, there were no catastrophic failures indicative of SEB for the devices tested. The testing amounted to $1 \times 10^7 ions/cm^2$ at $86MeV \cdot cm^2/mg$ distributed over four parts.

4. Appendix A

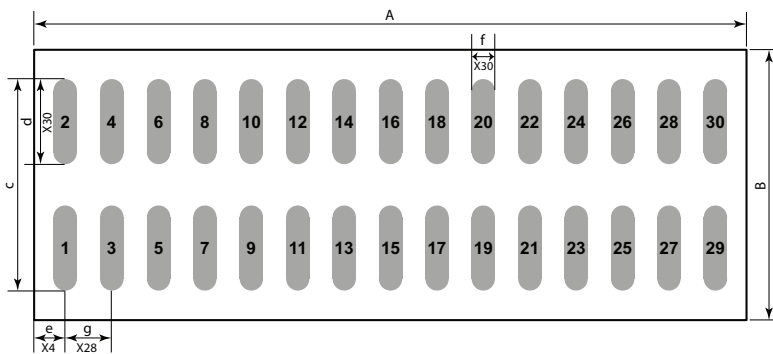
Dead-Bug View of EPC2924 (from EPC2024 datasheet) and Connection for SEB testing.

Gate, Source, and Substrate



Die Outline

Solder Bar View



DIM	Micrometers		
	MIN	Nominal	MAX
A	6020	6050	6080
B	2270	2300	2330
c	2047	2050	2053
d	717	720	723
e	210	225	240
f	195	200	205
g	400	400	400

- Pad no. 1 is Gate
- Pads 2, 5, 6, 9, 10, 13, 14, 17, 18, 21, 22, 25, 26, 29 are Source
- Pads 3, 4, 7, 8, 11, 12, 15, 16, 19, 20, 23, 24, 27, 28 are Drain
- Pad 30 is Substrate

5. Revision History

Rev.	Date	Description
0.00	May.14.19	Initial release

Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
 - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
 - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.
6. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
11. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.4.0-1 November 2017)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:
www.renesas.com/contact/

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.