Single Event Effects Testing of the ISL70227SRH, Dual 36V Rad Hard Precision Operation Amplifiers

AN1756 Rev 0.00 May 21, 2012

Introduction

intersil

The intense heavy ion environment encountered in space applications can cause a variety of transient and destructive effects in analog circuits, including single-event latch-up (SEL), single-event transients (SET) and single-event burnout (SEB). These effects can lead to system-level failures including disruption and permanent damage. For predictable and reliable system operation, these components have to be formally designed and fabricated for SEE hardness, followed by a detailed SEE testing to validate the design. This report discusses the results of SEE testing of Intersil's ISL70227SRH.

Related Documents

• ISL70227SRH Data Sheet, FN7925

Product Description

The ISL70227SRH is a low noise 10MHz BW high precision, dual operational amplifier featuring very low input bias current and low offset voltage with low temperature drift. A super-beta NPN input stage with input bias current cancellation provides low input bias current and low input offset voltage while a complimentary bipolar output stage enables high capacitive load drive without external compensation. These features plus its radiation tolerance make the ISL70227SRH the ideal choice for applications requiring both high DC accuracy and AC performance.

The ISL70227SRH is implemented in an advanced bonded wafer SOI process using deep trench isolation, resulting in a fully isolated structure. This choice of process technology also results in latch-up free performance, whether electrically or single event induced (SEL).

This amplifier is designed to operate over a wide supply range of 4.5V to 36V. Applications for these amplifiers include precision active filters, low noise front ends, loop filters, data acquisition and charge amplifiers.

The part is packaged in a 10 lead hermetic ceramic flat pack and operates over the extended temperature range of -55 °C to +125 °C. A summary of key full temperature range specifications follows:

- Input Offset Voltage 100µV, max.
- Offset Voltage Drift 1µV/°C, max.
- Input Offset Current 12nA, max.
- Supply Current/Amplifier 3.7mA, max.
- Gain Bandwidth Product 10MHz, typ.

SEE Test Objective

The objectives of SEE testing on the ISL70227SRH were to evaluate its susceptibility to single event latch-up and single event burnout and determine its SET behavior.

SEE Test Facility

Testing was performed at the Texas A&M University (TAMU) Cyclotron Institute heavy ion facility. This facility is coupled to a K500 super-conducting cyclotron, which is capable of generating a wide range of test particles with the various energy, flux and fluence levels needed for advanced radiation testing.

SEE Test Procedure

The part was tested for single event latch-up and burnout, using Au ions (LET = 86.4MeV \cdot cm²/mg) with a case temperature of 125 °C and single event transient characterized using Ne, Ar, and Kr ions with a case temperature of 25 °C.

The device under test (DUT) was mounted in the beam line and irradiated with heavy ions of the appropriate species. The parts were assembled in 10 lead dual in-line packages with the metal lid removed for beam exposure. The beam was directed onto the exposed die and the beam flux, beam fluence and errors in the device outputs were measured.

The tests were controlled remotely from the control room. All input power was supplied from portable power supplies connected via cable to the DUT. The supply currents were monitored along with the device outputs. All currents were measured with digital ammeters, while all the output waveforms were monitored on a digital oscilloscope for ease of identifying the different types of SEE, which the part displayed. Events were captured by triggering on changes in the output.

SEE Test Set-Up Diagrams

A schematic of the evaluation board is shown in Figure 1.

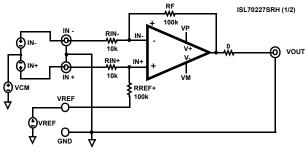


FIGURE 1. ISL70227SRH SEE TEST SCHEMATIC

Each operational amplifier was set up in a non-inverting operation with G = 10V/V. The IN- inputs were grounded and the input signal was applied to the IN+ pin.

Cross-section Calculation

Cross sections (CS) are calculated as shown by Equation 1:

CS (LET) = N/F

where:

- CS is the SET cross section (cm²), expressed as a function of the heavy ion LET
- LET is the linear energy transfer in MeV · cm²/mg, corrected according to the incident angle, if any
- N is the total number of SET events
- F is fluence in particles/cm², corrected according to the incident angle, if any

A value of 1/F is the assumed cross section when no event is observed.

Single Event Latch-up and Burnout Results

The first testing sequence looked at destructive effects due to burnout or latch-up. A burnout condition is indicated by a permanent change in the device supply current after application of the beam. If the increased current is reset by cycling power, it is termed a latch-up. No burnout or latch-up was observed using Au ions (LET = 86.4MeV \cdot cm²/mg) at 0° incidence from the perpendicular. Testing was performed on four parts at +125°C (case temperature) and up to the maximum voltage, $V_S = \pm 18.2V$. The first three parts (part ID 1, 2 & 3) commenced testing with $V_S = \pm 15V$ and on subsequent tests V_S voltage was increased to $\pm 17.5V$ and then $\pm 18.2V$. The last parts were tested with a V_S of \pm 17.5V and \pm 18.2V. All test runs were run to a fluence of 2×10^6 /cm². A power supply applied a DC voltage of 200mV to the non-inverting inputs of the amplifiers during the test. Functionality of all outputs was verified after exposure. IDD and I_{EE} were recorded pre and post exposure, with 5% resolution. Results are shown in Table 1 for the 36.4V total supply voltage.

Single Event Transient Testing Test Method

Biasing used for SET test runs was $V_S = \pm 4.5V$ and $\pm 18V$. Similar to SEL/B testing, a DC voltage of 200mV was applied to the non-inverting inputs of the amplifiers. Signals from the switch board in the control room were connected to two LECROY oscilloscopes: one set to capture transients due to the output of channel A and the other to capture transients on the output of channel B.

SET events are recorded when movement on output during beam exposure exceeds the set window trigger of ± 100 mV. Summary of the scope settings are:

- a. Scope 1 is set to trigger on Channel 1 to a OUTA window of ±100mV. Measurements on Scope 1 are: CH1 = OUTA 200mV/div, CH2 = OUTB 500mV/div, CH3 = OUT 200mV/div, CH4 = OUT5 500mV/div.
- b. Scope 2 is set to trigger on Channel 3 to a OUTB window of ±80mV. Measurements on Scope 1 are: CH1 = OUTA 200mV/div, CH2 = OUTA 500mV/div, CH3 = OUTB 200mV/div, CH4 = OUT5 500mV/div.

The switch board at the end of the 20-ft cabling was found to require terminations of 10nF to keep the noise on the waveforms to a minimum.

Cross Section Results

Compared to other Intersil radiation tolerant circuits, the ISL70227SRH was not designed for single event transient mitigation. The best approach to characterize the single event transient response is to represent the data on a LET threshold plot.

Figure 2 shows the cross section of the IC versus the LET level, at $V_S = \pm 4.5V$ and $\pm 18V$. It can be seen that for an LET < 5.4 MeV· cm²/mg, the cross section is nearly the same independent of supply voltage. As the linear energy transfer increases, there is noticeable increase in cross section area with a lower supply voltage. Data from Figure 2 is represented in Table 2.

Figures 3 through 6 show the cross section of each channel independently at $V_S = \pm 4.5V$ and $\pm 18V$ with confidence interval bars for a 90% confidence level.

TEMP (°C)	LET (MeV∙cm ² /mg)	SUPPLY CURRENT PRE- EXPOSURE (mA)	SUPPLY CURRENT POST- EXPOSURE (mA)	LATCH EVENTS	CUMULATIVE FLUENCE (PARTICLES/cm ²)	CUMULATIVE CROSS SECTION (cm ²)	DEVICE ID	SEB/L
+125	86	10.6	10.6	0	2.0 x 10 ⁶	5.0 x 10 ⁻⁷	1	PASS
+125	86	10.8	10.8	0	2.0 x 10 ⁶	5.0 x 10 ⁻⁷	2	PASS
+125	86	11.0	11.0	0	2.0 x 10 ⁶	5.0 x 10 ⁻⁷	3	PASS
+125	86	10.7	10.7	0	2.0 x 10 ⁶	5.0 x 10 ⁻⁷	4	PASS
	TOTAL EVENTS 0							
OVERALL FLUENCE 8.0 x 10 ⁶								
	OVERALL CS 1.25 x 10 ⁻⁷							
						TOTAL UNITS	4	

TABLE 1. ISL70227SRH DETAILS OF SEB/L TESTS FOR VS = $\pm 18.2V$ and LET = $86.4MeV \cdot cm^2/mg$

(EQ. 1)

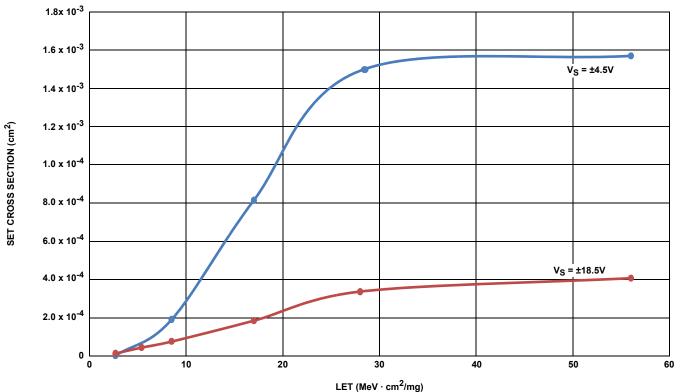


FIGURE 2. SET CROSS SECTION vs. LINEAR ENERGY TRANSFER vs. SUPPLY VOLTAGE

TABLE 2. DETAILS OF THE SET CROSS SECTION OF THE ISL70227SRH vs LET vs S	UPPLY VOLTAGE

SUPPLY VOLTAGE (V)	ION	ANGLE (°)	EFF LET (MeV·cm ² /mg)	FLUENCE PER RUN (PARTICLES/cm ²)	NUMBER OF RUNS	TOTAL SET	EVENT CS cm ²
±4.5V	Ne	0	2.7	2.0 x 10 ⁶	4	18	2.25 x 10 ⁻⁶
±4.5V	Ar	0	8	2.0 x 10 ⁶	3	1146	1.91 x 10 ⁻⁴
±4.5V	Ar	60	17	2.0 x 10 ⁶	4	6514	8.14 x 10 ⁻⁴
±4.5V	Kr	0	28	2.0 x 10 ⁶	4	5968	1.49 x 10 ⁻³
±4.5V	Kr	60	56	2.0 x 10 ⁶	4	6276	1.57 x 10 ⁻³
			1				-
±18V	Ne	0	2.7	2.0 x 10 ⁶	4	111	1.39 x 10 ⁻⁶
±18V	Ne	60	5.4	2.0 x 10 ⁶	4	362	4.53 x 10 ⁻⁶
±18V	Ar	0	8	2.0 x 10 ⁶	4	614	7.68 x 10 ⁻⁶
±18V	Ar	60	17	2.0 x 10 ⁶	4	1478	1.85 x 10 ⁻⁵
±18V	Kr	0	28	2.0 x 10 ⁶	4	2695	3.37 x 10 ⁻⁴
±18V	Kr	60	56	2.0 x 10 ⁶	4	3260	4.08 x 10 ⁻⁴

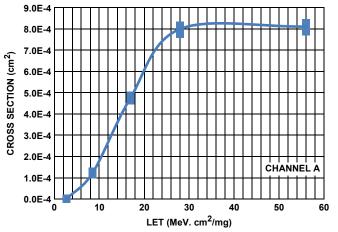


FIGURE 3. CHANNEL A SET CROSS SECTION vs. LET FOR V_S = $\pm 4.5 V$ WITH 90% CONFIDENCE LEVEL INTERVAL BARS

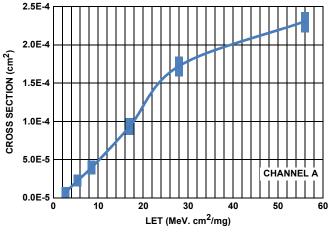


FIGURE 5. CHANNEL A SET CROSS SECTION vs. LET FOR V_S = $\pm 18V$ WITH 90% CONFIDENCE LEVEL INTERVAL BARS

Single Event Transient Response

The ISL70227SRH exhibited large single event transients compared to the ISL70218SRH [1]. Surprisingly, the duration of all the transients were less than 100μ s, with the majority of the transients lasting less than 50μ s. Figures 7 though 28 represent output waveforms of each channel of the amplifier under test at various bias conditions and LET values. The plots are composites of the first 50 transients captured on the scope. This information is useful in quantifying the excursion of the output as a result of SEE induced transients.

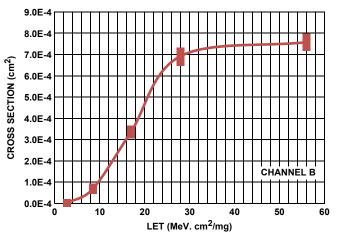


FIGURE 4. CHANNEL B SET CROSS SECTION vs. LET FOR V_S = $\pm 4.5 V$ WITH 90% CONFIDENCE LEVEL INTERVAL BARS

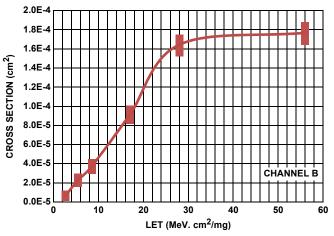
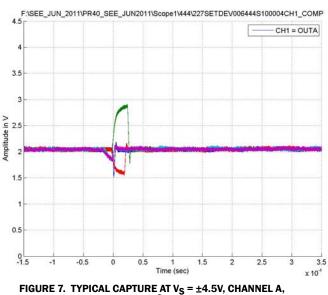
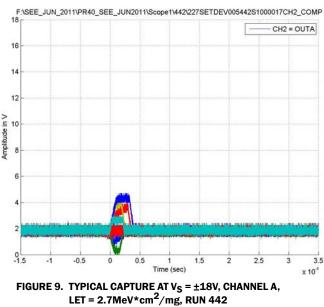


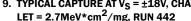
FIGURE 6. CHANNEL B SET CROSS SECTION vs. LET FOR V_S = $\pm 18V$ WITH 90% CONFIDENCE LEVEL INTERVAL BARS

Typical SET Captures



LET = 2.7MeV*cm²/mg, RUN 445





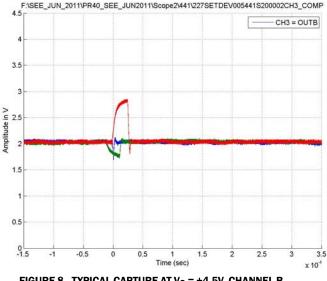


FIGURE 8. TYPICAL CAPTURE AT VS = ±4.5V, CHANNEL B, LET = $2.7 \text{MeV} \times \text{cm}^2/\text{mg}$, RUN 441

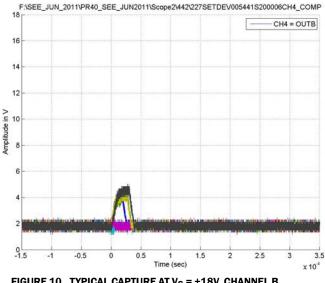


FIGURE 10. TYPICAL CAPTURE AT $V_S = \pm 18V$, CHANNEL B, LET = $2.7 \text{MeV} \cdot \text{cm}^2/\text{mg}$, RUN 433

intersil

F:\SEE_JUN_2011\PR40_SEE_JUN2011\Scope1\452\227SETDEV008452S1000049CH2_COMP 18

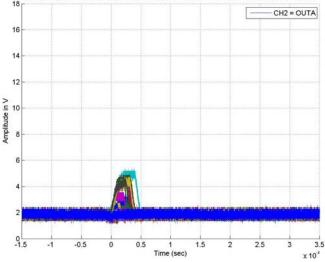
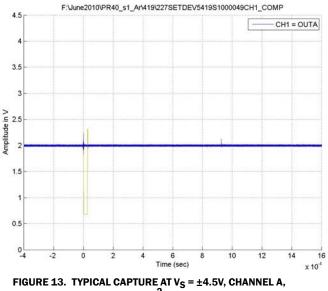


FIGURE 11. TYPICAL CAPTURE AT $V_S = \pm 18V$, CHANNEL A, LET = 5.4 MeV*cm²/mg, RUN 452





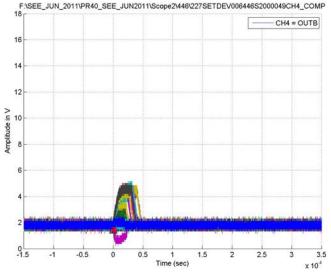
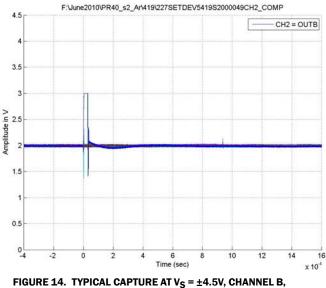


FIGURE 12. TYPICAL CAPTURE AT V_S = ±18V, CHANNEL B, LET = 5.4MeV*cm²/mg, RUN 446



LET = $8.5 \text{MeV} \cdot \text{cm}^2/\text{mg}$, RUN 419

intersil

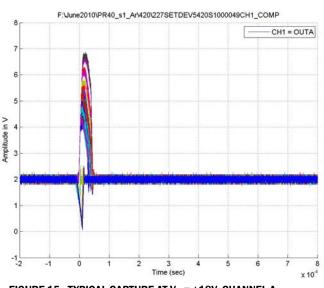
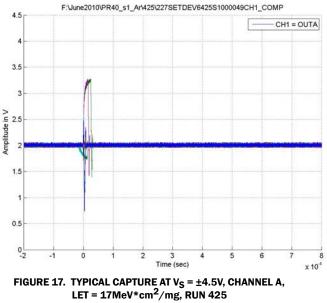
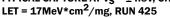
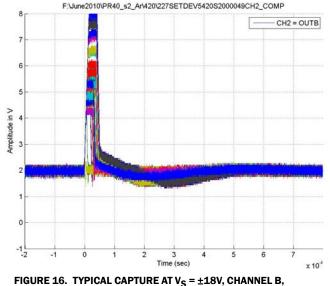


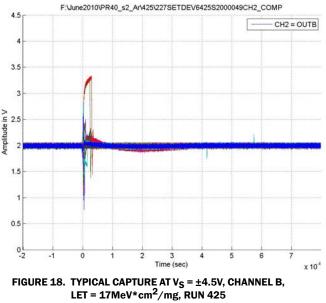
FIGURE 15. TYPICAL CAPTURE AT V_S = \pm 18V, CHANNEL A, LET = 8.5MeV*cm²/mg², RUN 420







LET = 8.5MeV*cm²/mg, RUN 420



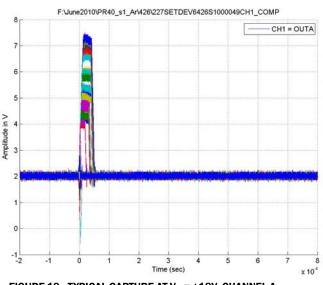
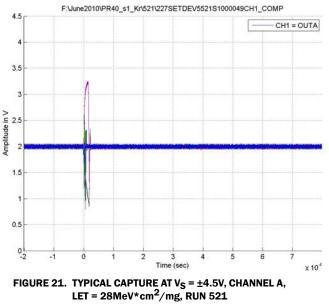
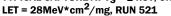


FIGURE 19. TYPICAL CAPTURE AT $V_S = \pm 18V$, CHANNEL A, LET = $17 \text{MeV} \cdot \text{cm}^2/\text{mg}$, RUN 426





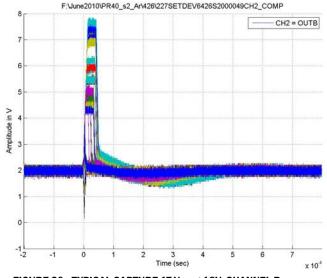


FIGURE 20. TYPICAL CAPTURE AT $V_S = \pm 18V$, CHANNEL B, LET = $17 \text{MeV} \times \text{cm}^2/\text{mg}$, RUN 404

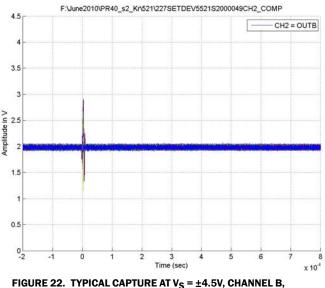


FIGURE 22. TYPICAL CAPTURE AT V_S = ± 4.5 V, CHANNEL B, LET = 28MeV*cm^2/mg, RUN 521

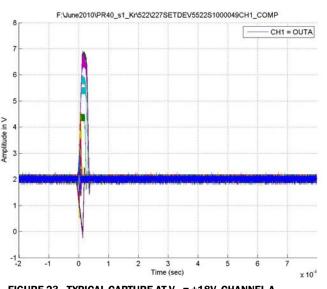
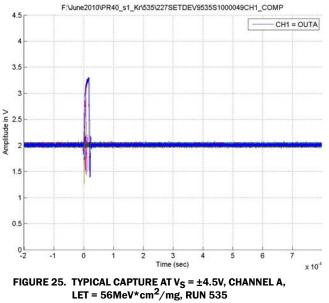
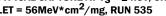


FIGURE 23. TYPICAL CAPTURE AT $V_S = \pm 18V$, CHANNEL A, LET = $28 \text{MeV} \cdot \text{cm}^2/\text{mg}$, RUN 522





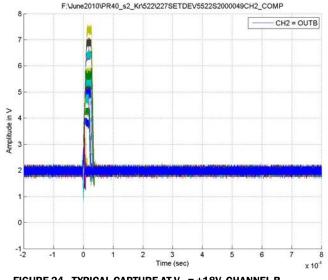


FIGURE 24. TYPICAL CAPTURE AT $V_S = \pm 18V$, CHANNEL B, LET = $28 \text{MeV} \times \text{cm}^2/\text{mg}$, RUN 512

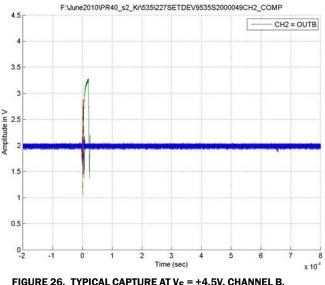
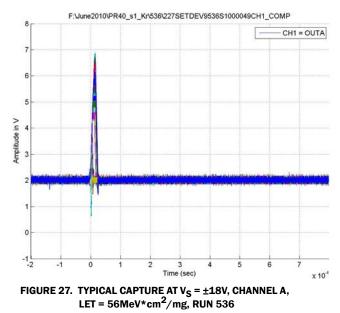


FIGURE 26. TYPICAL CAPTURE AT V_S = ± 4.5 V, CHANNEL B, LET = 56MeV*cm^2/mg, RUN 535



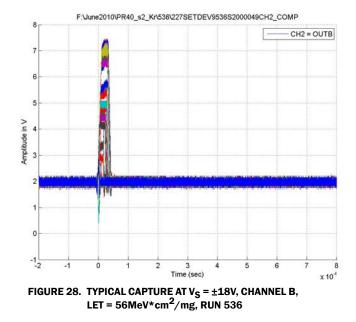
Summary

Single Event Burnout/Latch-up

No single event burnout (SEB) was observed for the device up to an LET of 86.4MeV \cdot cm²/mg (+125 °C) and voltage supply of V_S = ± 18.2V. No single event latch-up (SEL) was observed for the device up to an LET of 86.4MeV \cdot cm²/mg (+125 °C) and voltage supply of V_S = ± 18.2V. SEL and SEB were tested and passed at a supply voltage greater than that absolute maximum supply voltage of ± 18V.

Single Event Transient

Based on the results presented, the ISL70227SRH op amp offers advantages over competitor's parts with respect to the duration of the SET output voltage excursion and a lower SET cross section at a gain of 10 [2], [3]. For devices with $V_S = \pm 4.5V$ the worst case output voltage transient expected was 1V. This was not a surprise since the output voltage headroom is 1.5V maximum; with $V_S = \pm 4.5V$ the maximum output voltage expected is 3V. Figure 14 shows the output driven to 3V and into saturation; the recovery time is less than 100µs. Figure 13 shows the same phenomenon in a negative direction. For amplifiers supplied with a $V_S = \pm 18V$, the transient excursions were much larger, however they did not extend to the expected VOH or VOL levels of ±16.5V. All the transients observed were 6V deviations or less and recovery time of the transients were less than 100µs. Compared to the ISL70218SRH, this part does not experience the long recovery time during a single event transient. This may be explained by the higher drive capability of the ISL70227SRH and its ability to drive highly capacitive loads compared to the ISL70218SRH.



References

- Oscar Mansilla, Richard Hood, Lawrence Pearce, Eric Thomson and Nick Vanvonno, Application Note <u>AN1677</u>, "Single Event Effects Testing of the ISL70218SRH, Dual 36V Rad Hard Low Power Operation Amplifiers", Intersil Corporation.
- [2] S. Larsson and S. Mattsson, "Heavy Ion Transients in Operational Amplifier of Type LM124, RH1014 and OP27", <u>https://escies.org/download/webDocumentFile?id=837</u>
- [3] Ray Ladbury and Stephen Buchner, "SEE Testing of the RH1013 Dual Precision Operational Amplifier", http://radhome.gsfc.nasa.gov/radhome/papers/T121805 RH1013.pdf

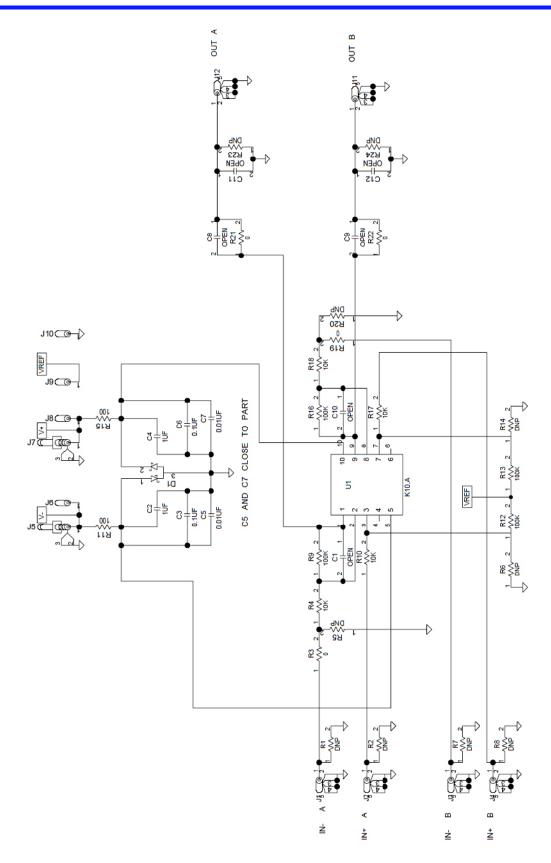


FIGURE 29. ISL70227SRH SEE TEST BOARD SCHEMATIC

FIGURE 30. ISL70227SRH SEE TEST BOARD TOP VIEW

Notice

- 1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information
- 2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples
- 3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
- 4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
- Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
 - "Standard" Computers: office equipment: communications equipment: test and measurement equipment: audio and visual equipment: home electronic appliances; machine tools; personal electronic equipment: industrial robots: etc.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc. Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.

- 6. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics oroducts outside of such specified ranges
- 7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
- 8. Plea e contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- 9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions
- 10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
- 11. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics
- 12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products
- (Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries
- (Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.4.0-1 November 2017)



Renesas Electronics Corporation

http://www.renesas.com

SALES OFFICES Refer to "http://www.renesas.com/" for the latest and detailed information Renesas Electronics America Inc. 1001 Murphy Ranch Road, Milpitas, CA 95035, U.S.A. Tel: +1-408-432-8888, Fax: +1-408-434-5351 Renesas Electronics Canada Limited 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004 Renesas Electronics Europe Limited Dukes Meadow, Miliboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tei: +44-1628-651-700, Fax: +44-1628-651-804 Renesas Electronics Europe GmbH Arcadiastrasse 10, 40472 Düsseldorf, Germar Tel: +49-211-6503-0, Fax: +49-211-6503-1327 Renesas Electronics (China) Co., Ltd. Room 1709 Quantum Plaza, No.27 ZhichunLu, Haidian District, Beijing, 100191 P. R. China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679 Renesas Electronics (Shanghai) Co., Ltd. Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, 200333 P. R. China Tel: +86-21-2226-0888, Fax: +86-21-2226-0999 Renesas Electronics Hong Kong Limited Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2265-6688, Fax: +852 2886-9022 Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670 Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300 Renesas Electronics Malaysia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amco Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Unit 1207, Block B, Menara Amcorp, Amcorp Tel: +60-3-7955-9390, Fax: +60-3-7955-9510 Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HAL 2nd Stage, Indiranagar, Bangalore 560 038, India Tel: +91-80-67208700, Fax: +91-80-67208777 Renesas Electronics Korea Co., Ltd. 17F, KAMCO Yangjae Tower, 262, Gangnam-daero, Gangnam-gu, Seoul, 06265 Korea Tei: +822-558-3737, Fax: +822-558-5338