

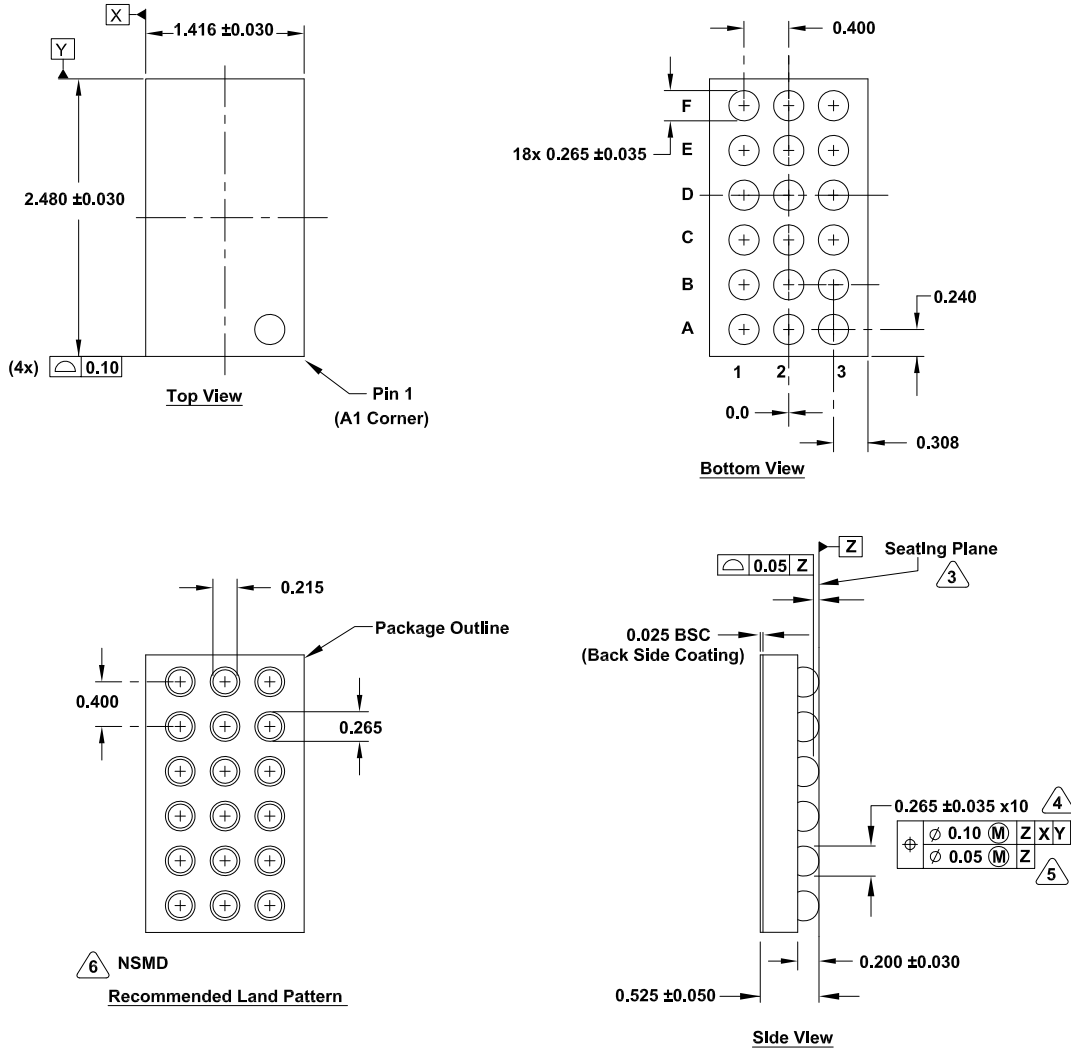
Plastic Packages for Integrated Circuits

Package Outline Drawing

W3x6.18

18 Ball Wafer Level Chip Scale Package (WLCSP 0.4mm Pitch) with BSC

Rev 0, 12/20



NOTES:

- All dimensions are in millimeters.
- Dimensions and tolerances per ASME Y14.5-1994.
- Primary datum **Z** and seating plane are defined by the spherical crowns of the bump.
- Dimension is measured at the maximum bump diameter parallel to primary datum **Z**.
- Bump position designation per JESD 95-1, SPP-010.
- NSMD refers to non-solder mask defined pad design per TB451.