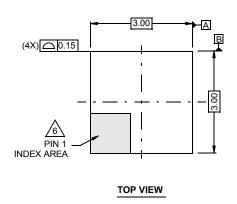
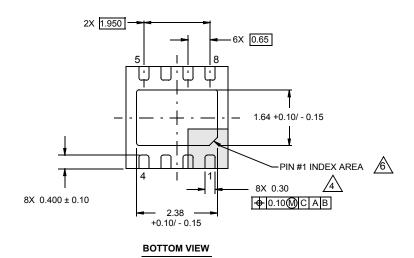
Plastic Packages for Integrated Circuits

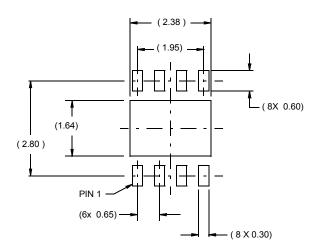
Package Outline Drawing

L8.3x3J

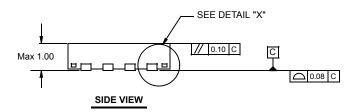
8 LEAD DUAL FLAT NO-LEAD PLASTIC PACKAGE Rev 1 3/15

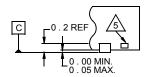






TYPICAL RECOMMENDED LAND PATTERN

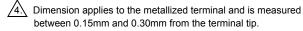




DETAIL "X"

NOTES:

- Dimensions are in millimeters.
 Dimensions in () for Reference Only.
- 2. Dimensioning and tolerancing conform to AMSE Y14.5m-1994.
- 3. Unless otherwise specified, tolerance: Decimal ± 0.05



Tiebar shown (if present) is a non-functional feature and may be located on any of the 4 sides (or ends).

The configuration of the pin #1 identifier is optional, but must be located within the zone indicated. The pin #1 identifier may be either a mold or mark feature.

