TMC DRP compliant H.264 Codec

Solution Summary

- TMC’s H.264 codec based on its original algorithm “DMNA” features high image quality and low CPU load, and it has been adopted in various camera systems, media players, etc.
- TMC has successfully developed an H264 encoder / decoder compliant to DRP which realized a half of the CPU load of the conventional one.
- TMC’s DRP compliant H.264 Codec achieves low load, low latency, and high quality images.

Features/Benefits

- Supporting H.264 codec (encoder / decoder) on ARM Cortex-A9 / Neon and DRP (Dynamically Reconfigurable Processor)
- Compliant to H.264 / AVC baseline profile (levels 1-3)
- Bit rate: up to 8Mbps
- Frame rate: up to 30fps
- Video format: YUV 4:2:2 (WVGA QVGA, VGA, …)

Graphics/ Diagrams

H.264 WVGA 30fps encoder with DRP-compliant realizes less than 50% CPU load!

RZ/A2M CPU Load

![CPU Load Diagram]

Target Markets and Applications

- Home Appliance
- Home Security
- Door Phone

https://www.tmath.co.jp/index_en.html

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**Demonstration kit to be loaned**

**Features**
- Compact (85 x 56 mm) size and convenient to carry makes easy demonstrated
- Simple connection setup for demonstration, without troublesome settings, since the software is already installed.
- CE certified kit, can be used globally

**Item list for the loaner kit**
- GR-MANGO encoder / decoder 1 ea. (SW installed)
- FPC for camera 1 piece
- HDMI conversion adapter 1 piece
- Camera 1 unit
- LAN cable (cross) 1 piece
- Manual 1 piece

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