RZ Ecosystem Partner Solution

Aurora PCB Verification Services

Solution Summary

Based on Aurora’s proprietary SI/PI verification platform that incorporates the industrial leading-edge simulation softwares, our service ensures simulation accuracy and fast turn-around time. Some RZ devices have high-speed parallel memory interfaces (DDR4/LPDDR4) and high-speed serial interfaces (USB, MIPI), Aurora’s Signal Integrity (SI) simulation services make sure your PCB design meets the RZ PCB verification guide as well as each interface’s specifications. Also, the RZ devices series has a verification guide for power-supply and Aurora’s Power Integrity (PI) simulation services verify your PCB design meets the guide.

Features/Benefits

Ensure your PCB design meets RZ Series PCB verification guide and interface specifications

- High-Speed Serial Interface Analysis – PCI Express/HDMI/MIPI/USB/Ethernet/…
- Low-Speed General Purpose I/F Analysis – I2C/SPI/I2S/…
- Power Integrity Analysis – AC/DC/Transient Analysis, Decap Optimization
- Thermal Analysis / EMI Analysis

Diagrams/Graphics

Target Markets and Applications

- IoT Applications
- Consumer Electronics
- Artificial Intelligence
- Computing Infrastructure
- Industrial Controls
- Smart Buildings
- HMI
- Communication

Aurora System Inc.

- **Company Information**
  - Established: 2016
  - Headquarter: San Jose, CA, USA
  - Development Office: USA, China
  - Sales, Support Office: USA, Japan, China
  - Taiwan, Singapore

- **Aurora System Business**
  - LSI, Package, PCB Co-simulation Service, Consulting Service, Verification Platform Development
  - High-Speed Parallel Interface Signal Integrity Analysis (DDR3/4/5, LPDDR4/4x/5)
  - High-Speed Serial Interface Signal Integrity Analysis (PCIe, MIPI, HDMI, USB, ...)
  - General I/O Signal Integrity Analysis (12C, 12S, 13C, ...)
  - Power Integrity Analysis (AC/DC/Transient Analysis)
  - Thermal / EMI Analysis

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**Simulation Service Process**

**Customer Submit**
Design Data to Aurora

**Aurora Perform Simulation**
And Verification Analysis

**Return Verification Report**
To Customer

- Support most of PCB and package design format (Cadence, Mentor, Altium, ANSYS, ...)
- BOM and stack up
- I/Os, I/Os AMI, SPICE, Package...

- DDI: DDR3/4/5, LPDDR4/4x/5
- SerDes: PCIe, MIPI, HDMI, USB
- GPO: 1x, SPI, SPMI, I2S, ...
- PON PI analysis, Optimization
- Extract SI/PI package model

- SI report
- Impedance, Delay, Skew, Eyediagram
- PI report
- DC/AC analysis, broadband model
- Package model
- RLCG and broadband model