

RZ Ecosystem Partner Solution

Aries MRZV2LS



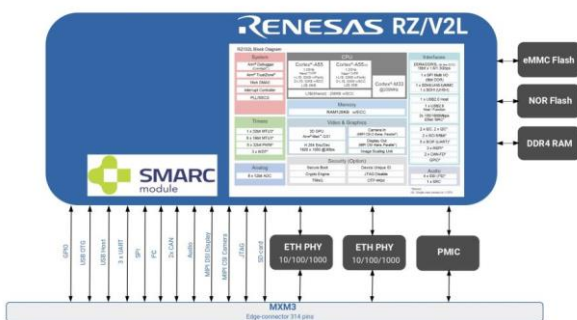
Solution Summary

The MRZV2LS is a SMARC V2.1 compliant System-on-Module developed based on Renesas RZ Family architecture offering high-performance for embedded systems. The [RZ/V2L](#) is equipped with a Cortex®-A55 (1.2GHz) CPU and a Renesas proprietary built-in AI accelerator – DRP-AI – for vision AI. The DRP-AI is configured with DRP and AI-MAC with a 16-bit DDR3L/DDR4 interface and a built-in 3D graphics engine with Arm Mali-G31 and video codec (H.264) to reduce board space and enable compact designs.

Features/Benefits

- Single or Dual Cortex-A55, up to 1GHz
- Optional AI accelerator; DRP-AI on MRZV2L
- 3D Graphics engine (Arm Mali-G31)
- Video Codec (H.264), Cortex-M33, 512MB – 4GB DDR4 RAM
- SPI NOR, 4GB-64GB eMMC NAND Flash
- Dual 10/100/1000MBit Ethernet with PHY
- USB2.0 Host/OTG, 2x CAN, UART, I2C, SPI, ADC
- Camera interface MIPI-CSI
- Display interface MIPI-DSI
- Commercial (0°C...+70°C) / industrial (-40°C...+85°C) temperature range supported

Block Diagram/Graphics



Target Markets and Applications

- IoT
- Industrial controls
- Mobile applications

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