

## Product Advisory (PA)

**Subject:** Label correction for the listed Intersil HSYE-117\* / DLA Standard Microcircuit Drawing (SMD) 5962-99547 products

**Publication Date:** 3/28/2017

**Effective Date:** 3/28/2017

### Revision Description:

Initial Release

### Description of Change:

This advisory is to inform you that Intersil has made corrections to the shipping labels for the following Intersil / DLA SMD Products:

| <u>Intersil Part Number</u> | <u>Standard Microcircuit Drawing</u> | <u>Intersil Part Number</u> | <u>Standard Microcircuit Drawing</u> |
|-----------------------------|--------------------------------------|-----------------------------|--------------------------------------|
| HSYE-117-8                  | 5962-9954703QYC                      | HSYE-117RH-8                | 5962F9954701QYC                      |
| HSYE-117EH-Q                | 5962F9954702VYC                      | HSYE-117RH-Q                | 5962F9954701VYC                      |
| HSYE-117RH/PROTO            |                                      |                             |                                      |

The update only pertains to the correction of the label, there is no change to the product.

### Reason for Change:

The labels that have been affixed to the packaging for the listed products have shown the products to be Pb-Free, this is an error. The die attach for this package uses a solder based pre-form. Label change is detailed in appendix A.

### Impact on fit, form, function, quality & reliability:

The change will have no impact on the form, fit, function, quality, or reliability of the devices.

### Product Identification:

Product affected by this change is identifiable via Intersil's internal traceability system.

**Qualification status:** Not Applicable

**Sample availability:** 3/28/2017

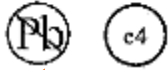
**Device material declaration:** Available upon request

*Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.*

| For additional information regarding this notice, please contact your regional change coordinator (below) |                                                                      |                                                                     |                                                                            |
|-----------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------|---------------------------------------------------------------------|----------------------------------------------------------------------------|
| Americas: <a href="mailto:PCN-US@INTERSIL.COM">PCN-US@INTERSIL.COM</a>                                    | Europe: <a href="mailto:PCN-EU@INTERSIL.COM">PCN-EU@INTERSIL.COM</a> | Japan: <a href="mailto:PCN-JP@INTERSIL.COM">PCN-JP@INTERSIL.COM</a> | Asia Pac: <a href="mailto:PCN-APAC@INTERSIL.COM">PCN-APAC@INTERSIL.COM</a> |

Appendix A:

(P)CUST P/N: 5962F9954701VYC  
(1P)INTERSIL P/N: IISYE-117RH-Q  
(1T)LOT NBR: G3W7CHF  
Product of: UNITED STATES Chips From: UNITED STATES  
(1D):1705 (2D): (Q)QTY:90 EA LLQ:90  
MSL: PBT: (V)SUPPLIER:INTERSIL



Error in label, correct labeling below.

(P)CUST P/N: 5962F9954701VYC  
(1P)INTERSIL P/N: HSYE-117RH-Q  
(1T)LOT NBR: G3W7CHF  
Product of: UNITED STATES Chips From: UNITED STATES  
(1D):1705 (2D): (Q)QTY:90 EA LLQ:90  
MSL: PBT: (V)SUPPLIER:INTERSIL

