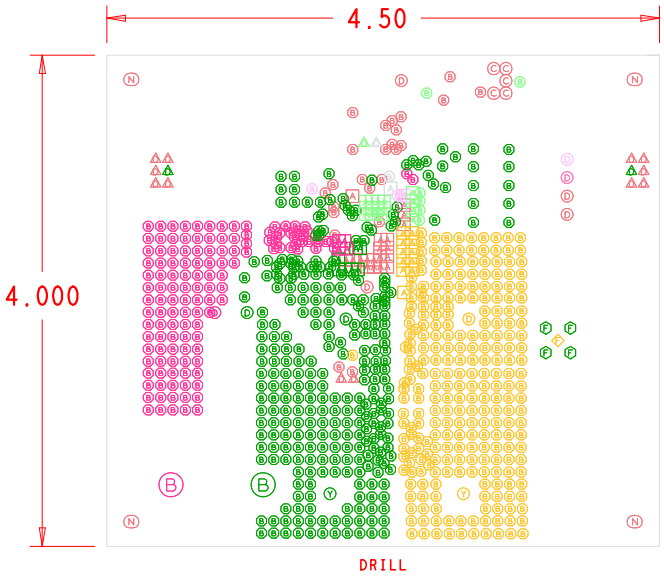
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

PHYSICAL BOARD DIMENSIONS & LAYER STRUCTURE

.062" +/- 10%

SILK TOP	silkt.art
MASK TOP	smaskt.art
COMPONENT	layer1.art
LAYER 2	layer2.art
LAYER 3	layer3.art
SOLDER	layer4.art
MASK BOTTOM	smaskb.art
SILK BOTTOM	silkb.art



DRILL

DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
Ⓑ	11.0	PLATED	710
Ⓐ	12.0	PLATED	58
Ⓒ	35.0	PLATED	5
Ⓓ	40.0	PLATED	10
△	41.0	PLATED	16
◇	63.0	PLATED	1
Ⓕ	68.0	PLATED	4
Ⓨ	200.0	PLATED	2
Ⓑ	215.0	PLATED	2
Ⓝ	125.0	NON-PLATED	4

NOTES:
1. THIS BOARD IS RoHS COMPLIANT.
2. PRINTED WIRING BOARD DESIGN AND ACCEPTANCE CRITERIA SHALL BE IAW WITH THE REQUIREMENTS OF IPC-D-275 AND IPC-A-600.
3. MATERIAL: FR4 (RoHS COMPLIANT), 2 OZ COPPER.
4. APPLY SOLDER MASK, BOTH SIDES OVER BARE COPPER IAW IPC-SM-840. CLASS 2 (LPI) (BLUE MASK).
5. ALL PATTERNS ARE VIEWED FROM THE PRIMARY SIDE LOOKING THROUGH THE BOARD.
6. UNLESS OTHERWISE SPECIFIED ALL HOLE DIAMETERS ARE AFTER PLATING.
7. APPLY SILKSCREEN USING WHITE NON-CONDUCTIVE EPOXY BASED INK.
8. PWB MUST BE 100% ELECTRICALLY TESTED FOR SHORTS AND CONTINUITY. USE NETLIST PROVIDED RTKA2108252H00000BU-B_IPC356.IPC IAW IPC-D-356.
9. MARK DATE CODE AND MANUFACTURES IDENIFICATION ON SOLDER SIDE PER IPC-6011 AND IPC-6012.
10. TOLERANCE ON ALL DRILL HOLES SHALL BE IAW IPC-D-2221 & 2222 UNLESS OTHERWISE SPECIFIED.
11. EGND AND GND_S1 ARE INTENSIONALLY SHORTED ON LAYER 1.
12. ALL 12 MIL VIA'S ARE TO BE THERMAL EPOXY FILLED AND CAPPED.

PART TO BE FREE OF BURRS AND SHARP EDGES
COMMERCIAL TOLERANCES APPLY TO STOCK SIZES
MANUFACTURING TOLERANCES PER 900002
WORKMANSHIP PER 900060
SCREW THREADS PER ANSI Y14.6
ABBREVIATIONS PER MIL-STD-12
LOGIC SYMBOLS PER IEEE STD 91
DIMENSIONING AND TOLERANCING PER ANSI Y14.5
ELECTRICAL AND ELECTRONIC DIAGRAM PER ANSI Y14.15
GRAPHIC SYMBOLS FOR ELECTRICAL AND ELECTRONIC DIAGRAM PER IEEE STD 315
ELECTRICAL AND ELECTRONIC REFERENCE DESIGNATIONS PER IEEE-STD 200
WELDING SYMBOLS PER ANSI/AWS A2.4
INTERFACE DRAWING IAW 006-STD-100

UNLESS OTHERWISE SPECIFIED DIMENSION ARE IN INCHES AND INCLUDE APPLIED FINISH TOLERANCES
2 PLACE DIM. ± .—
3 PLACE DIM. ± .—
ANGLES ± —°

CONTRACT NO.			
DRN BY 07/20/2018 Tim Klemann	CHK BY		
ENGR Vidisha Gupte	ELEC ENGR		
PROJ ENGR	MFG ENGR		
RELIABILITY	CONFIGURATION		
APPROVAL- DFTG	QA		
APPROVAL	SYS ENGR		

PRINTED WIRING BOARD, EVAL-
RTKA2108252H00000BU

SIZE	DWG NO.	REV
D	RTKA2108252H00000BU	B

SCALE 1/1 SHEET 1 OF 1