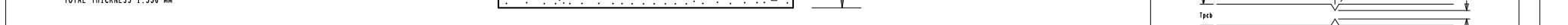
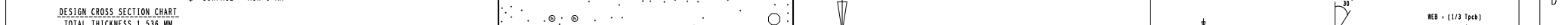


PREFERRED PANELISATION REQUIREMENTS	
REFER TO THE PANEL DRAWING IF SUPPLIED OTHERWISE USE DETAILS BELOW	

39.00 mm



	# SURFACE - AIR 0 MM L1: TOP CONDUCTOR - COPPER 0.018 MM			<table border="1"> <tr> <td>         #1 USE A 2.4 MM(±0.1mm) ROUT       </td> <td>         3.00 MM       </td> </tr> </table>	#1 USE A 2.4 MM(±0.1mm) ROUT	3.00 MM
#1 USE A 2.4 MM(±0.1mm) ROUT	3.00 MM					

**FIGURE 1**

**ALL UNITS ARE IN MILLIMETERS**

FIGURE	SIZE	PLATED	QTY
•	0.2	PLATED	411
○	1.5	PLATED	1
○	0.7	NON-PLATED	2
⊙	0.9	NON-PLATED	2
⊙	0.9	NON-PLATED	2

**MANUFACTURING SPECIFICATIONS**

**L1: DIELECTRIC - FR-4 0.36 MM**

**L2: LAYER\_2 PLANE - COPPER 0.035 MM**

**L3: LAYER\_3 CONDUCTOR - COPPER 0.035 MM**

**DIELECTRIC - FR-4 0.71 MM**

**DIELECTRIC - FR-4 0.36 MM**

**BREAK-OFF STRIP**

**CIRCUIT BOARD**

**FULL RADIUS (2 POS'NS)**

**(RATBITE) 0.50MM DRILLED HOLES(3 OFF)**

**V-SCORING**

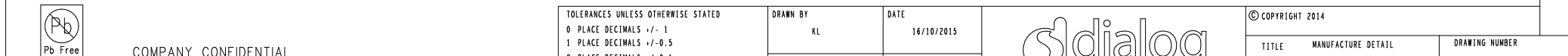
**NOT REQUIRED**

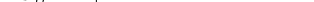
**WHEN V-SCORING IS APPLIED TO THE BOARD AS A METHOD OF REMOVING THE BREAK-OFF STRIPS THE FOLLOWING RULES ARE TO BE APPLIED**

Q1	SOLDER MASK TO EDGE CLEARANCE (0.5mm (20thru))
Q1	SOLDER MASK TO EDGE CLEARANCE (0.5mm (20thru))

DESIGN CROSS SECTION CHART  
TOTAL THICKNESS 1.536 MM

L4: BOTTOM CONDUCTOR - COPPER 0.018 MM  
SURFACE - AIR 0 MM

[illegible]

2 PLACE DECIMALS 77-0.1	CHK'D BY ??	DATE ??/??/????		??-??-????	224-14-D
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	CHN D BY ??	DATE ??/??/?????	SEMICONDUCTOR	??-??-????	Z24-14-D
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		0 PLACE DECIMALS +/- .1 1 PLACE DECIMALS +/- 0.5	KL	16/10/2015				
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Pb Free	COMPANY CONFIDENTIAL	1 PLACE DECIMALS +/-0.5 2 PLACE DECIMALS +/-0.1	CHN'D BY _____ DATE _____		TITLE _____ MANUFACTURE DETAIL _____ 11-12-1111	DRAWING NUMBER _____ 274-14-D
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		??	??/??/????	SEMICONDUCTOR	11/11/11	11/11/11
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