

Product Change Notice

(PCN tracking number : CST-R2-AJ095 Rev.1.0)

August 5, 2016

To: Valued RENESAS Customer,

Renesas Product Summary: Standard SRAM (SOP, μ TSOP and FBGA) products.

Change Description:

1. Site change of Final-test process
2. Product integration by EOL of “-5SR, -7SI, -7SR” products and by part name unification to “-5SI” product

Reason for Change:

1. Due to obsolescence of manufacturing equipment
2. For long-time, stable supply by improvement of mass production efficiency

Identification: Identifiable by RENESAS internal code, printed on the shipping label

Anticipated Impact:

1. Packing specification is changed accompanied with the site change of Final-test process
2. Electrical characteristics of “-5SI” product is completely upper-compatible with “-5SR, -7SI, -7SR” products.

Date of Change: From January 2017 onward

Schedule:

1. Regarding the site change of Final-test process (excluding EOL products),
Mass production of the post-change products from January 2017, in order of preparations.

2. Regarding the EOL of “-5SR, -7SI, -7SR” products,
Last-Time-Buy quantity forecast to RENESAS: June, 2017
Last Time to Order: December, 2017
Last Time to Ship: December, 2018
Commercial samples of unified “-5SI” product: From December, 2016

Supplemental Information: Please see the page 3 to 5 and the attachments (Appendix for CST-R2-AJ095).

Contact: General Purpose Analog and Power Solution Department 3,
General Purpose Analog and Power Business Division,
2nd Solution Business Unit

Internal Reference:

Attachments: Appendix for CST-R2-AJ095

In case of any questions, please contact your Renesas sales representative.

Customer Response (to be returned by email or mail)

- Acknowledge
- Acceptable
- Unacceptable (pls. comment)
- Not applicable

Company: _____
Name & Position: _____
Email: _____
Phone: _____

Note: Acknowledgement must be received by Renesas within 30 days of delivery of the PCN or Renesas will consider the change as approved. If timely acknowledgement is provided by Customer, then Customer shall have 90 days from the date of receipt of this PCN in which to make any objections to the PCN. If Customer fails to make objections to this PCN within 90 days of the receipt of the PCN then Renesas will consider the PCN changes as approved. If customer cannot accept the PCN then customer must provide Renesas with a last time buy demand and purchase order.

Comments

Signature of customer

1. Background of Change

Renesas announces 2 types of Product Changes on Standard SRAM (SOP, μ TSOP and FBGA) products. One is a site change of final-test process due to obsolescence of manufacturing equipment. The other is a product integration by EOL of “-5SR, -7SI, -7SR” products and by part name unification to “-5SI” product. It aims for long-time, stable supply by improvement of mass production efficiency. We greatly appreciate your kind understanding and early approval for this notification.

2. Details of Change

- (1) The site of Final-test process is transferred from “Renesas Semiconductor Beijing” to “Powertech Technology Inc.”
- (2) Regarding the EOL of “-5SR, -7SI, -7SR” products,
 - (a) “Access and Temperature grades” of “-5SR, -7SI, -7SR” in 256Kb to 4Mb Low Power SRAM products are put to EOL, and part name of the products are unified to “-5SI”.
 - (b) Electrical characteristics (DC/AC) of unified “-5SI” product are completely upper-compatible with “-5SR, -7SI, -7SR” products.
 - (c) The unified “-5SI” products are processed under the post-change condition as described in above (1).
 - (d) “Access and Temperature grades” which is laser-marked on package’s surface, is changed from “-5SR, -7SI, -7SR” to “-5SI.”
- (3) Regarding these changes,
 - (a) There are no changes in the site of Front-end (Wafer) process or revision of photomasks.
 - (b) There are no changes in the site of Assembly process or assembly materials.
 - (c) There are no changes in Reliability and quality level.
 - (d) There are no changes in Electrical characteristics (DC/AC), excluding “-5SR, -7SI, -7SR” of pre-change products.
 - (e) Packing specification is changed. For more detailed information, see the appendix for CST-R2-AJ095.

Comparison

Item		Pre Change	Post Change
Final test	Company	Renesas Semiconductor Beijing	Powertech Technology Inc.
	Country	China	Taiwan
Packing specification	Magazine packing (Tube packing)	Please see the appendix.	Please see the appendix.
	Tray packing	Please see the appendix.	Please see the appendix.
	Tape & Reel packing	Please see the appendix.	Please see the appendix.

3. Release Support and Milestones

Sample submission	<p>(1) Regarding the site change of Back-end process (excluding EOL products), we are not planning to supply samples.</p> <p>(2) Regarding the EOL of “-5SR, -7SI, -7SR” products, we will supply commercial samples of unified “-5SI” from December, 2016.</p>
Renesas report	<p>(1) Regarding the site change of Back-end process (excluding EOL products), we are not planning to submit the reliability report.</p> <p>(2) Regarding the EOL of “-5SR, -7SI, -7SR” products, we will submit the reliability report of unified “-5SI” from December, 2016.</p>

4. Identification

Identifiable by RENESAS internal code, printed on the shipping label

5. Schedule

- (1) Regarding the site change of Back-end process (excluding EOL products),
 Mass production of the post-change products from January 2017, in order of preparations.
- (2) Regarding the EOL of “-5SR, -7SI, -7SR” products,
 Last-Time-Buy quantity forecast to RENESAS: June, 2017
 Last Time to Order: December, 2017
 Last Time to Ship: December, 2018
 Commercial samples of unified “-5SI” product: From December, 2016

6. Supplemental Information

Please see the attachments (Appendix for CST-R2-AJ095).

7. Product list

Package type	Product Type (Memory Cap., Supply Voltage)	Orderable part name	
		Pre Change	Post Change
28pin-SOP	256Kb 5V	R1LP5256ESP-5SI, -5SR, -7SI, -7SR#B0 / #S0	R1LP5256ESP-5SI#B0 / #S0
	256Kb 3V	R1LV5256ESP-5SI, -5SR, -7SI, -7SR#B0 / #S0	R1LV5256ESP-5SI#B0 / #S0
32pin-SOP	1Mb 5V	R1LP0108ESN-5SI, -5SR, -7SI, -7SR#B0 / #S0	R1LP0108ESN-5SI#B0 / #S0
	1Mb 3V	R1LV0108ESN-5SI, -5SR, -7SI, -7SR#B0 / #S0	R1LV0108ESN-5SI#B0 / #S0
	4Mb 5V	R1LP0408DSP-5SI, -5SR, -7SI, -7SR#B0 / #S0	R1LP0408DSP-5SI#B0 / #S0
	4Mb 3V	RMLV0408EGSP-4S2#CA0 / #HA0	←
48ball-FBGA	4Mb 3V	RMLV0416EGBG-4S2#AC0 / #KC0	←
	8Mb 3V	RMLV0816BGBG-4S2#AC0 / #KC0	←
	16Mb 3V	R1LV1616HBG-4SI, 5SI#B0 / #S0	←
		RMLV1616AGBG-5S2#AC0 / #KC0	←
	32Mb 3V	RMWV3216AGBG-5S2#AC0 / #KC0	←
	64Mb 3V	R1WV6416RBG-5SI#B0 / #S0	←
52pin-μTSOP	8Mb 3V	RMLV0816BGSD-4S2#AC0 / #HC0	←
	16Mb 3V	RMLV1616AGSD-5S2#AC0 / #HC0	←
	32Mb 3V	R1LV3216RSD-5SI#B0 / #S0	←
	64Mb 3V	R1WV6416RSD-5SI#B0 / #S0	←