

Product Change Notice (PCN)

Subject: Electrical Specification Change to Standard Microcircuit Drawing 5962-13214 for Intersil Product ISL70444SEH*

Publication Date: 6/16/2017

Effective Date: 9/16/2017

Revision Description:

Initial Release

Description of Change:

This notice is to inform you of changes to the electrical specifications in DLA (Defense Logistics Agency) SMD (Standard Microcircuit Drawing) 5962-13214 for the listed ISL70444SEH products. The changes to the Offset Voltage (VOS), Power Supply Rejection Ratio (PSRR), and Input Offset Current (IOS) tests in table IA and associated deltas in table IIB are detailed in Appendix A (next page).

Standard microcircuit drawing	Intersil Part Number	Standard microcircuit drawing	Intersil Part Number
	ISL70444SEHF/PROTO		ISL70444SEHVFS2745
5962F1321401VXC	ISL70444SEHVF	5962F1321401V9A	ISL70444SEHVX
	ISL70444SEHVFR5757		ISL70444SEHX/SAMPLE

Reason for Change:

The change aligns the SMD with the product characteristics and is necessary to maintain product manufacturability in support of customer delivery requirements. Details regarding the change are contained on the following page. The updated SMD is available on the DLA web site at: <https://landandmaritimeapps.dla.mil/programs/smcr/default.aspx>

Product Identification:

There have been no changes to the die/silicon or product itself. There will be no change in the external marking of the packaged parts.

Qualification status: Not applicable

Sample availability: 6/16/2017

Device material declaration: Available upon request

Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.

For additional information regarding this notice, please contact your regional change coordinator (below)			
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Appendix A – SMD changes

Test	Symbol	Conditions <u>1/</u> -55°C ≤ TA ≤ +125°C VS = ±18 V unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Offset voltage	VOS	VCM = 0 V	1	01		300	μV
		VCM = +VS to -VS	1,2,3			400	
		M,D,P,L,R,F <u>2/</u>	1				
		VCM = 0 V	1	01, 02		400	
		VCM = +VS to -VS	1,2,3,			500	
		M,D,P,L,R,F <u>2/</u>	1				
Input offset current	IOS	VCM = +VS to -VS	1,2,3	01	-17	+17	nA
		M,D,P,L,R,F <u>2/</u>	1				
		VCM = +VS to -VS	1	01, 02	-30	+30	
			2,3		-50	+50	
		M,D,P,L,R,F <u>2/</u>	1		-50	+50	
Power supply rejection ratio	PSRR	VS = -18 V, +VS = -0.5 V to 18 V	1,2,3	01	88		dB
		+VS = 18 V, VS = -0.5 V to -18 V					
		M,D,P,L,R,F <u>2/</u>	1				
		-VS = -18 V, +VS = 0.5 V to 18 V, +VS = 18 V, -VS = -0.5 V to -18 V	1,2,3	01, 02	83		
		M,D,P,L,R,F <u>2/</u>	1				

Test	Symbol	Conditions <u>1/</u> -55°C ≤ TA ≤ +125°C VS = ±2.5 V unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Offset voltage	Vos	VCM = 0 V	1	01		300	μV
		VCM = +VS to -VS	1,2,3			400	
		M,D,P,L,R,F <u>2/</u>	1				
		VCM = 0 V	1	01, 02		400	
		VCM = +VS to -VS	1,2,3			500	
Input offset current	Ios	VCM = +VS to -VS	1,2,3	01	-17	+17	nA
		M,D,P,L,R,F <u>2/</u>	1				
		VCM = +VS to -VS	1	01, 02	-30	+30	
			2,3		-50	+50	
		M,D,P,L,R,F <u>2/</u>	1		-50	+50	
Power supply rejection ratio	PSRR	VS = -2.5 V, +VS = 0.5 V to 2.5 V	1,2,3	01	80		dB
		+VS = -2.5 V, VS = 0.5 V to 2.5 V					
		M,D,P,L,R,F <u>2/</u>	1				
		-VS = -2.5 V, +VS = 0.5 V to 2.5 V,	1,2	01, 02	80		
		+VS = -2.5 V, -VS = -0.5 V to -2.5 V	3		70		
		M,D,P,L,R,F <u>2/</u>	1		80		

Test	Symbol	Conditions <u>1/</u> -55°C ≤ TA ≤ +125°C VS = ±1.5 V unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Offset voltage	Vos	VCM = 0 V	1	01		300	μV
		VCM = +VS to -VS	1,2,3			400	
		M,D,P,L,R,F <u>2/</u>	1				
		VCM = 0 V	1	01, 02		400	
		VCM = +VS to -VS	1,2,3			500	
Input offset current	Ios	M,D,P,L,R,F <u>2/</u>	1				nA
		VCM = +VS to -VS	1,2,3	01	-17	+17	
		VCM = +VS to -VS	1	01, 02	-30	+30	
			2,3		-50	+50	
		M,D,P,L,R,F <u>2/</u>	1		-50	+50	

TABLE IIB. Burn-in and life test delta parameters. (TA = +25°C). 1/

Parameters	Symbol	Device types	Min	Max	Units
Offset voltage	Vos	01	-100	100	μV
		01, 02	-125	125	
Input bias current	IB	01	-10	10	nA
		01, 02	-75	75	
Supply current / amplifier	IS	01, 02	-500	500	μA

1/ Deltas are performed at room temperature.