

## Product Change Notice (PCN)

**Subject:** Electrical Specification Change to Standard Microcircuit Drawing 5962-13215 for Intersil Products ISL71590SEH\*

**Publication Date:** 1/20/2016

**Effective Date:** 1/20/2016

**Revision Description:**

Initial Release

**Description of Change:**

This notice is to inform you of changes to the electrical specifications in DLA (Defense Logistics Agency) SMD (Standard Microcircuit Drawing) 5962-13215 for the listed ISL71590SEH\* products. The Ambient temperature error and Absolute temperature error without external calibration limits have been changed as shown in Appendix A.

Intersil Part Number	Standard microcircuit drawing	Intersil Part Number	Standard microcircuit drawing	Intersil Part Number	Standard microcircuit drawing
ISL71590SEHEV1Z		ISL71590SEHMF		ISL71590SEHVX	5962F1321501V9A
ISL71590SEHF/PROTO		ISL71590SEHVF	5962F1321501VXC	ISL71590SEHX/SAMPLE	

**Reason for Change:**

The change aligns the SMD with the product characteristics and is necessary to maintain product manufacturability in support of customer delivery requirements. Details regarding the change are contained on the following page. The updated SMD is available on the DLA web site at: <http://www.landandmaritime.dla.mil/Programs/Smcr/>

**Product Identification:**

There have been no changes to the die/silicon or product itself. There will be no change in the external marking of the packaged parts.

**Qualification status:** Complete

**Sample availability:** 1/20/2016

**Device material declaration:** Available upon request

*Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.*

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: <a href="mailto:PCN-US@INTERSIL.COM">PCN-US@INTERSIL.COM</a>	Europe: <a href="mailto:PCN-EU@INTERSIL.COM">PCN-EU@INTERSIL.COM</a>	Japan: <a href="mailto:PCN-JP@INTERSIL.COM">PCN-JP@INTERSIL.COM</a>	Asia Pac: <a href="mailto:PCN-APAC@INTERSIL.COM">PCN-APAC@INTERSIL.COM</a>

Appendix A – SMD changes (see attached)

Appendix A – SMD changes

TABLE IA. Electrical performance characteristics.

Test	Symbol	Conditions <u>1/</u> -55°C ≤ T <sub>A</sub> ≤ +125°C +V <sub>S</sub> = 5 V unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Power supply voltage range	V <sub>S</sub>		1,2,3	01	4	31	V
Ambient temperature error	E		1	01	-0.5	0.5	°C
		M,D,P,L,R,F <u>1/</u>	1		-4.5 -2.0	0.5	
Absolute temperature error without external calibration	EA		2,3	01	+1.7 -2.0	+1.7 +2.0	°C