



Integrated Device Technology, Inc.
6024 Silver Creek Valley Road, San Jose, CA 95138

PRODUCT/PROCESS CHANGE NOTICE (PCN)

PCN #: **TP1912-02** Date: December 20, 2019
Product Affected: 8A35018D-000NLG
8A35018D-000NLG8
8A35018D-000NLG#
Date Effective: March 20, 2020

MEANS OF DISTINGUISHING CHANGED DEVICES:
 Product Mark
 Back Mark
 Date Code 2007 and later
 Other

Contact: Michael Rupert
E-mail: michael.rupert.jy@renesas.com

Attachment: Yes No
Samples: February 1, 2020

DESCRIPTION AND PURPOSE OF CHANGE:

- Die Technology
- Wafer Fabrication Process
- Assembly Process
- Equipment
- Material
- Testing
- Manufacturing Site
- Data Sheet
- Other - Die Revision Change

This notification is to advise our customers of improvements made to the affected part numbers.

Description of Change:

The firmware has been updated from version 5.1.3 to version 5.2.2. See the attached document titled "8A35018D Firmware Version 5.2.2 Release Notes" for descriptions of the firmware changes.

The ClockMatrix firmware is stored in the device using Read Only Memory (ROM). The firmware was updated by changes to the Via2 layer. The Via2 layer ROM changes were the only changes to the 8A35018D device.

There is no change to the orderable part number. The current version of this device will be discontinued as of the effective date on this notice. For further information please contact your local IDT sales and distribution channels.

RELIABILITY/QUALIFICATION SUMMARY:

Regression testing of the v5.2.2 firmware has been successfully completed. No change to the form or fit of the device except for the changes described in the document titled "ClockMatrix Firmware Version 5.2.2 Release Notes".

CUSTOMER ACKNOWLEDGMENT OF RECEIPT:

IDT records indicate that you require written notification of this change. Please E-Mail to grant approval or request additional information. If IDT does not receive acknowledgement within 30 days of this notice it will be assumed that this change is acceptable.