

# PRODUCT/PROCESS CHANGE NOTICE (PCN)

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PCN #: SR0102-03 DATE: 2/26. Product Affected: SRAM 1M Product Family Refer to attached list.  Manufacturing Location Affected: Salinas, California  Date Effective: 5/28/01		MEANS OF DISTING  ■ Product Mark  □ Back Mark  ■ Date Code  □ Other	STINGUISHING CHANGED DEVICE k Die revision or top mark date cod per attached change matrix.	
Contact: George Snell				
Title: Quality Assurance Manager Phone #: (831) 754-4556 Fax #: (831) 754-4672 E-mail: george.snell@idt.com		Attachment:: Samples:	Yes	□ No
L-man. <u>george.snen@idt.com</u>				
DESCRIPTION AND PURPOSE OF CHANGE:  Die Technology  Wafer Fabrication Process  Assembly Process  Equipment  Material  Testing  Manufacturing Site  Data Sheet  Other	to Salinas,	cisting qualified produ California (Fab 2) to e tached change matrix t	enhance Fab capacity	loading.
<b>RELIABILITY/QUALIFICATION SUMMARY:</b> Qualification of each product will verify that there is no	impact to c	quality or reliability of	the product.	
CUSTOMER ACKNOWLEDGMENT OF RECEING IDT records indicate that you require written notification to grant approval or request additional information. If it will be assumed that this change is acceptable. IDT reserves the right to ship either version manufactuon the earlier version has been depleted.	on of this cl IDT does no	ot receive acknowledg	ement within 30 days	of this notice
Customer:		Approval for s	hipments prior to	effective date.
Name/Date:	E-N	Mail Address:		
Title:	Pho	one# /Fax# :		
CUSTOMER COMMENTS:				
IDT ACKNOWLEDGMENT OF RECEIPT:				
RECD. BY:		DATE:		_

Integrated Device Technology, Inc. 2975 Stender Way, Santa Clara, CA - 95054

# PRODUCT/PROCESS CHANGE NOTICE (PCN)

### ATTACHMENT - PCN #: SR0102-03

**PCN Summary** 

**PCN Type:** Fab Site Change

**Commodity** Memory

Forecast or Execute Execute

Planned or Unplanned Unplanned

Data Sheet Change None Expected

#### **Detail of Change**

Transfer existing or duplicate reticle sets of qualified products to the Salinas, California Wafer Fab Facility from Hillsboro, Oregon Wafer Fab Facility.

		Current W	Current Wafer Fab		Transfer Wafer Fab			
		Manufacturing	Wafer	Die	Manufacturing	Wafer	Die	Top Mark Distinction for
Part Name	Technology	Site	Size	Revison	Site	Size	Revison	change in Wafer Fab
71024S	Cmos 9	Hillsboro, OR	8 inch	М	Salinas, CA	6 inch	L	Die Rev "L" in Datecode
71028S	Cmos 9	Hillsboro, OR	8 inch	М	Salinas, CA	6 inch	L	Die Rev "L" in Datecode
71016S	Cmos 9	Hillsboro, OR	8 inch	N	Salinas, CA	6 inch	Ν	Datecode> N 0117x
71124S	Cmos 9	Hillsboro, OR	8 inch	N	Salinas, CA	6 inch	N	Datecode> N 0117x
71128S	Cmos 9	Hillsboro, OR	8 inch	N	Salinas, CA	6 inch	Ν	Datecode> N 0117x

#### **Conversion schedule (Estimated)**

Sample Availability		<b>Production Shipments</b>		
71024S	May 14, 2001	May 28, 2001		
71028S	May 14, 2001	May 28, 2001		
71016S	May 14, 2001	May 28, 2001		
71124S	May 14, 2001	May 28, 2001		
71128S	May 14, 2001	May 28, 2001		



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# PRODUCT/PROCESS CHANGE NOTICE (PCN)

## ATTACHMENT - PCN #: SR0102-03

Qualification Plan: QS-0102-07

**Expected Completion Date** 

Test Vehicle: 71024L 4/12/2001 5/12/2001

Option : 71028L	Required Sample / # Fails	LOT #1	LOT #2
Operating Life Test: Dynamic @+135°C, Vcc=6V for 750 hours	116/0		
Operating Life Test: Static @+135°C, Vcc=6V for 750 hours	116/0		
High Temp. Storage Life Test (Unbiased, 1000 hours @+150°C)	77 / 0		
Temperature Cycling: ( -65°C to +150°C, 500 cycles)	45 / 0		
ESD Human Body Model	6/0		
ESD Charged Device Model	6/0		
Latch up: ( Tested to 2X Vcc)	10/0		

Tests are completed for unshaded areas. Product released is based on qualification of initial lot.

**Characterization Data:** Characterization will be completed as part of product Qualification

and data available upon request. Characterization will varify that there is no change to existing datasheet parameters.



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# PRODUCT/PROCESS CHANGE NOTICE (PCN)

## ATTACHMENT - PCN #: SR0102-03

Qualification Plan: QS-0102-08

**Expected Completion Date** 

Test Vehicle: 71016N 5/25/2001 6/25/2001

Option : 71124N, 71128N	Required Sample / # Fails	LOT #1	LOT #2
Operating Life Test: Dynamic @+135°C, Vcc=6V for 750 hours	116/0		
Operating Life Test: Static @+135°C, Vcc=6V for 750 hours	116/0		
High Temp. Storage Life Test (Unbiased, 1000 hours @+150°C)	77 / 0		
Temperature Cycling: ( -65°C to +150°C, 500 cycles)	45 / 0		
ESD Human Body Model	6/0		
ESD Charged Device Model	6/0		
Latch up: ( Tested to 2X Vcc)	10/0		

Tests are completed for unshaded areas. Product released is based on qualification of initial lot.

**Characterization Data:** Characterization will be completed as part of product Qualification

and data available upon request. Characterization will varify that there is no change to existing datasheet parameters.