

Integrated Device Technology, Inc. 6024 Silver Creek Valley Road, San Jose, CA - 95138

			CHANGE NUTICE (PCN)
PCN #:	N1101-02	DATE: February 1, 2011	MEANS OF DISTINGUISHING CHANGED DEVICES:
Product A			■ Product Mark ICS841S04CGIL
	841S04BGI	841S04BGILF	☐ Back Mark
Data Effect	841S04BGIT	841S04BGILFT	☐ Date Code ☐ Other
Date Effec		7 1, 2011	☐ Other
Contact:	Peter Jenkins		Au 1
Title:	Marketing		Attachment: Yes No
Phone #:	(480) 763-2048		Camplea. Available nov.
Fax #: E-mail:	(408) 763-2001 Peter.Jenkins@i	dt com	Samples: Available now
		RPOSE OF CHANGE:	
☐ Die Te			
	Fabrication Proce bly Process	I his notification is to a	dvise our customers that IDT has made a die revision on
☐ Equipn	•		ne die change will improve the production yield and reduce the
☐ Materia			tion of the device. The datasheet specifications will also be
☐ Testing		updated to reflect the c	hanges. The orderable part number have also changed.
	acturing Site		
<ul><li>Data Sl</li><li>Other</li></ul>	heet  * Die revision	an an	
KELIABI	ILITY/QUALIFI	CATION SUMMARY:	
There is n	o expected change	e to the product quality or reliability p	performance.
CUSTON	IER ACKNOWI	LEDGMENT OF RECEIPT:	
IDT recor	ds indicate that y	ou require written notification of this	change. Please use the acknowledgement below or E-Mail
			not receive acknowledgement within 90 days of this notice
		change is acceptable.	
			he process change effective date until the inventory
on the ear	lier version has b	een depleted.	
Customer	: 	[	☐ Approval for shipments prior to effective date.
			_ 11
Name/Da	te:		E-Mail Address:
Name/Da			
Title:			E-Mail Address:
Title:	IER COMMENT		E-Mail Address:



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## PRODUCT/PROCESS CHANGE NOTICE (PCN)

ATTACHMENT I - PCN # : N1101-02

**PCN Type:** Product Line

**Data Sheet Change:** Yes

**Details Of Change:** 

Changing orderable part number: From To

841S04BGI 841S04CGI 841S04BGIT 841S04CGIT 841S04BGILF 841S04CGILF 841S04BGILFT 841S04CGILFT

The die change will improve the production yield and reduce the overall power consumption of the device.

The following tables reflect the updates on the 841S04CGI rev A of the datasheet.

**Table 5A.** POWER SUPPLY DC CHARACTERISTICS,  $V_{DD-REF} = V_{DDA} = V_{DD-SRC} = 3.3V \pm 5\%$ ,  $T_A = -40$ °C TO 85°C

			FROM			ТО			
Sym	Parameter	Test Conditions	Min	Typical	Max	Min	Typical	Max	Unit
V <sub>DD_REF</sub>	Core Supply Voltage		3.135	3.3	3.465	3.135	3.3	3.465	V
V <sub>DDA</sub>	Analog Supply Voltage	-	$V_{DD\_REF}$ -0.25	3.3	$V_{DD\_REF}$	$V_{DD}$ -0.21	3.3	$V_{ m DD}$	V
V <sub>DD_SRC</sub>	Core/SRC Supply Voltage	-	3.135	3.3	3.465	-	-	-	V
I <sub>DD_REF</sub>	Crystal Supply Current	-	-	-	8	-	-	-	mA
I <sub>DD_SRC</sub>	Power Supply Current	-	-	-	160	-	-	80	mA
$I_{DD}$	Analog Supply Current	_	-	-	25	-	-	21	mA

**Table 5B.** LVCMOS/LVTTL DC CHARACTERISTICS, V<sub>DD-REF</sub> = V<sub>DD-SRC</sub> = 3.3V±5%, T<sub>A</sub> = -40°C TO 85°C

			FROM			TO			
Sym	Parameter	Test Conditions	Min	Typical	Max	Min	Typical	Max	Unit
V <sub>IHSMBUS</sub>	Input High Voltage		2.2	-	-	2.2	-	-	V
$V_{\rm ILSMBUS}$	Input Low Voltage	-	-	-	1.0	-	-	1.0	V
I <sub>IH</sub>	Input High Current	V <sub>DD</sub> =V <sub>IN</sub> =3.465V	-	-	5	-	-	10	uA
I <sub>OH</sub>	Output Current	-	-	14	-	-	-	-	uA
I <sub>OZ</sub>	High Impedance Lkg Current	-	-10	-	10	-	-	-	uA

**Table 6.** AC CHARACTERISTICS,  $\frac{V_{DD-REC}-V_{DDA}-V_{DD-SRC}}{V_{DD-SRC}} = 3.3V \pm 5\%$ ,  $T_A = -40^{\circ}$ C TO 85°C

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			FROM			ТО			
Sym	Parameter	Test Conditions	Min	Typical	Max	Min	Typical	Max	Unit
tjit(per)	Period Jitter, RMS	-	-	-	3	-	2.24	3	ps
$t_R/t_F$	SRCT/SRCC Rise/Fall	-	175	-	700	150	-	700	ps
ΔtR/tF	Rise/Fall Time Variation	V <sub>DD</sub> =V <sub>IN</sub> =3.465V	-	-	125	-	-	145	ps
$V_{HIGH}$	Voltage High	_	520	-	800	520	-	875	mV

## Sample Availability:

Samples are now available for all affected devices.

Please contact your local IDT sales representative for your sample request.