

Integrated Device Technology, Inc. 6024 Silver Creek Valley Road, San Jose, CA - 95138

PRODUCT/PROCESS CHANGE NOTICE (PCN)

PCN #: A1808-02	DATE: 5-Sep-201	8 MEANS OF DISTINGUISHING CHANGED DEVICES:						
Product Affected: 4RCD0229KB1ATC		Product Mark						
4RCD0229KB1ATC	8	■ Back Mark Assembly lot# will have a suffix "E"						
4RCD0229KB1ATC	/M	Date Code						
4RCD0229KB1ATC	8/M	□ Other						
4RCD0229KB1ATC	8/B							
Date Effective: 5-Dec-2018								
Contact: IDT PCN DESK		Attachment: Yes No						
E-mail: pcndesk@idt.com	Didt.com Samples: Please contact your local sales representative for sample request & availability.							
DESCRIPTION AND PURPOSE OF CH	IANGE:							
 Die Technology Wafer Fabrication Process Assembly Process This notice is to advise our customers that IDT has successfully qualified an alternat substrate material, ETS for the select products. 								
EquipmentMaterial								
TestingManufacturing Site	□ Testing Attachment I details the qualification data for this change.							
□ Data Sheet								
□ Other								
RELIABILITY/QUALIFICATION SUM	IMARY:							
Refer to qualification data shown in attach								
CUSTOMER ACKNOWLEDGMENT)F RECEIPT:							
IDT records indicate that you require written notification of this change. Please use the acknowledgement below or E-Mail to grant approval or request additional information. If IDT does not receive acknowledgement within 30 days of this notice								
it will be assumed that this change is accept		not receive deknowledgement within 50 days of this notice						
IDT reserves the right to ship either version manufactured after the process change effective date until the inventory								
on the earlier version has been depleted.								
Customer:		Approval for shipments prior to effective date.						
Name/Date:		E-Mail Address:						
Title:		hone# /Fax# :						
CUSTOMER COMMENTS:								
IDT ACKNOWLEDGMENT OF RECE	IPT:							
RECD. BY:		DATE:						



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ATTACHMENT I - PCN # : A1808-02

PCN Type:	Adding Alternate Materials			
Data Sheet Change:	None			
	No change in moisture sensitivity level (MSL)			

Detail Of Change:

This notice is to advise our customers that IDT has successfully qualified an alternate substrate material, ETS for the select products. Refer Table 1 for comparizon of the existing substrate and the new alternate substrate.

There is no change to the moisture performance of this package.

Description	Current Substrate	Alternate Substrate		
Substrate Structure	6 Layers MSAP	4 Layers ETS		
Bottom Solder Mask Layer	AUS320	SR1		
Layer 1-2	GHPL830NS	GHPL830NS		
Core Layer	HL832NS	No Core		
Layer 3-4	GHPL830NS	GHPL830NS		
Bottom Solder Mask Layer	AUS320	SR1		

Table 1: Substrate Material Comparizon



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Qualification Information and Qualification Data:

Affected Packages: FCCSP-253

Qual Plan & Results: Tests are in accordance with JEDEC47 recommended tests.

Qualification Vehicle: FCCSP-253 (3 lots)

		Test Results (Rej / SS)		
Test Description	Test Method	Lot 1	Lot 2	Lot 3
* Temperature Cycling (-55°C to 125°C, 700 cycles)	JESD22-A104	0/25	0/25	0/25
* HAST - biased (130 °C/85% RH, 96 Hrs)	JESD22-A110	0/25	0/25	0/25
High Temperature Storage Bake (150°C, 1000 Hrs)	JESD22-A103	0/25	0/25	0/25
Solder Ball Shear Test	JESD22-B117	0/5	0/5	0/5
Moisture Sensitivity Level, MSL	J-STD-20 / MSL 3, 260 °C	0/25	0/25	0/25

* Tests were subjected to Preconditioning per JESD22-A113 prior to stress test