

## Product Change Notice (PCN)

**Subject:** Datasheet Parameter Change for Select RFD Devices

**Publication Date:** 8/9/2021

**Effective Date:** 11/9/2021

**Revision Description:**

Initial Release

**Description of Change:**

Renesas has made a minor change to the datasheet parameter, Logic Input Low Threshold from Max: 0.63V to Max: 0.55V for test yield improvement.

This is an update of the datasheet only, no change of the silicon design, die revision, circuitry, process or device performance.

The updated datasheets are accessible via Renesas website.

**Affected Product List:** Refer Appendix A.

**Reason for Change:**

The change is for improved test yield.

**Impact on Fit, Form, Function, Quality & Reliability:**

The change will have no impact on the form, fit, function, quality, reliability and environmental compliance of the products.

**Product Identification:**

Datecode 2147 and above.

**Qualification Status:** Not applicable

**Sample Availability Date:** Not applicable

**Device Material Declaration:** Available upon request

Note:

1. Acknowledgement must be received by Renesas within 30 days or Renesas will consider the change as approved.
2. If timely acknowledgement is provided by Customer, then Customer shall have 90 days from the date of receipt of this PCN to make any objections to this PCN. If Customer fails to make objections to this PCN within 90 days of the receipt of the PCN then Renesas will consider the PCN changes as approved.
3. If customer cannot accept the PCN then customer must provide Renesas with a last time buy demand and purchase order.

**For additional information regarding this notice, please contact [idt-pcn@lm.renesas.com](mailto:idt-pcn@lm.renesas.com)**

**Appendix A – Affected Product List**

F0452BLEGK	F0452CLFGK8	F0453BLEGK	F0453CLFGK8
F0452BLEGK8	F0452LEGK	F0453BLEGK8	F0453LEGK
F0452CLFGK	F0452LEGK8	F0453CLFGK	F0453LEGK8