

Product Change Notice (PCN)

Subject: Assembly & sorting factory addition for RL78/I1B,I1C LFQFP package products.

Publication Date: 2/2/2024

Effective Date: 5/1/2024

Revision Description:

Initial release

Description of Change:

Group name : RL78/I1B, I1C

Target package : 12mm×12mm 0.5mm pitch 80pin LFQFP

- 1) Additional back-end factory: Renesas Semiconductor KL Sdn. Bhd. (KL)

manufacturing factory

Before			After		
Wafer	ASSY	Sorting/packing	Wafer	ASSY	Sorting/packing
Kawashiri	BJ	BJ	Kawashiri	BJ	BJ
	Greatek	KYEC		Greatek	KYEC
	ASEKH	KYEC		ASEKH	KYEC
	ASEKH	BJ		ASEKH	BJ
	-	-		KL	KL

Note1: “Renesas Semiconductor (Beijing) Co.,Ltd (BJ)”

“ADVANCED SEMICONDUCTOR ENGINEERING, INC. (ASEKH) ”

“Greatek Electronics Inc. (Greatek)”

“King Yuan Electronics Co., Ltd (KYEC) ”

Note2: “KL: Factory addition”

- 2) Assembly material
Use materials certified by additional factory.
- 3) Package outline
There is no change in footprint for additional factory products.
- 4) Marking
The number of characters in the lot number and the marking font are changed.
- 5) Storage conditions after opening the moisture proof packaging.
KL products have the same conditions as BJ products.
“30°C/70%RH/ within 168hr”

Affected Product List:

R5F10MMEDFB#10	R5F10NMEDFB#10	R5F10NMGDFB#50
R5F10MMEDFB#50	R5F10NMEDFB#15	R5F10NMGDFB#55
R5F10MMGDFB#10	R5F10NMEDFB#50	R5F10NMJDFB#10
R5F10MMGDFB#50	R5F10NMEDFB#55	R5F10NMJDFB#15
R5F10MMEDH99FB#10	R5F10NMGDFB#10	R5F10NMJDFB#50
R5F10MMEDH99FB#50	R5F10NMGDFB#15	R5F10NMJDFB#55

Reason for Change:

Stable supply for target group products.

Impact on Fit, Form, Function, Quality & Reliability:

Impact on Fit : No Impact

Form : Please refer to “EP20-AB-24-0010_RL78_LFQFP_KL_Difference specification”
for detail.

Function : No Impact

Quality : No Impact

Reliability : No Impact

Product Identification:

Possible to confirm the production history data from the packing label or trace code.

Qualification Status: We will prepare by 5/1/2024

Sample Availability Date: 5/1/2024

PCN sample is a representative ES sample.

The ES sample has the same functionality as the mass-produced product and its sample is the representative (ROM/RAM capacity, Fields of application and Wafer process).

Differences from MP products: The sorting place is different (the test program is the same).

Device Material Declaration: Contact Renesas sales, distributor, or agency.

Note:

1. Acknowledgement must be received by Renesas within 30 days or Renesas will consider the change as approved.
2. If timely acknowledgement is provided by Customer, then Customer shall have 90 days from the date of receipt of this PCN to make any objections to this PCN. If Customer fails to make objections to this PCN within 90 days of the receipt of the PCN then Renesas will consider the PCN changes as approved.
3. If customer cannot accept the PCN then customer must provide Renesas with a last time buy demand and purchase order.

For additional information regarding this notice, please contact your Renesas sales representative.

DIFFERENCE OF SPECIFICATION 12x12mm 0.5mm pitch 80pin LFQFP

Assembly factory: KL Sorting factory: KL

EP2 OPERATIONS STRATEGY DEPARTMENT
EMBEDDED PROCESSING 2ND BUSINESS DIVISION
EMBEDDED PROCESSING PRODUCT GROUP
RENESAS ELECTRONICS CORPORATION.

Ver.1.0

EP2O-AB-24-0010

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
 2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
 3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
 4. You shall be responsible for determining what licenses are required from any third parties, and obtaining such licenses for the lawful import, export, manufacture, sales, utilization, distribution or other disposal of any products incorporating Renesas Electronics products, if required.
 5. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
 6. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
 - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
 - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.

Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.
 7. No semiconductor product is absolutely secure. Notwithstanding any security measures or features that may be implemented in Renesas Electronics hardware or software products, Renesas Electronics shall have absolutely no liability arising out of any vulnerability or security breach, including but not limited to any unauthorized access to or use of a Renesas Electronics product or a system that uses a Renesas Electronics product. RENESAS ELECTRONICS DOES NOT WARRANT OR GUARANTEE THAT RENESAS ELECTRONICS PRODUCTS, OR ANY SYSTEMS CREATED USING RENESAS ELECTRONICS PRODUCTS WILL BE INVULNERABLE OR FREE FROM CORRUPTION, ATTACK, VIRUSES, INTERFERENCE, HACKING, DATA LOSS OR THEFT, OR OTHER SECURITY INTRUSION ("Vulnerability Issues"). RENESAS ELECTRONICS DISCLAIMS ANY AND ALL RESPONSIBILITY OR LIABILITY ARISING FROM OR RELATED TO ANY VULNERABILITY ISSUES. FURTHERMORE, TO THE EXTENT PERMITTED BY APPLICABLE LAW, RENESAS ELECTRONICS DISCLAIMS ANY AND ALL WARRANTIES, EXPRESS OR IMPLIED, WITH RESPECT TO THIS DOCUMENT AND ANY RELATED OR ACCOMPANYING SOFTWARE OR HARDWARE, INCLUDING BUT NOT LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY, OR FITNESS FOR A PARTICULAR PURPOSE.
 8. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
 9. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
 10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
 11. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
 12. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
 13. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
 14. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.
- (Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.
 (Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev. 5.0-1 October 2020)

DIFFERENCE OUTLINE

- Target package

12x12mm 0.5mm pitch 80pin LFQFP

- Difference points

- 1) Assembly factory

Existing factory: Renesas Semiconductor (Beijing) Co.,Ltd (BJ)

Existing factory: ADVANCED SEMICONDUCTOR ENGINEERING, INC. (ASEKH)

Existing factory: Greatek Electronics Inc. (Greatek)

Additional factory: Renesas Semiconductor KL Sdn. Bhd. (KL)

- 2) Sorting & Packing factory

Existing factory: Renesas Semiconductor (Beijing) Co.,Ltd (BJ)

Existing factory: King Yuan Electronics Co., Ltd (Kyec)

Additional factory: Renesas Semiconductor KL Sdn. Bhd. (KL)

DIFFERENCE OUTLINE

3) Assembly material

Use materials certified by additional factory.

4) Package outline

There is no change in footprint for additional factory products.

Please refer to the package outline drawing and dimension comparison for the external dimensions.

5) Marking

The number of characters in the lot number and the marking font are changed.

6) Storage conditions after opening the moisture proof packaging.

KL products have the same conditions as BJ products.

30°C/70%RH/ within 168hr

DIFFERENCE OUTLINE

7) Specification and characteristics of product:

No impact

8) Quality and reliability:

No impact

DIFFERENCE OF SPECIFICATION

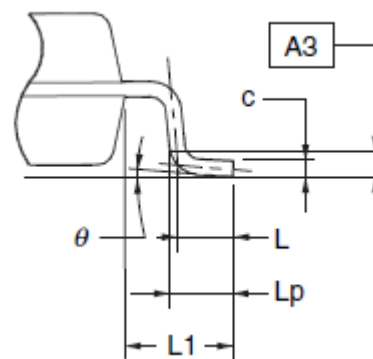
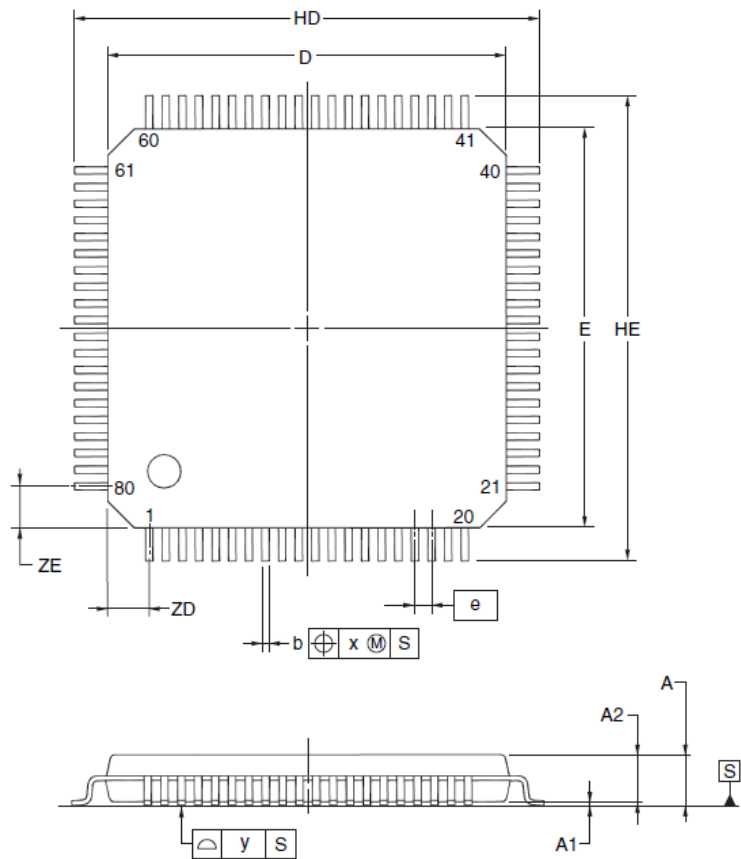
Item		Additional factory	Existing factory	Existing factory	Existing factory
Assembly factory		KL	BJ	Greatek	ASEKH
Sorting factory		KL	BJ	KYEC	BJ or KYEC
Package	Outline	There are differences (Refer to pages 7 to 10)			
Lead frame	Material	No change			
	Inner pattern	Refer to outline drawing (pages 11)			
Die mount	Material	Ag epoxy paste D *	Ag epoxy paste A *	Ag epoxy paste B *	Ag epoxy paste C *
Bonding wire	Material	No change: Cu (Pd coating)			
Resin	Material	Epoxy resin D * (halogen-free)	Epoxy resin A * (halogen-free)	Epoxy resin B * (halogen-free)	Epoxy resin C * (halogen-free)
Plating	Material	No change			
Marking		There are differences (Refer to pages 12 to 13)			
Packing	Tray/ Emboss tape	No change			
Storage conditions	after opening	30°C/70%RH/ within 168hr		30°C/60%RH/ within 168hr (JEDEC standard)	

* Factory certified materials.

There are differences in materials, but there is no change in reliability or characteristics.

12mm×12mm 0.5mm pitch 80pin LFQFP Package outline (KL)

RENESAS Code : PLQP0080KE-A

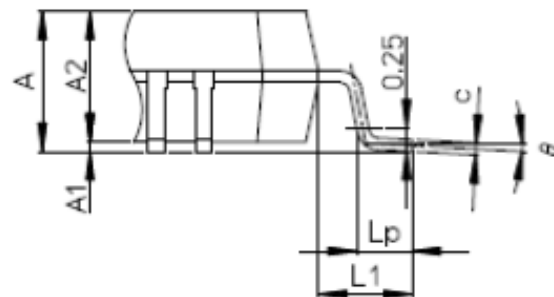
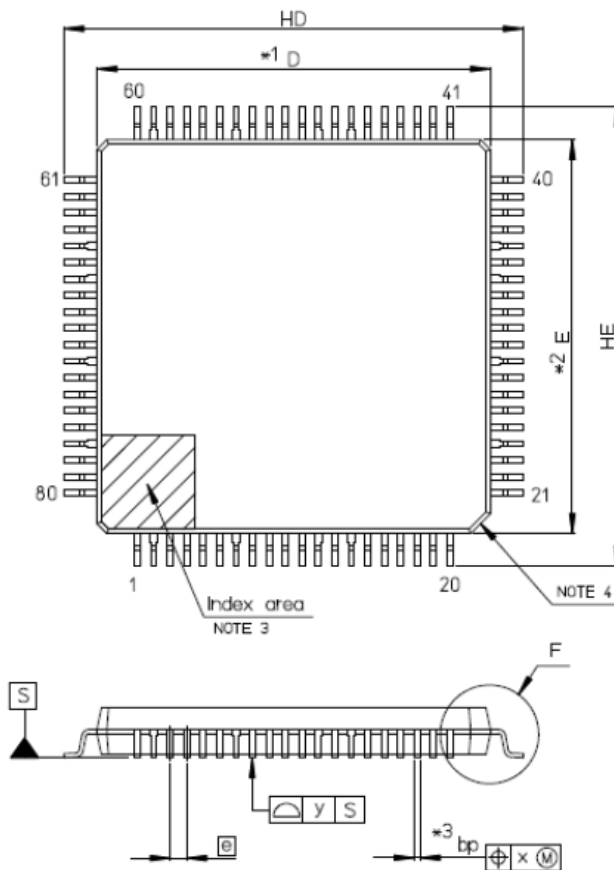


(UNIT:mm)

ITEM	DIMENSIONS
D	12.00±0.20
E	12.00±0.20
HD	14.00±0.20
HE	14.00±0.20
A	1.60 MAX.
A1	0.10±0.05
A2	1.40±0.05
A3	0.25
b	0.22±0.05
c	0.145 ^{+0.055} _{-0.045}
L	0.50
Lp	0.60±0.15
L1	1.00±0.20
θ	3°+5° _{-3°}
e	0.50
x	0.08
y	0.08
ZD	1.25
ZE	1.25

12mm×12mm 0.5mm pitch 80pin LFQFP Package outline (BJ/ASEKH)

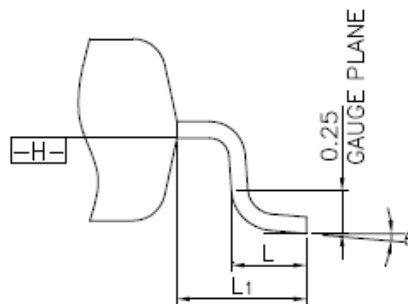
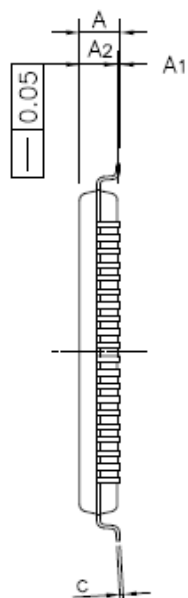
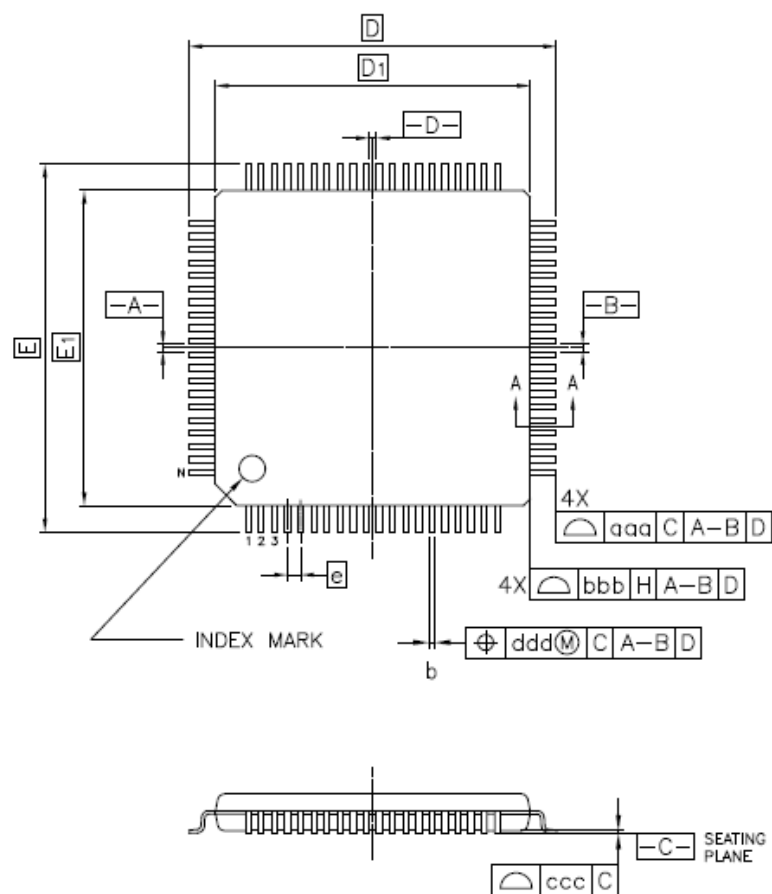
RENESAS Code : PLQP0080KB-B



Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	11.9	12.0	12.1
E	11.9	12.0	12.1
A2	—	1.4	—
HD	13.8	14.0	14.2
HE	13.8	14.0	14.2
A	—	—	1.7
A1	0.05	—	0.15
bp	0.15	0.20	0.27
c	0.09	—	0.20
θ	0°	3.5°	8°
e	—	0.5	—
x	—	—	0.08
y	—	—	0.08
Lp	0.45	0.6	0.75
L1	—	1.0	—

12mm×12mm 0.5mm pitch 80pin LFQFP Package outline (Greatek)

RENESAS Code : PLQP0080KJ-A



Reference Symbol	Dimension in Millimeters		
	Min.	Norm.	Max.
A	—	—	1.60
A ₁	0.05	—	0.15
A ₂	1.35	1.40	1.45
D	—	14.00	—
D ₁	—	12.00	—
E	—	14.00	—
E ₁	—	12.00	—
N	—	80	—
e	—	0.50	—
b	0.17	0.22	0.27
c	0.09	—	0.20
θ	0°	3.5°	7°
L	0.45	0.60	0.75
L ₁	—	1.00	—
aaa	—	—	0.20
bbb	—	—	0.20
ccc	—	—	0.08
ddd	—	—	0.08

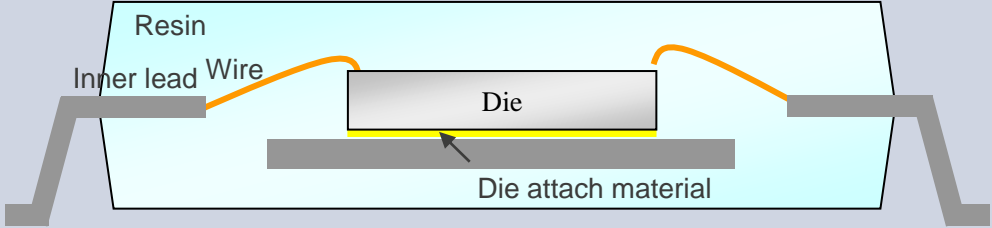

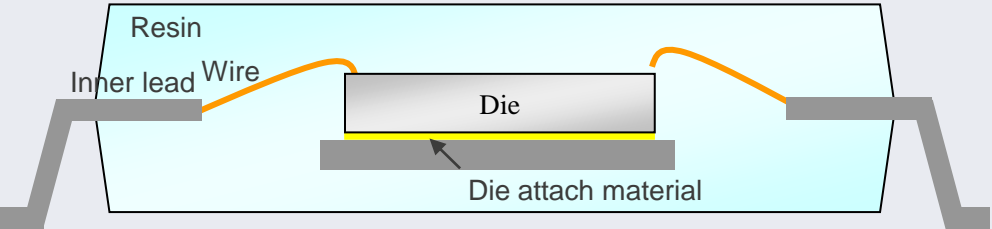

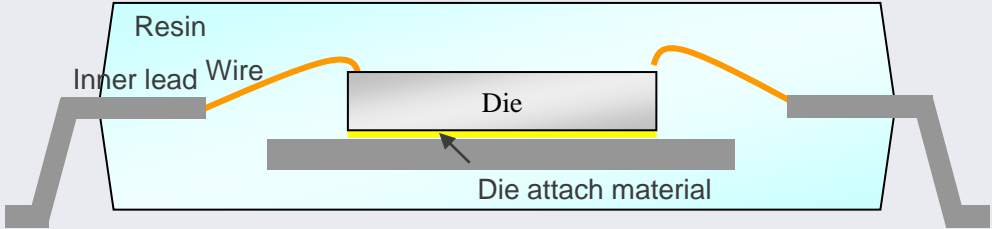

Dimension comparison: 12mm x 12mm 0.5mm pitch 80pin LFQFP

KL,BJ package symbols
complied to JEITA standard,
and Greatek package symbols
complied to JEDEC standard.

KL Symbol	12x12mm 80pin LFQFP PLQP0080KE-A			BJ ASEKH Symbol	12x12mm 80pin LFQFP PLQP0080KB-B			Greatek Symbol	12x12mm 80pin LFQFP PLQP0080KJ-A		
	Dimension in Millimeters				Dimension in Millimeters				Dimension in Millimeters		
	Min	Nom	Max		Min	Nom	Max		Min	Nom	Max
A	-	-	1.60	A	-	-	1.70	A	-	-	1.60
A1	0.05	0.10	0.15	A1	0.05	-	0.15	A1	0.05	-	0.15
A2	1.35	1.40	1.45	A2	-	1.40	-	A2	1.35	1.40	1.45
HD	13.80	14.00	14.20	HD	13.80	14.00	14.20	D	-	14.00	-
D	11.80	12.00	12.20	D	11.90	12.00	12.10	D1	-	12.00	-
HE	13.80	14.00	14.20	HE	13.80	14.00	14.20	E	-	14.00	-
E	11.80	12.00	12.20	E	11.90	12.00	12.10	E1	-	12.00	-
-	-	-	-	-	-	-	-	N	-	80	-
e	-	0.50	-	e	-	0.50	-	e	-	0.50	-
b	0.17	0.22	0.27	bp	0.15	0.20	0.27	b	0.17	0.22	0.27
c	0.10	0.145	0.20	c	0.09	-	0.20	c	0.09	-	0.20
θ	0°	3.0°	8°	θ	0°	3.5°	8°	θ	0°	3.5°	7°
Lp	0.45	0.60	0.75	Lp	0.45	0.60	0.75	L	0.45	0.60	0.75
L1	0.80	1.00	1.20	L1	-	1.00	-	L1	-	1.00	-
-	-	-	-	-	-	-	-	aaa	-	-	0.20
-	-	-	-	-	-	-	-	bbb	-	-	0.20
y	-	-	0.08	y	-	-	0.08	ccc	-	-	0.08
x	-	-	0.08	x	-	-	0.08	ddd	-	-	0.08

Package structure image

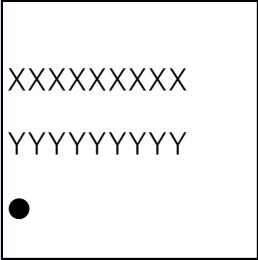
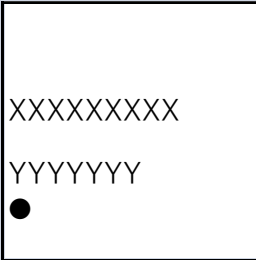
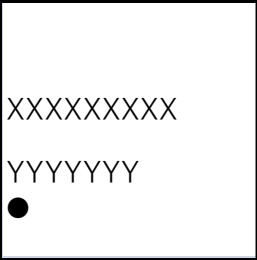
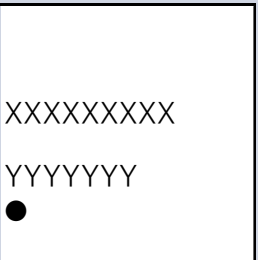
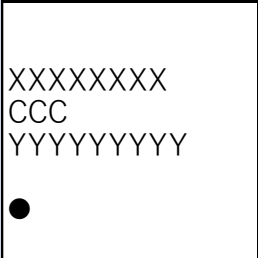
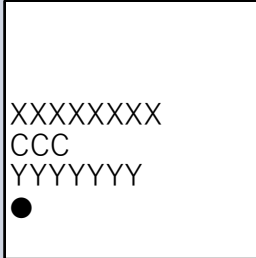
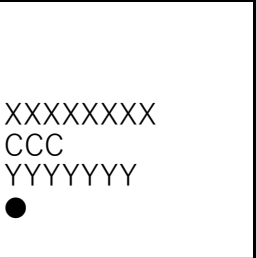
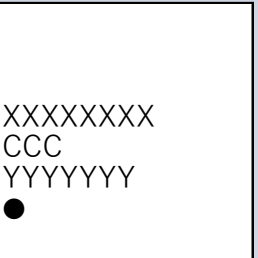
Package Section and die pad shape is a reference example.

Assembly factory	PKG cross section	Die pad shape
Additional factory		 KL
Existing factory		 BJ
		 Greatek / ASEKH

There is no impact on the reliability by die pad shape.









12x12mm 0.5mm pitch 80pin LFQFP marking specifications

Marking position is reference example.

Assembly factory	KL (Additional factory)	BJ (Existing factory)	Greatek (Existing factory)	ASEKH (Existing factory)
Blank products				
	1st row 9 characters: product name 2nd row - 3rd row 9 characters: Lot № 4th row -	1st row - 2nd row 9 characters: product name 3rd row - 4th row 7 characters: Lot №	1st row - 2nd row 9 characters: product name 3rd row - 4th row 7 characters: Lot №	1st row - 2nd row 9 characters: product name 3rd row - 4th row 7 characters: Lot №
ROM products				
	1st row 8 characters: product name 2nd row 3 characters: ROM code 3rd row 9 characters: Lot № 4th row -	1st row - 2nd row 8 characters: product name 3rd row 3 characters: ROM code 4th row 7 characters: Lot №	1st row - 2nd row 8 characters: product name 3rd row 3 characters: ROM code 4th row 7 characters: Lot №	1st row - 2nd row 8 characters: product name 3rd row 3 characters: ROM code 4th row 7 characters: Lot №

Marking visibility

Marking position and character is reference example.

Assembly factory	KL (Additional factory)	BJ (Existing factory)	Greatek (Existing factory)	ASEKH (Existing factory)
Overall photo	 Overall photo of a square, dark green integrated circuit (IC) chip mounted on a green printed circuit board (PCB). The chip has four circular solder pads at the corners and a dense array of gold wire bonds around the perimeter. The marking "R5F10RLGA" and "2141IME51" is visible in the center.	 Overall photo of a square, dark green integrated circuit (IC) chip mounted on a green printed circuit board (PCB). The chip has four circular solder pads at the corners and a dense array of gold wire bonds around the perimeter. The marking "R5F100LGA" and "406KZ00" is visible in the center.	 Overall photo of a square, dark green integrated circuit (IC) chip mounted on a green printed circuit board (PCB). The chip has four circular solder pads at the corners and a dense array of gold wire bonds around the perimeter. The marking "R5F104LJA" and "1348901" is visible in the center.	 Overall photo of a square, dark green integrated circuit (IC) chip mounted on a green printed circuit board (PCB). The chip has four circular solder pads at the corners and a dense array of gold wire bonds around the perimeter. The marking "R5F10WLGA" and "540LP00" is visible in the center.
Enlarged photo	 Enlarged photo of the KL chip marking, showing the characters "R5F" in a light, slightly faded font against the dark green background of the chip.	 Enlarged photo of the BJ chip marking, showing the characters "R5F" in a light, slightly faded font against the dark green background of the chip.	 Enlarged photo of the Greatek chip marking, showing the characters "R5F" in a light, slightly faded font against the dark green background of the chip.	 Enlarged photo of the ASEKH chip marking, showing the characters "R5F" in a light, slightly faded font against the dark green background of the chip.

Actual colors may be different from ones in the photo.

4M changing points

(Addition of assembly and sorting factory , Change of material)

Item	Check Result	Judgement
Machine	Changing at assembly and sorting. The machines are equivalent to present machines. There are production of similar products and we have already checked the additional products have no risk on the production.	No risk
Method	The same as current products.	No risk
Man	Using operator certification system. Only certificated operator can work for the production.	No risk
Material	Only use certificated materials. The products has been certificated by reliability test same as existing products and have no risk.	No risk

[Renesas.com](https://www.renesas.com)