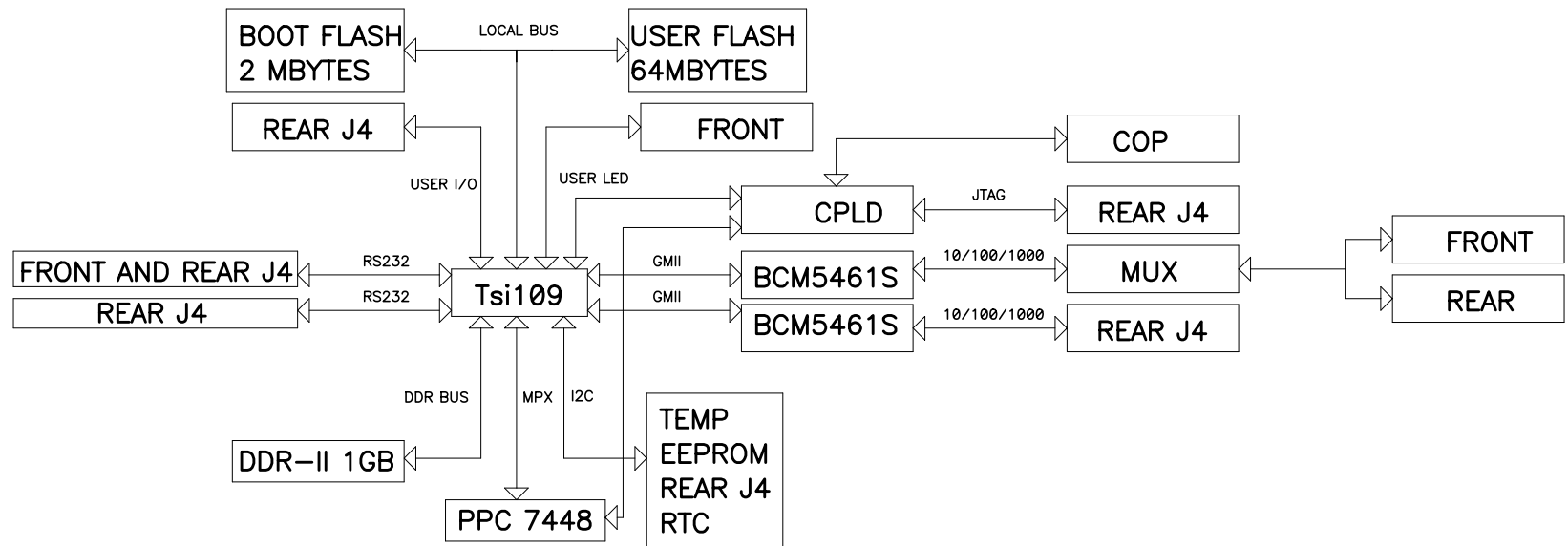
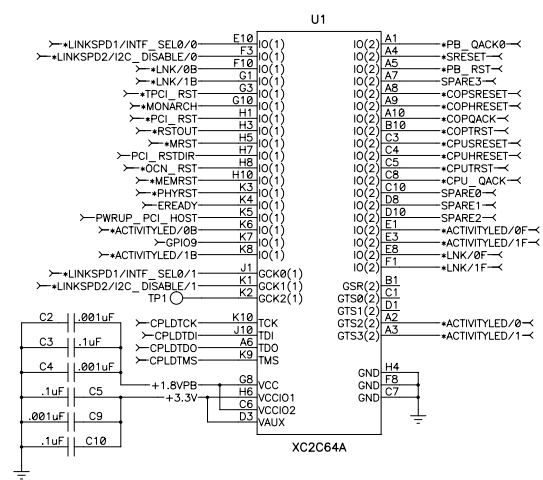
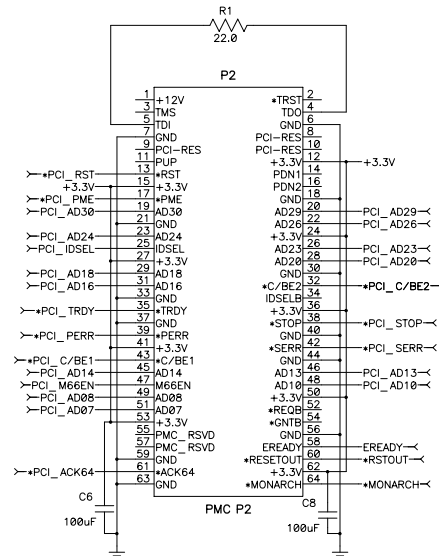
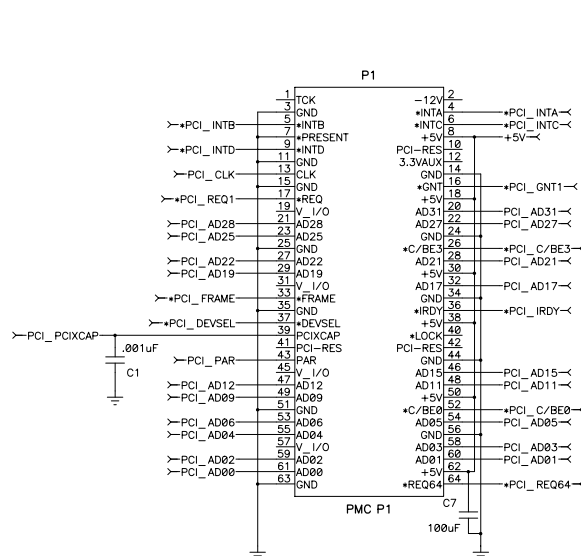
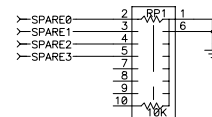


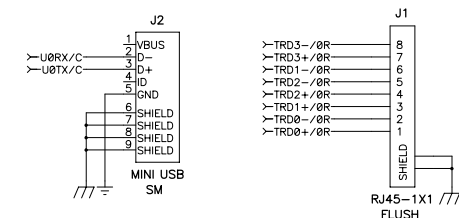
BLOCK DIAGRAM



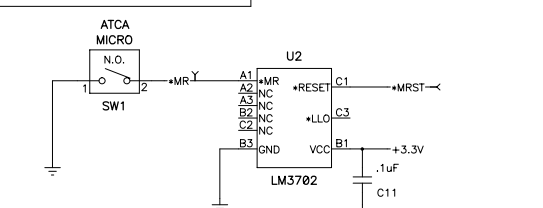
MATCH LENGTH WITH IN THE PCI @ 500 MIL
DO NOT INCLUDE THESE SIGNALS IN THE LIST
PCI_PCIXCAP; *PCI_INTA; *PCI_INTB; *PCI_INTC; *PCI_INTD;
*PCI_RST; PCI_M66EN; *MONARCH; *RSTOUT; EREADY



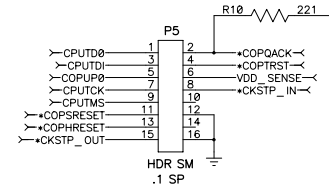
FRONT PANEL



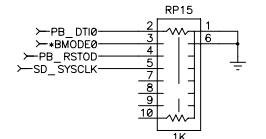
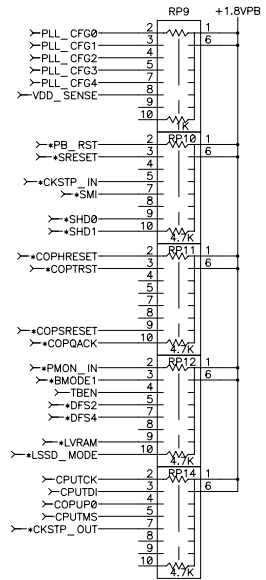
BACK OF THE BOARD



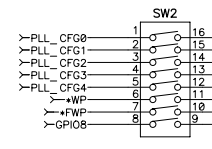
FOLLOW THE APP NOTE 2748 FROM FREESCALE FOR THE POWER LAYOUT AND ESCAPE PATTERN RECOMMENDATIONS



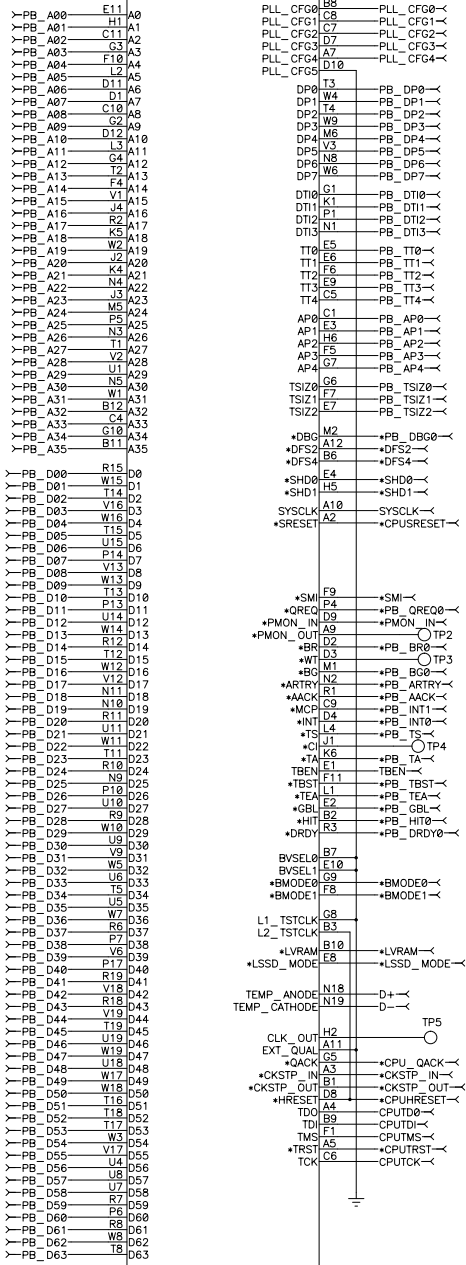
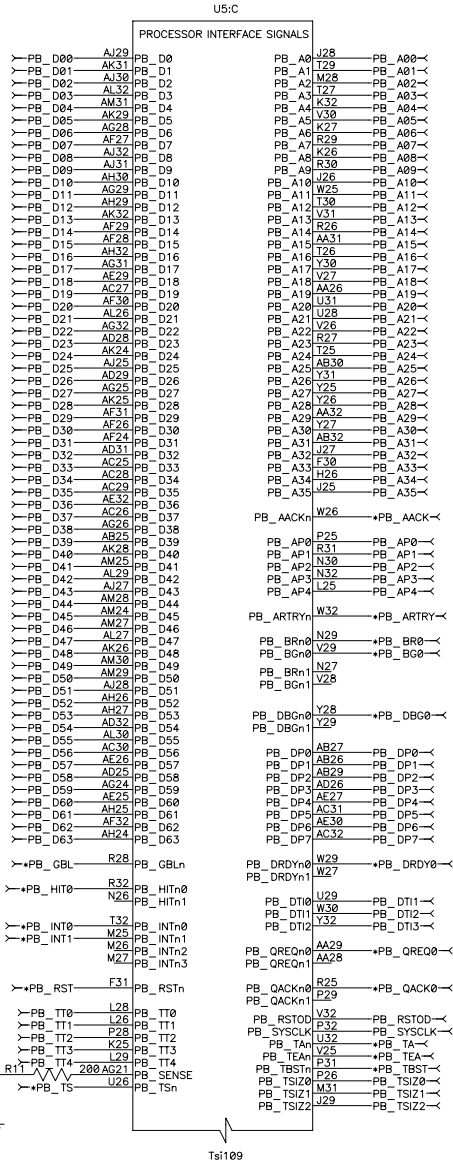
OPTION LOAD
BACK OF THE BOARD



SYSCLK RUNS AT 200MHZ
TABLE 13 ON THE MPC7448 SHOWS
THE VALID PLL CFG SETTINGS



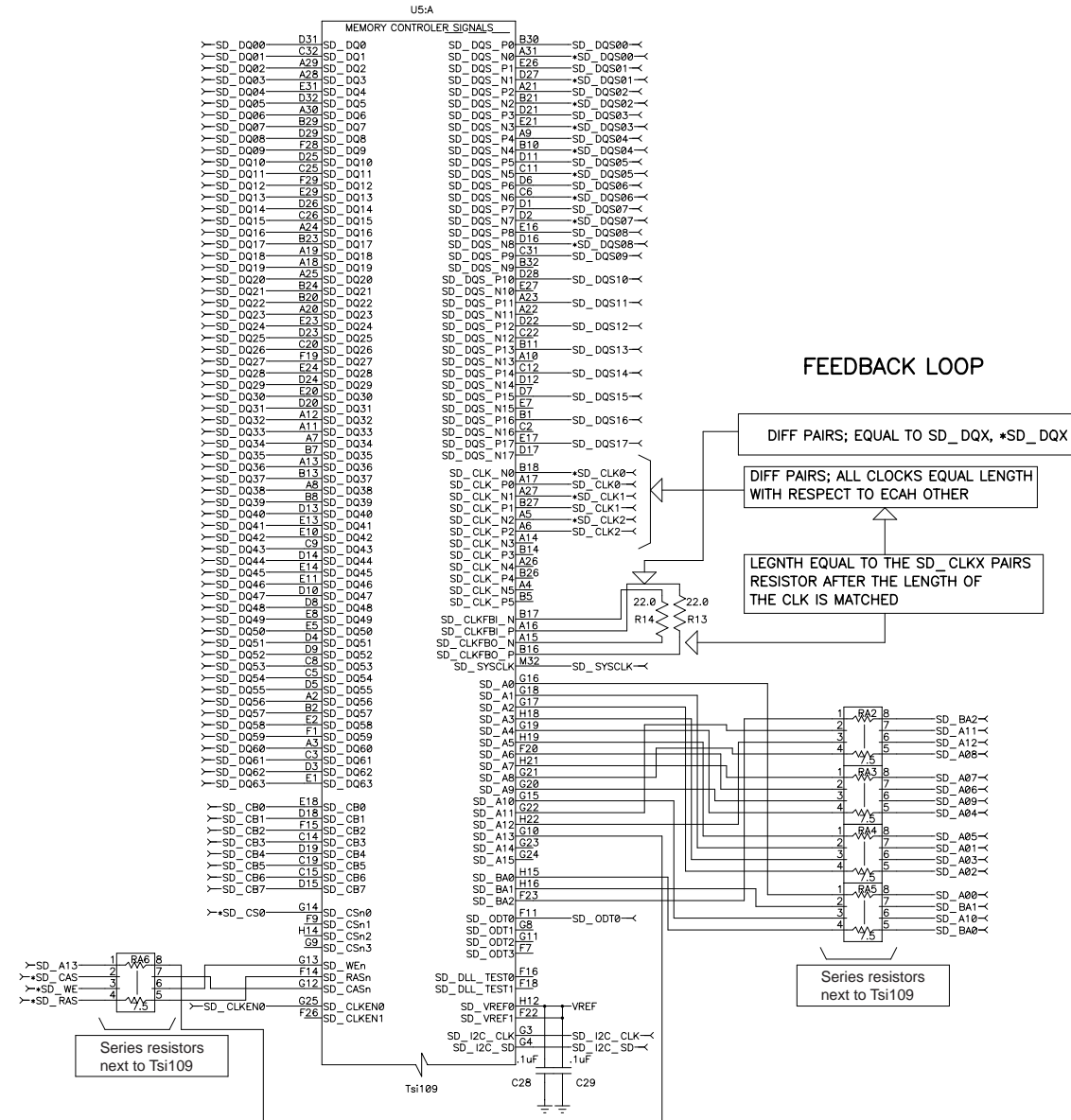
*WP	CLOSED WRITE PROTECT THE BOOT FLASH
*FWP	CLOSED WRITE PROTECT THE DATA FLASH
GPIOB	USER DEFINED; CLOSED LOW; OPEN HIGH

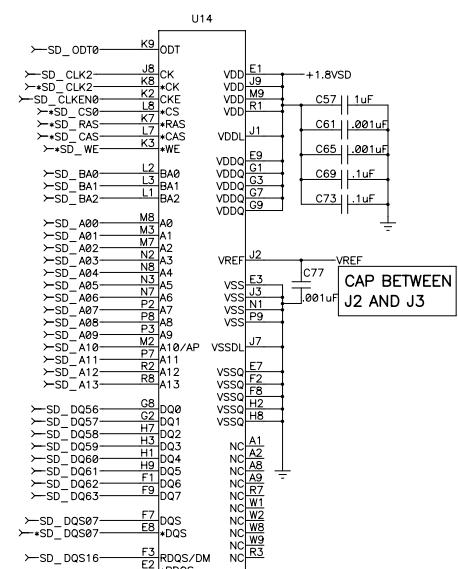
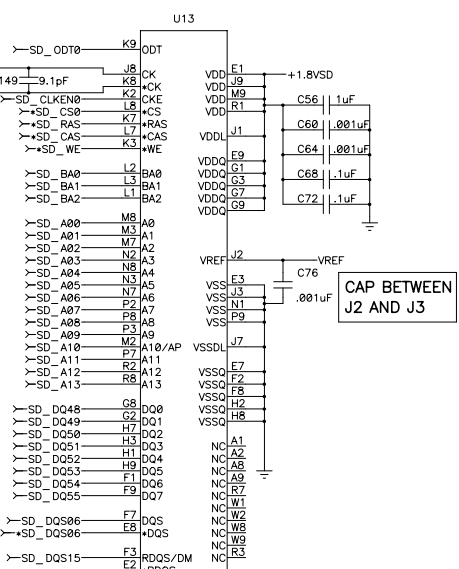
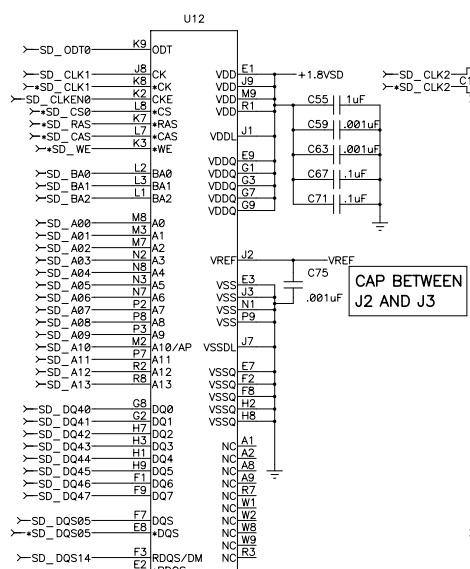
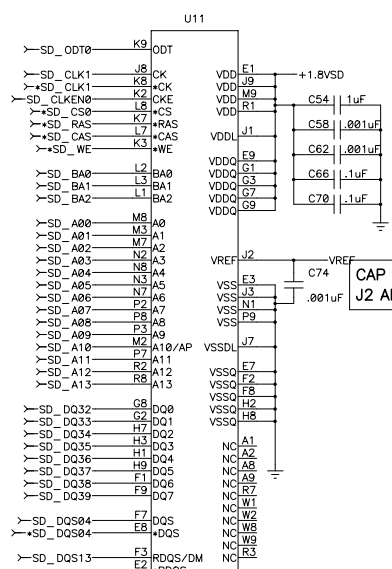
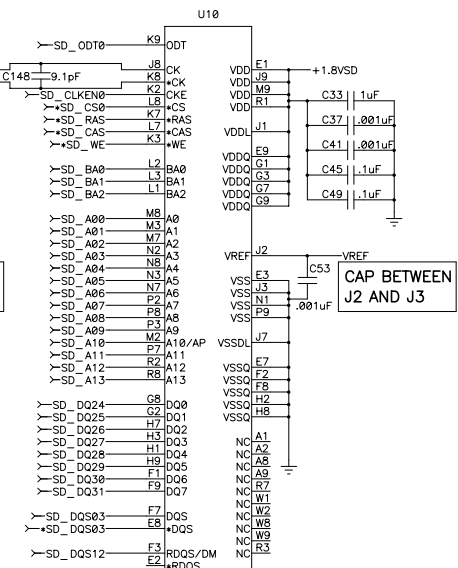
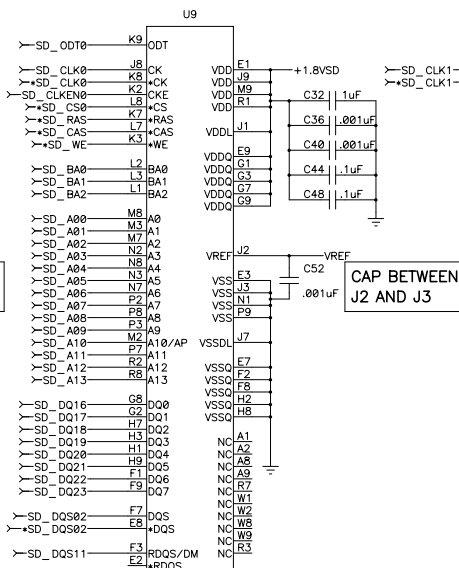
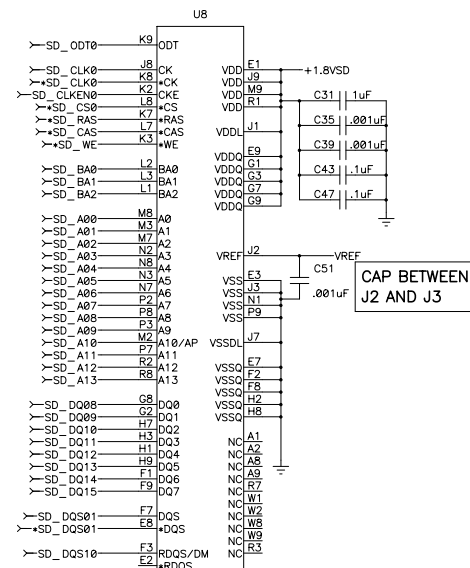
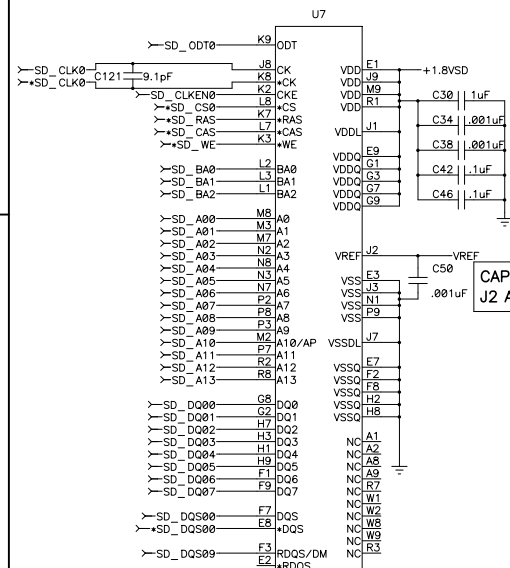


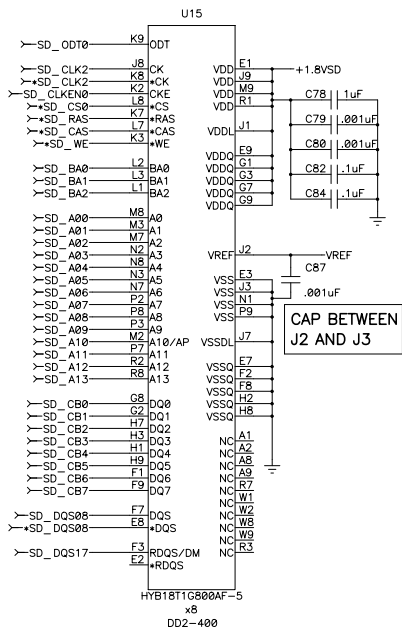
MPC7448

ROUTING ORDER FOR THE MEMORY GROUPS

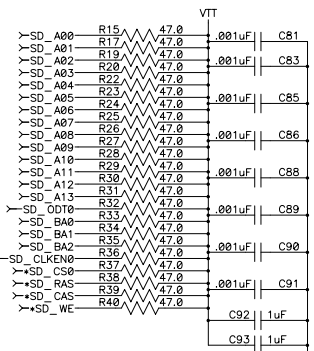
- 1 – DQS PAIRS (0–9) MUST MATCH THE DQ (CB ON ECC), DM BITS WITHIN THE GROUP OF DQS MUST MATCH CLOSELY
- 2 – ADD, WE, CAS, RAS, BA (ADDRESS/COMMAND) MUST MATCH
- 3 – CS, ODT, CKE (CONTROL)
- 3 – CLKS MUST MATCH



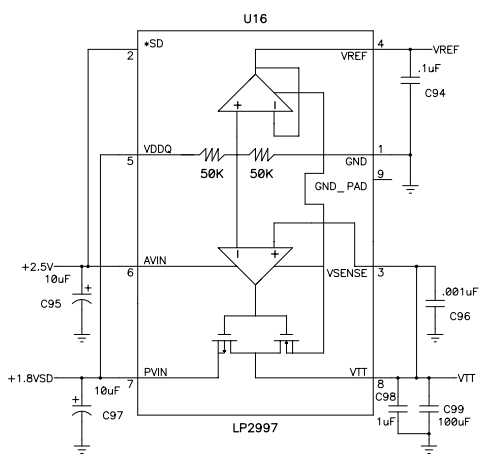
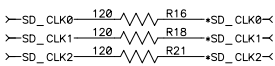




TERMINATION AT THE END OF THE NET



PLACE THE TERMINATIONS AT THE END OF EACH CLOCK SIGNALS



VTT MUST BE ON A ISLAND NOT A TRACE

1

2

3

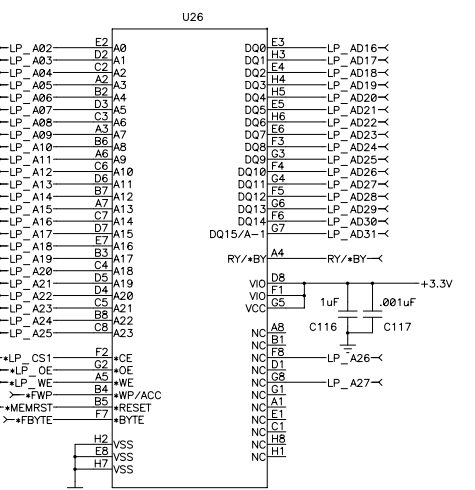
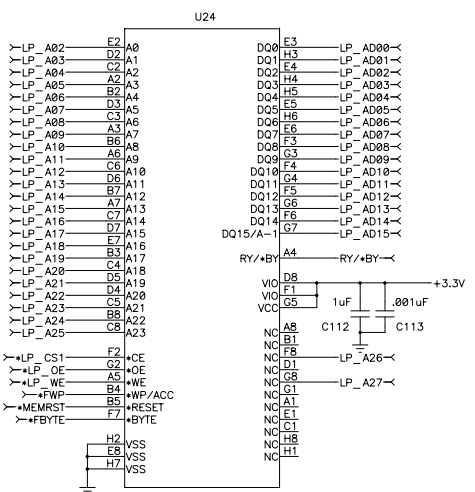
4

1

2

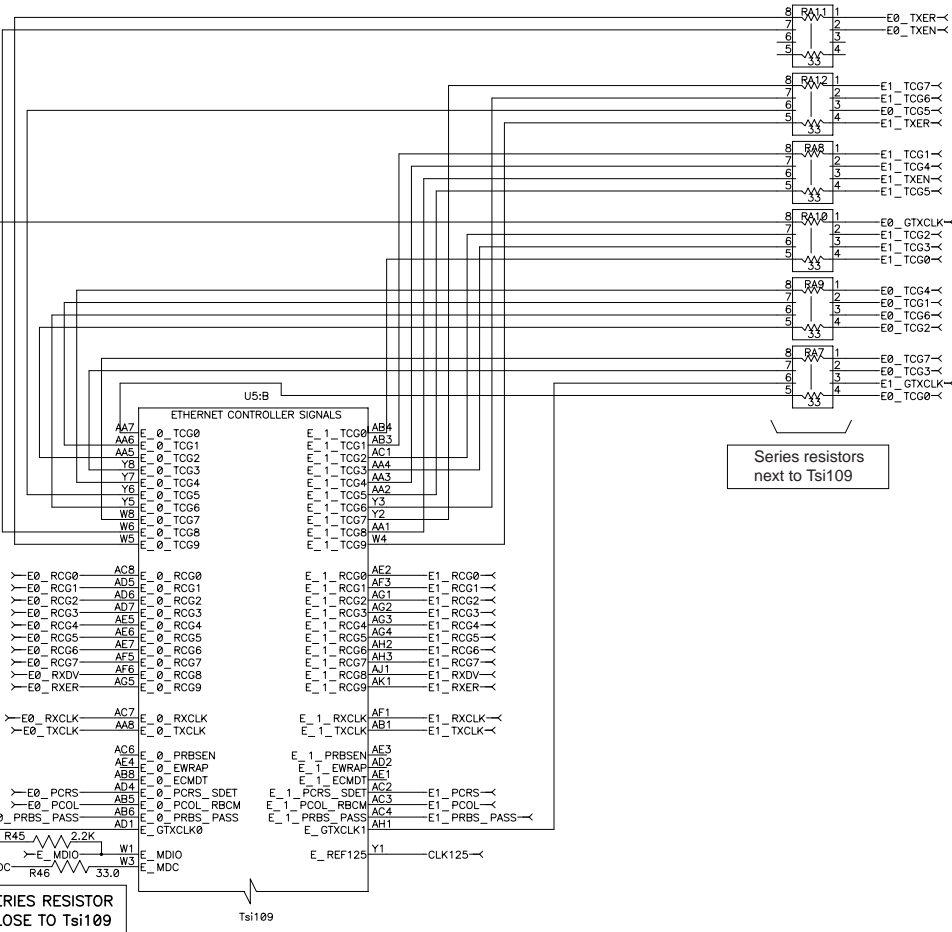
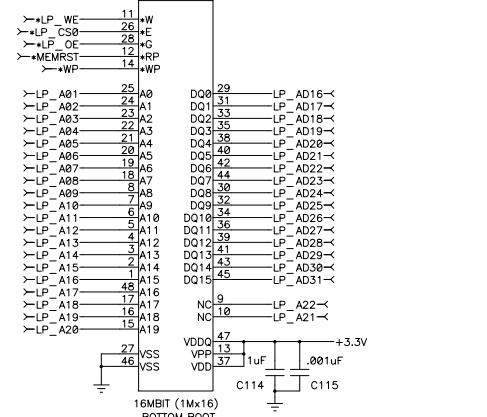
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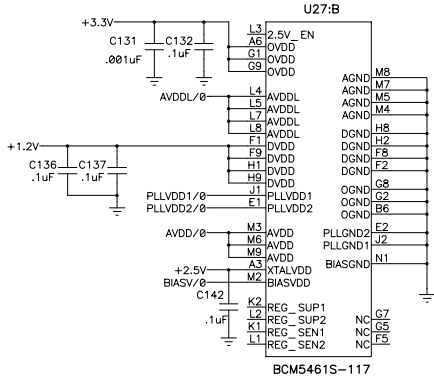
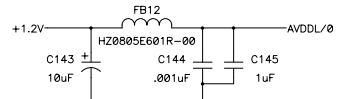
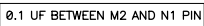
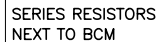
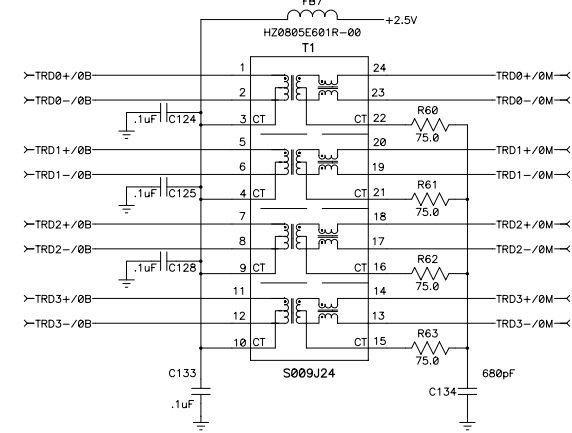
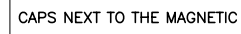
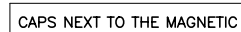
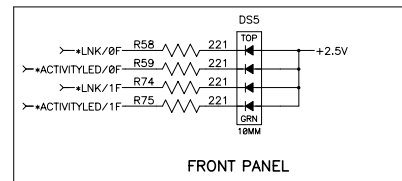
4

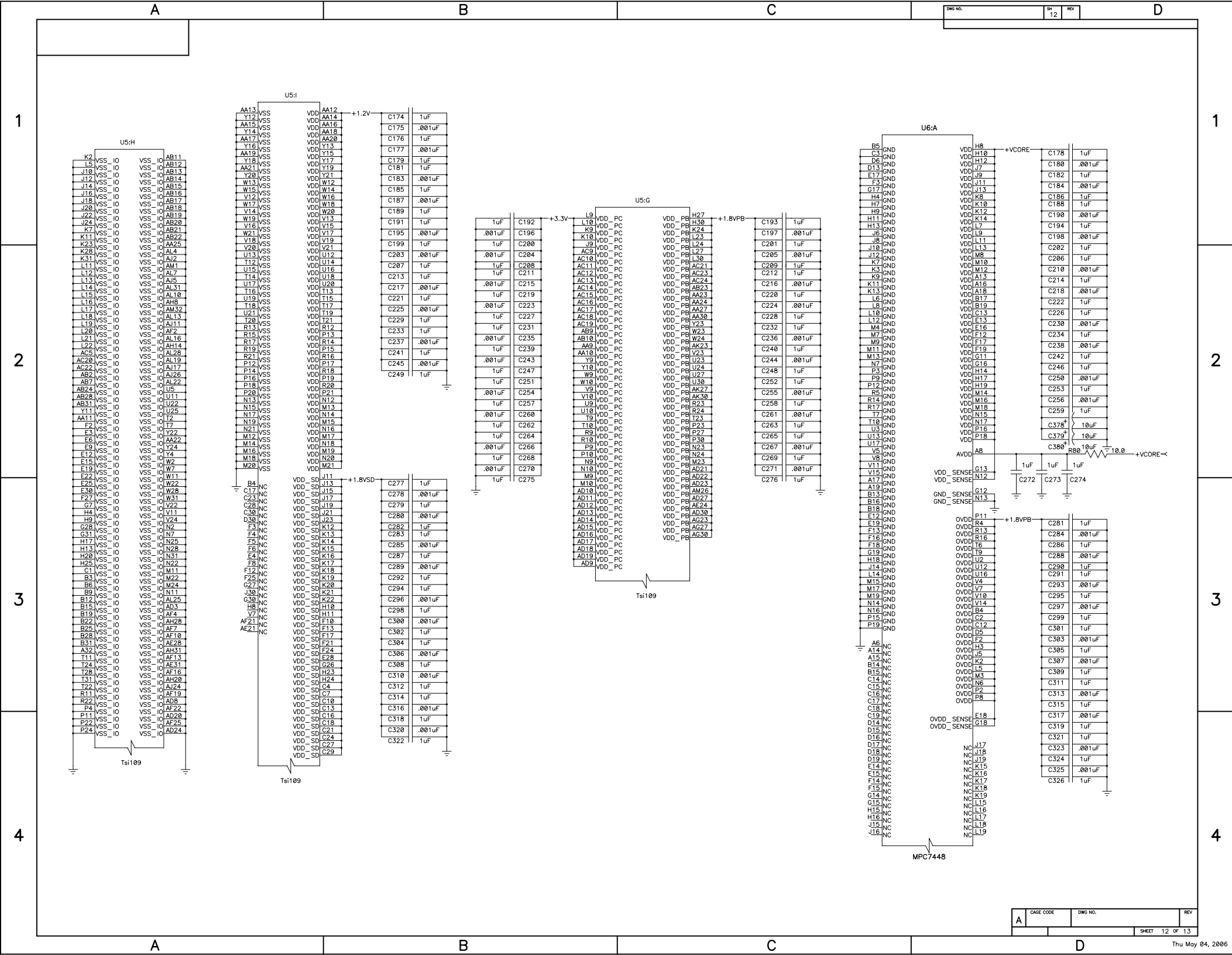


START MDC ROUTE FROM TSI
DO NOT "T" OFF, POINT TO POINT
START THE MDIO ROUTE FROM TSI
DO NOT "T" OFF, POINT TO POINT

SERIES RESISTOR
CLOSE TO Tsi109







POWER SEQUENCING
 +5V AND +3.3V INPUTS RAMP AT THE SAME TIME
 FIRST THE +V CORE, +2.5v, +1.8VSD
 SECOND THE +1.2V
 THIRD +1.8VPB

+V CORE = +1.1V

THESE COMPONENTS
 MUST BE NEXT TO THE FET

PLACE ON THE BACK
 SO IT CAN BE SEEN

