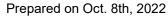
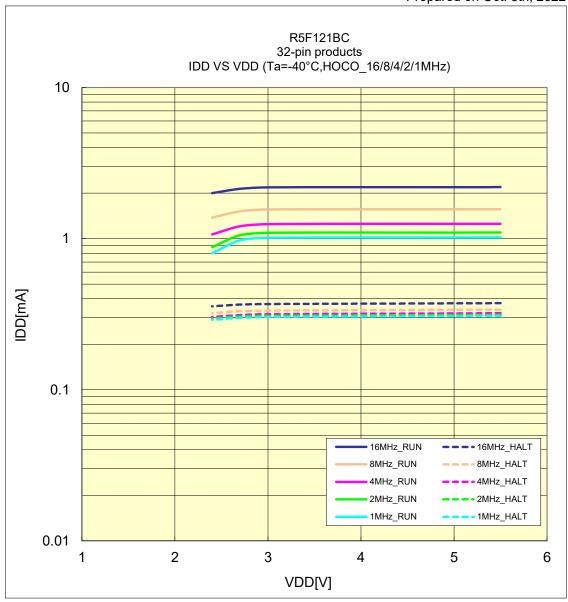
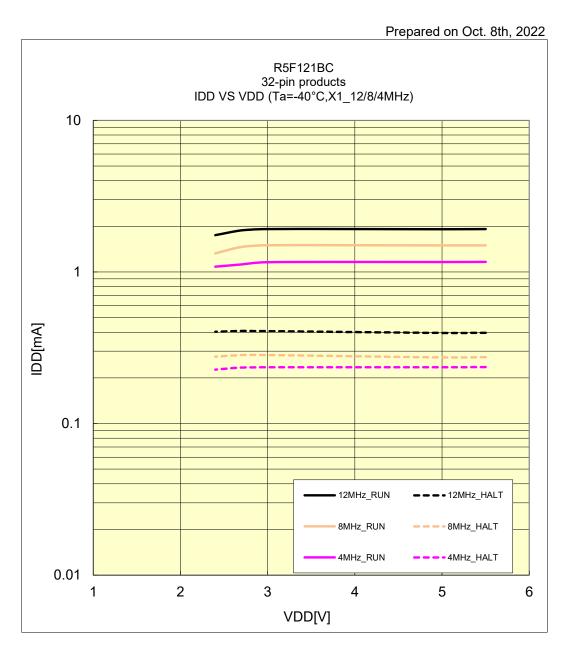
IDD VS VDD(-40°C/HOCO_16/8/4/2/1MHz)

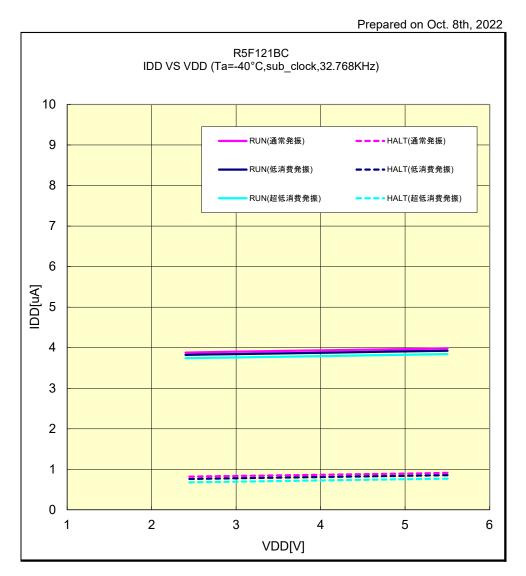




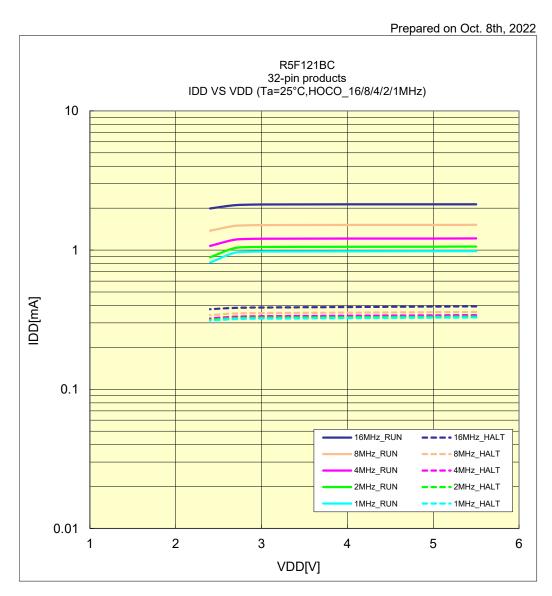
IDD VS VDD(-40°C/X1_12/8/4MHz)



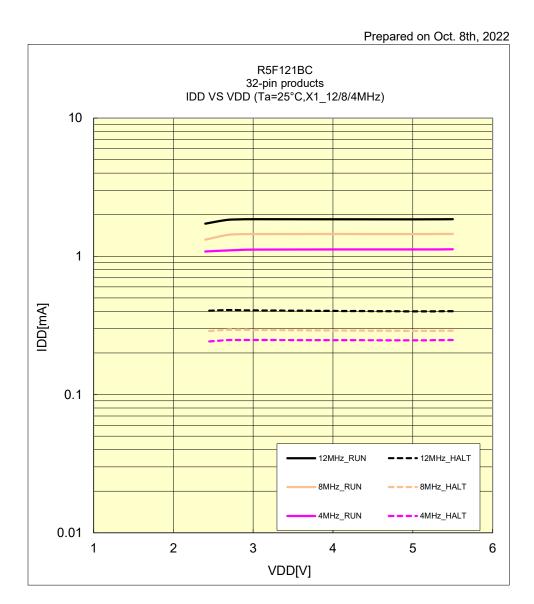
IDD VS VDD(-40°C/sub_clock/32.768KHz)



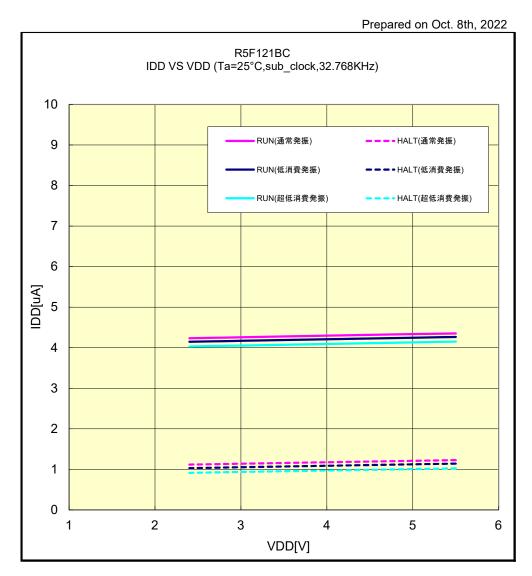
IDD VS VDD(25°C/HOCO_16/8/4/2/1MHz)



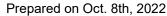
IDD VS VDD(25°C/X1_12/8/4MHz)

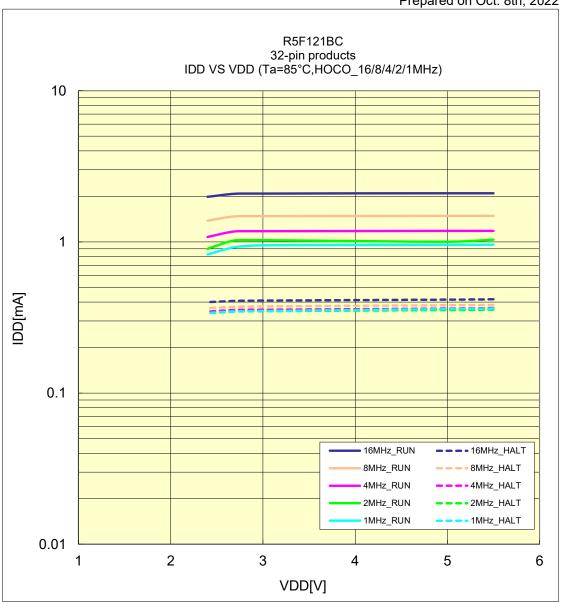


IDD VS VDD(25°C/sub_clock/32.768KHz)

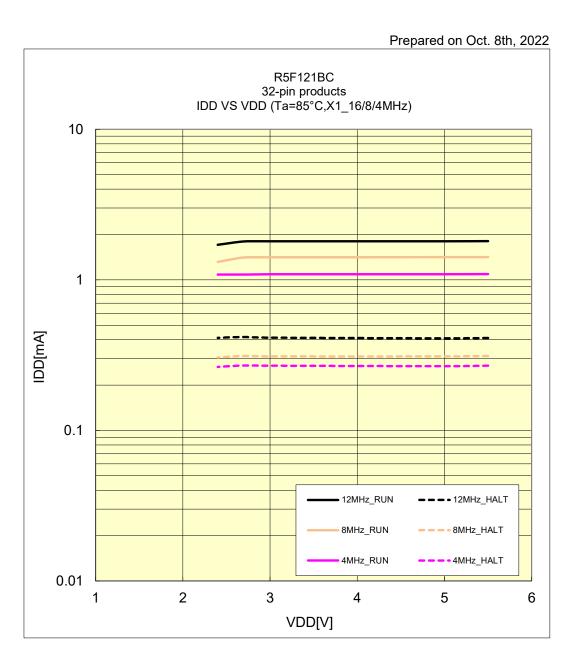


IDD VS VDD(85°C/HOCO_16/8/4/2/1MHz)

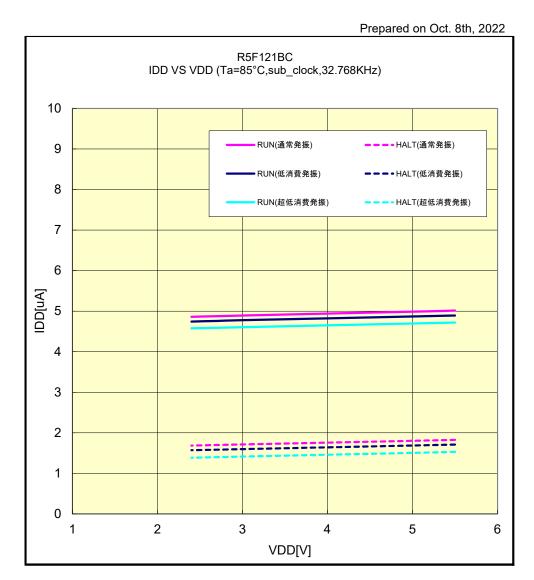




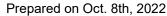
IDD VS VDD(85°C/X1_12/8/4MHz)

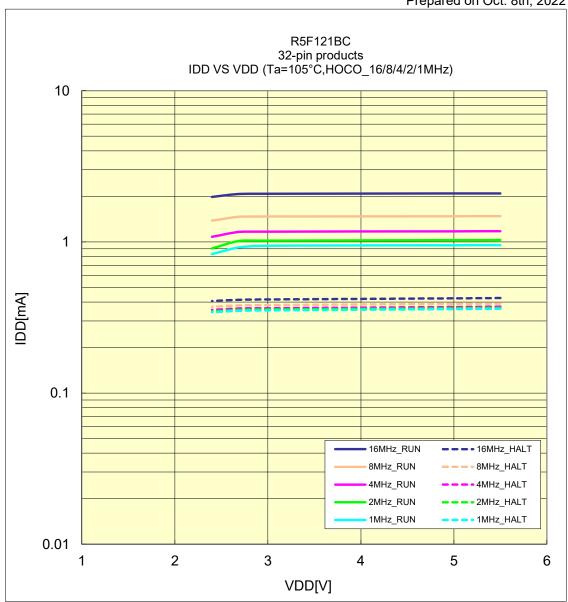


IDD VS VDD(85°C/sub_clock/32.768KHz)

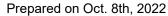


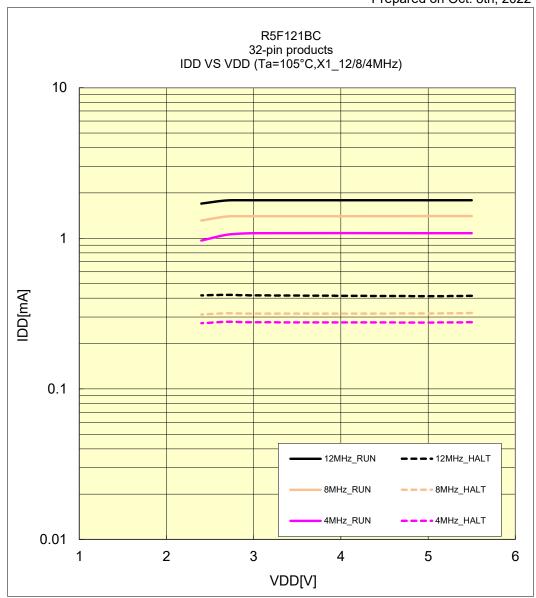
IDD VS VDD(105°C/HOCO_16/8/4/2/1MHz)



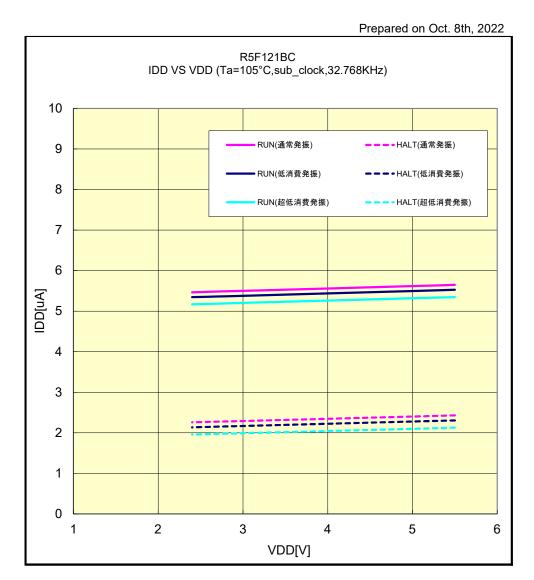


IDD VS VDD(105°C/X1_12/8/4MHz)

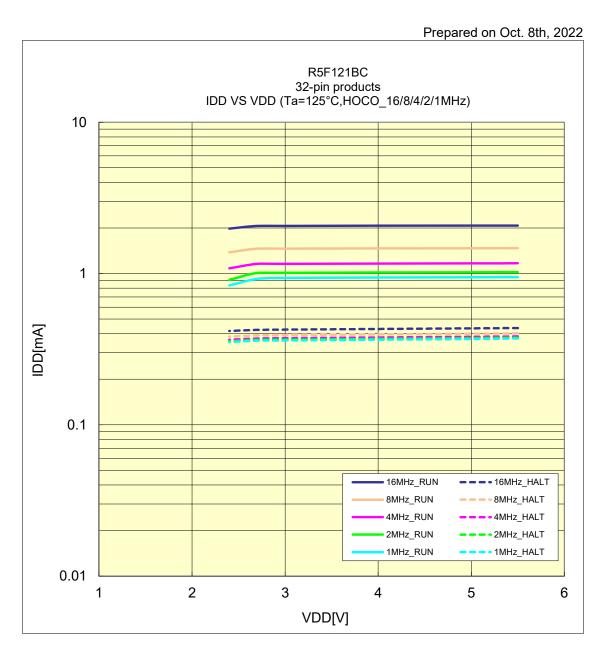




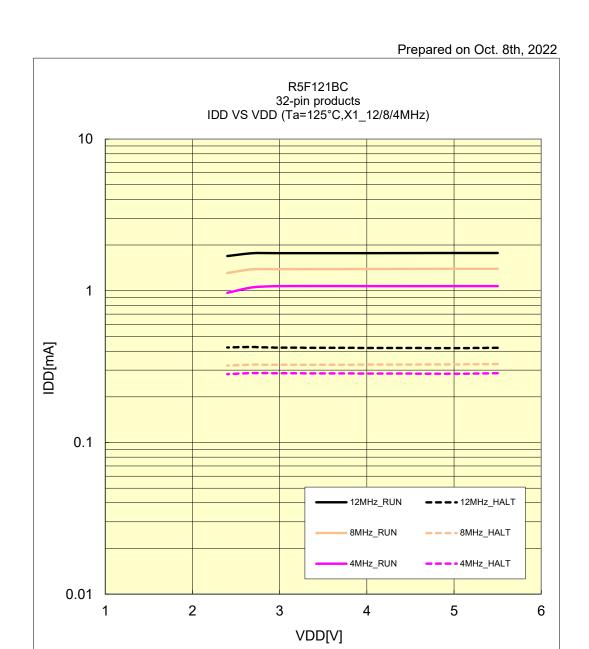
IDD VS VDD(105°C/sub_clock/32.768KHz)



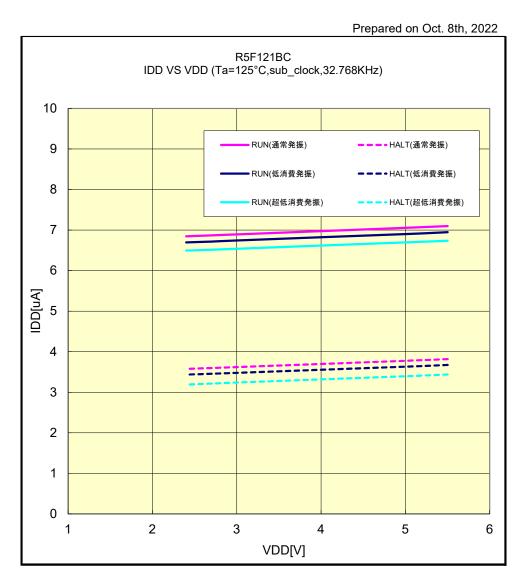
IDD VS VDD(125°C/HOCO_16/8/4/2/1MHz)



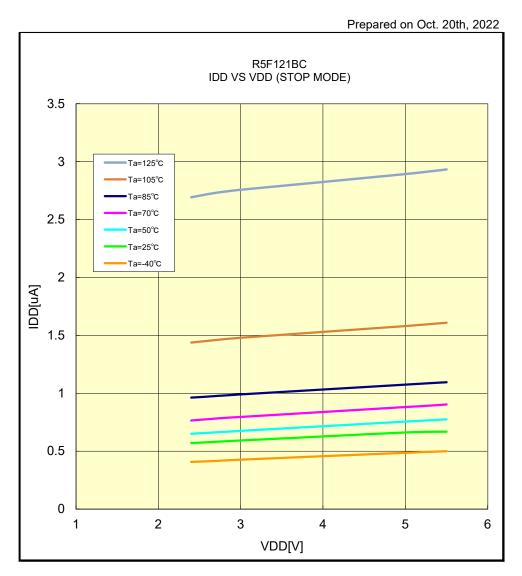
IDD VS VDD(125°C/X1_12/8/4MHz)



IDD VS VDD(125°C/sub_clock/32.768KHz)



IDD VS VDD(STOP MODE)



IDD VS Ta(STOP MODE)

