RENESAS ELECTRONICS SINGLE-CHIP 16-BIT MICROCOMPUTER R5F213G2DNXXXSP

ROM number

| | Date: | |
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| Receipt | | |
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ROM PROGRAMMING CONFIRMATION FORM

Note: Please fill in all items marked *

| | | | | | In in an iteme marked |
|---|-----------|----------------|------------------------------|--------------|-----------------------|
| | | Company | TEL | ure | Submitted by |
| * | Applicant | Name | () | ant ignat | |
| | | Date issued | Year / Month / Date Date: | Applica s | |

* 1. Mask file

Please kindly verify and confirm the mask file in the submitted CD-R prior to submission. Please submit mask files on CD-R. And the number of the mask file must be 1 mask file per one CD-R.

| Part Number | R5F213G2DNXXXSP | | | | | | | | | |
|--|-------------------------|---------------|--|--------------|--|--|--|--|--|--|
| File Code | | | | (hexad | lecimal notation) | | | | | |
| Mask file name | | | | .MSK | (no more than 8 characters) | | | | | |
| ★ 2. Mask option Set the mask option in the mask file generating utility as follows: | | | | | | | | | | |
| Address : 1 | <u>0h</u> | Data | <u>a : 01h</u> | | | | | | | |
| ★ 3. ROM data whic Check the option values as ROM data □ OFS register | function select ata. | registers (OF | ⁻ S, OFS2) and I S2 register | D code areas | to be set for appropriate ID code areas | | | | | |
| CAUTION: Note 1 : ROM order of this product programs the Data Flash area. Note 2 : ROM data confirmation request ROM programming will be processed based on the mask file generated by the mask file generating utility. Only in case when ROM data programmed in the actual mass produced product differs from that of above mentioned mask file, RENESAS takes the responsibility. There is no Engineering Sample, thus please confirm the ROM data at the receipt of the Initial product delivery. Should you find any problem, please return immediately. 2 weeks without technical error feedback towards RENESAS will automatically be regarded as acceptance of products. Note 3 : Mark specification 3G2DNyyy Please refer to Fig. 1 about mark specification. yyy : ROM number XXXXXX : Lot number | | | | | | | | | | |