
IDT[®]
Migrating from the Pericom
PI7C8140A to the Tsi340[™]
PCI-to-PCI Bridge

80E3000_AN001_02

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1. Migrating from the Pericom PI7C8140A to the Tsi340 PCI-to-PCI Bridge

This application note details the differences between the Tsi340™ and the Pericom™ PI7C8140A. It is intended to help easily migrate designs from the PI7C8140A to the Tsi340 by highlighting any differences between the devices.

This document discusses the following:

- “Feature Compatibility” on page 3
- “Software Compatibility” on page 4
- “Pin Compatibility” on page 4

Revision History

80E3000_AN001_02, Formal, September 2009

This document was rebranded as IDT. It does not include any technical changes.

80E3000_AN001_01, Formal, November 2006

This is the first revision of this document.

1.1 Feature Compatibility

The Tsi340 is feature compatible with the PI7C8140A. The Tsi340 implements the additional feature listed in [Table 1](#).

Table 1: Tsi340 and PLX PCI 6140 Feature Comparison

| Feature | Tsi340 | PI7C8140A |
|-----------------------------|--------------|--------------|
| PCI Local Bus Specification | Revision 2.3 | Revision 2.2 |

1.2 Software Compatibility

The Tsi340 and PI7C8140A are fully software compatible within the standard PCI configuration space, with the exception of the value returned by the Vendor ID register (see [Table 2](#)).



For more information on using device specific features and registers which are outside the standard PCI configuration space, refer to the *Tsi340 User Manual*.

Table 2: PCI Standard Configuration Space Register Differences

| Register | Tsi340 | PI7C8140A |
|-----------|--------|-----------|
| Vendor ID | 0x10E3 | 0x12D8 |

1.3 Pin Compatibility

There are no pin differences between the Tsi340 and PIC78140A.



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