

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

Mask ROM number	
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740 FAMILY MASK ROM CONFIRMATION FORM
SINGLE-CHIP MICROCOMPUTER M37542M2-XXXSP/FP/GP/HP
RENESAS TECHNOLOGY

Receipt	Date :	
	Section head signature	Supervisor signature

Note : Please fill in all items marked*.

* Customer	Company name	TEL ()	Issuance signature	Submitted by	Supervisor
	Date issued	Date:			

*1. Confirmation

Specify the name of the product being ordered.
Three EPROMs are required for each pattern if this order is performed by EPROMs.
One floppy disk is required for each pattern if this order is performed by a floppy disk.

Microcomputer name: M37542M2-XXXSP M37542M2-XXXFP M37542M2-XXXGP
 M37542M2-XXXHP

Ordering by EPROMs

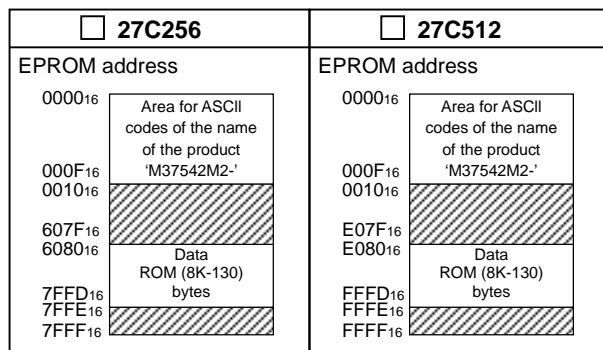
Specify the type of EPROMs submitted.
If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM

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 (hexadecimal notation)

EPROM type (indicate the type used)



In the address space of the microcomputer, the internal ROM area is from address E080₁₆ to FFFD₁₆. The reset vector is stored in addresses FFFC₁₆ and FFFD₁₆.

- (1) Set the data in the unused area (the shaded area of the diagram) to "FF₁₆".
- (2) The ASCII codes of the product name "M37542M2-" must be entered in addresses 0000₁₆ to 0008₁₆. And set the data "FF₁₆" in addresses 0009₁₆ to 000F₁₆. The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address 0000 ₁₆ 'M' = 4D ₁₆ 0001 ₁₆ '3' = 33 ₁₆ 0002 ₁₆ '7' = 37 ₁₆ 0003 ₁₆ '5' = 35 ₁₆ 0004 ₁₆ '4' = 34 ₁₆ 0005 ₁₆ '2' = 32 ₁₆ 0006 ₁₆ 'M' = 4D ₁₆ 0007 ₁₆ '2' = 32 ₁₆	Address 0008 ₁₆ '-' = 2D ₁₆ 0009 ₁₆ FF ₁₆ 000A ₁₆ FF ₁₆ 000B ₁₆ FF ₁₆ 000C ₁₆ FF ₁₆ 000D ₁₆ FF ₁₆ 000E ₁₆ FF ₁₆ 000F ₁₆ FF ₁₆
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We recommend the use of the following pseudo-command to set the start address of the assembler source program because ASCII codes of the product name are written to addresses 0000₁₆ to 0008₁₆ of EPROM.

EPROM type	27C256	27C512
The pseudo-command	△* = △\$8000 △.BYTE△ 'M37542M2-'	△* = △\$0000 △.BYTE△ 'M37542M2-'

Note : If the name of the product written to the EPROMs does not match the name of the mask confirmation form, the ROM will not be processed.

Ordering by floppy disk

We will produce masks based on the mask files generated by the mask file generating utility. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this mask file. Thus, extreme care must be taken to verify the mask file in the submitted floppy disk.

The submitted floppy disk must be 3.5-inch 2HD type and DOS/V format. And the number of the mask files must be 1 in one floppy disk.

Microcomputer name: M37542M2-XXXSP M37542M2-XXXFP M37542M2-XXXGP
 M37542M2-XXXHP

File code

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 (hexadecimal notation)

Mask file name

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 .MSK (equal or less than eight characters)

Note: When submitting data by floppy disk, do not write data to the product name area (addresses 0000₁₆ to 000F₁₆).

Write data to only ROM data area (addresses E080₁₆ to FFFD₁₆).

※2. Mark specification

Mark specification must be submitted using the correct form for the package being ordered. Fill out the appropriate mark specification form (32P4B for M37542M2-XXXSP, 36P2R-A for M37542M2-XXXFP, 32P6U-A for M37542M2-XXXGP, 36PJW-A for M37542M2-XXXHP) and attach it to the mask ROM confirmation form.

Mask ROM number	
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SINGLE-CHIP MICROCOMPUTER M37542M2-XXXSP/FP/GP/HP
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※3. Usage conditions

For our reference of new products, please reply to the following questions about the usage of the products you ordered.

(1) Which operation source clock you use?

- Ceramic resonator
- RC oscillation
- External clock input
- Quartz-crystal oscillation
- Other()
- On-chip oscillator

What frequency do you use?
 $f(X_{IN}) =$ MHz

(2) What is the voltage of power supply (VDD) you use?

Typ.= V Min.= V Max.= V

(3) What is the ambient temperature you use?

Typ.= °C Min.= °C Max.= °C

(4) Which clock division ratio you use?

- Double-speed mode ($f(\phi)=F(X_{IN})$)
- High-speed mode ($f(\phi)=F(X_{IN})/2$)
- Middle-speed mode ($f(\phi)=F(X_{IN})/8$)
- Applied from on-chip oscillator

(5) Please reply to the following questions about timer function.

(i) Which timer you use?

- Timer1 TimerX TimerA TimerB

(ii) Which count source of timer you use?

- Timer X $f(X_{IN})$ $f(X_{IN})/2$ $f(X_{IN})/16$
- Timer A $f(X_{IN})/2$ $f(X_{IN})/16$ $f(X_{IN})/32$ $f(X_{IN})/64$ $f(X_{IN})/128$
- $f(X_{IN})/256$ On-chip oscillator output
- Timer B $f(X_{IN})/2$ $f(X_{IN})/16$ $f(X_{IN})/32$ $f(X_{IN})/64$ $f(X_{IN})/128$
- $f(X_{IN})/256$ Timer A underflow

(iii) Which operating mode you use?

- Timer X Timer mode Pulse output mode
- Event counter mode Pulse width measurement mode

(iv) Do you use the Output compare?

- Use ()channel Not use

(v) Do you use the Input capture?

- Use ()channel Not use

(6) Do you use the Serial I/O?

- Use Not use
- Serial I/O1 (Clock synchronous Serial I/O1 mode Asynchronous Serial I/O1(UART1) mode)
- Serial I/O2 (Clock synchronous Serial I/O2 mode Asynchronous Serial I/O2(UART2) mode)

(7) Do you use the A/D converter?

- Use Not use

(8) Do you use the Watchdog timer?

- Use Not use

(9) Do you use the oscillation stop detection circuit?

- Use Not use

Thank you cooperation.

※4. Comments