Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

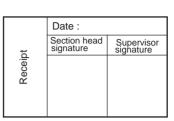
April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

RENESAS TECHNOLOGY SINGLE-CHIP 16-BIT MICROCOMPUTER M30620MC-XXXFP/GP MASK ROM CONFIRMATION FORM

Mask ROM number			
	Mack	numbor	1



Note : Please complete all items marked [‰].

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		Company		TEL			Submitted by	Supervisor
		name		(``	ce		
*	Customer	name		()	lan		
~~~	Gustomer	Date issued	Date :			lssu sign		

#### *1. Check sheet

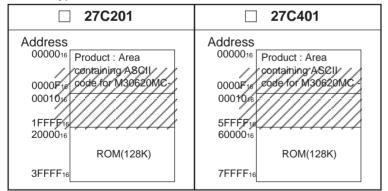
Name the product you order, and choose which to give in, EPROMs or floppy disks. If you order by means of EPROMs, three sets of EPROMs are required per pattern. If you order by means of floppy disks, one floppy disk is required per pattern.

#### □ In the case of EPROMs

Renesas will create the mask using the data on the EPROMs supplied, providing the data is the same on at least two of those sets. Renesas will, therefore, only accept liability if there is any discrepancy between the data on the EPROM sets and the ROM data written to the product. Please carefully check the data on the EPROMs being submitted to Renesas.

Microcomputer type No. :	☐ M30620MC-X>	XFP		] M30620MC-XXXGP
Checksum code for tota	al EPROM area :			(hex)

EPROM type :



(1) Write "FF16" to the lined area.

(2) The area from 0000016 to 0000F16 is for storing

data on the product type name. The ASCII code for 'M30620MC-' is shown at right. The data in this table must be written to address 0000016 to 0000F16.

Both address and data are shown in hex.

Address			Address	
0000016	'M '	= 4D16	0000816	'' = 2D ₁₆
0000116	'3'	= 3316	0000916	FF16
0000216	'0'	= 3016	0000A16	FF16
0000316	'6'	= 3616	0000B16	FF ₁₆
0000416	'2'	= 3216	0000C16	FF ₁₆
0000516	'0'	= 3016	0000D16	FF ₁₆
0000616	'M '	= 4D ₁₆	0000E16	FF16
0000716	'C '	= 4316	0000F16	FF ₁₆



REJ19B0760-0100

## RENESAS TECHNOLOGY SINGLE-CHIP 16-BIT MICROCOMPUTER M30620MC-XXXFP/GP MASK ROM CONFIRMATION FORM

The ASCII code for the type No. can be written to EPROM addresses 0000016 to 0000F16 by specifying the pseudo-instructions for the respective EPROM type shown in the following table at the beginning of the assembler source program.

EPROM type	27C201	27C401
Code entered in source program	$\triangle$ .SECTION $\triangle$ ASCIICODE, ROM DATA $\triangle$ .ORG $\triangle$ 0C0000H $\triangle$ .BYTE $\triangle$ ' M30620MC- '	$\triangle$ .SECTION $\triangle$ ASCIICODE, ROM DATA $\triangle$ .ORG $\triangle$ 080000H $\triangle$ .BYTE $\triangle$ ' M30620MC- '

Note: The ROM cannot be processed if the type No. written to the EPROM does not match the type No. in the check sheet.

### $\Box$ In the case of floppy disks

Renesas processes the mask files generated by the mask file generation utilities out of those held on the floppy disks you give in to us, and forms them into masks. Hence, we assume liability provided that there is any discrepancy between the contents of these mask files and the ROM data to be burned into products we produce. Check thoroughly the contents of the mask files you give in. Prepare 3.5 inches 2HD(IBM format) floppy disks. And store only one mask file in a floppy disk.

Microcomputer type No. :	13062	20M0	C-XX	XFI	Ρ		M30620MC-XXXGP
File code :							(hex)
Mask file name :							.MSK (alpha-numeric 8-digit)

*2. Mark specification

The mark specification differs according to the type of package. After entering the mark specification on the separate mark specification sheet (for each package), attach that sheet to this masking check sheet for submission to Renesas.

For the M30620MC-XXXFP, submit the 100P6S mark specification sheet. For the M30620MC-XXXGP, submit the 100P6Q mark specification sheet.

#### * 3. Usage Conditions

For our reference when of testing our products, please reply to the following questions about the usage of the products you ordered.

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(1)	) Which	kind c	of XIN-XOU	⊤ oscillation	circuit is	used?
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Ceramic resonator	Quartz-crystal oscillator
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External clock input	□Other (
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What frequency do you use?

f(XIN) =	MHz



Mask ROM number

RENESAS TECHNOLOG MICROCOMPUTER M30 MASK ROM CONFIR	620MC-XXXFP/GP	Mask ROM numbe
(2) Which kind of XCIN-XCOUT osci ☐ Ceramic resonator ☐ External clock input What frequency do you use?	illation circuit is used? □Quartz-crystal oscilla □Other (	ator )
f(XCIN) = kHz (3) Which operation mode do you Single-chip mode Microprocessor mode		node
<ul> <li>(4) Which operating ambient temp</li> <li>□-10 °C to 75 °C</li> <li>□-10 °C to 85 °C</li> </ul>	erature do you use? □–20 °C to 75 °C □–20 °C to 85 °C	□ –40 °C to 75 °C □ –40 °C to 85 °C
<ul> <li>(5) Which operating supply voltage</li> <li>□ 2.7V to 3.2V</li> <li>□ 4.2V to 4.7V</li> </ul>	e do you use? □ 3.2V to 3.7V □ 4.7V to 5.2V	□ 3.7V to 4.2V □ 5.2V to 5.5V

Thank you cooperation.

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* 4. Special item (Indicate none if there is no specified item)

