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\*\* Can omit resistor and tie signal directly to PMIC\_PWR to reduce BOM, but this will prevent access to these pins for flexibility / debug.

SCHEMATIC TEMPLATE REV.xdxd_V7 (01-06-2017)		SIZE <b>A3L</b>	TITLE: DA9062 i.MX 7S VARIANT: N/A	
DESIGNED BY: TG <Designed by 2>	SAVED DATE: 29/01/2018:16:44	DA9062_iMX71000 <small>BOARD No. Sch Rev Variant No. Var Rev</small>		
REVIEWED BY: N/A <Reviewed by 2>	REVIEWED DATE: 29/01/2018	DRAWING No: SHEET NAME: DA9062		
		PAGE: 1 of 2		

