

**Option List HD6432258(W),HD6432256(W)**

Please check off the appropriate applications by  ,  or  within  and enter the necessary information

(1) ROM size

<input type="checkbox"/> HD6432258(W) : 256-kbytes
<input type="checkbox"/> HD6432256(W) : 128-kbytes

(2) System Oscillator(OSC1,OSC2)

<input type="checkbox"/> Crystal oscillator	f =	MHz
<input type="checkbox"/> External clock	f =	MHz

(3) I<sup>2</sup>C Bus Option

<input type="checkbox"/> I <sup>2</sup> C bus used
<input type="checkbox"/> I <sup>2</sup> C bus not used

Date of order	
Customer	
Department	
Name	
ROM code Name	
LSI number (Renesas entry)	

- Notes:
1. The "I<sup>2</sup>C bus used" option includes all cases where data transfer is performed via the SCI and SDA pins using the on-chip I<sup>2</sup>C bus interface function ( hardware module ). If the I<sup>2</sup>C bus interface function ( hardware module ) in used, various bus interface with different bus specifications and names are also included in "I<sup>2</sup>C bus used".
  2. When "I<sup>2</sup>C bus not used" is selected, values cannot be set in registers relating to the I<sup>2</sup>C bus interface (ICCR,ICSR,ICDR,ICMR). These register always read H'FF. With emulator and F-ZTAT versions, the "I<sup>2</sup>C bus used" option is selected. If the "I<sup>2</sup>C bus not used" option is selected, it is essential to ensure that "I<sup>2</sup>C bus interface related registers are not accessed.

For the Microcomputer Family item in 1.Basic Specifications in the Single-Chip Microcomputer Ordering Specifications Sheet \*, please specify the appropriate item from the table below according to the combination of items 1 and 4 above. If the "I<sup>2</sup>C bus used" option is selected, please also specify this in Special Specifications(Product Specifications, Mark Specifications, etc....) in 1.Basic specifications.

\* Please contact the relevant sales department for information on the Single-Chip Microcomputer Ordering Specifications Sheet.

ROM Size \ I <sup>2</sup> C	I <sup>2</sup> C bus used	I <sup>2</sup> C bus not used
256Kbytes	H D 6 4 3 2 2 5 8 W	H D 6 4 3 2 2 5 8
128Kbytes	H D 6 4 3 2 2 5 6 W	H D 6 4 3 2 2 5 6