

## Option List **HD6433613,HD6433614**

Please check off the appropriate applications by , ,  or  within  and enter the necessary information.

### 1. ROM size

<input type="checkbox"/> HD6433613	24k-word ROM
<input type="checkbox"/> HD6433614	32k-word ROM

Date of order	
Customer	
Department	
Name	
ROM code Name	
LSI number (Renesas entry)	

### 2. I/O options(Check off the I/O options required and shaded options are not available)

pin name	I/O	I/O option	
		B	C
P10/IRQ0	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P11/IRQ1	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P12/IRQ2	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P13/IRQ3	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P14/IRQ4	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P15/TMOE/IRQ5	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P16/EVENT	I	<input type="checkbox"/>	<input type="checkbox"/>
P17	I	<input type="checkbox"/>	<input checked="" type="checkbox"/>
P20	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P21	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P22	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P23	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P24	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P25	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P26	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P27	I/O	<input type="checkbox"/>	<input type="checkbox"/>

Standard pins

pin name	I/O	I/O option	
		B	C
P80	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P81	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P82	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P83	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P84	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P85	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P86	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P87	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P90/PWM	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P91/SCK1	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P92/SI1	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P93/SO1	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P94/SCK2	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P95/SI2	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P96/SO2	I/O	<input type="checkbox"/>	<input type="checkbox"/>
P97/UD	I/O	<input type="checkbox"/>	<input type="checkbox"/>
PA0	I/O	<input type="checkbox"/>	<input type="checkbox"/>
PA1	I/O	<input type="checkbox"/>	<input type="checkbox"/>
PA2	I/O	<input type="checkbox"/>	<input type="checkbox"/>
PA3	I/O	<input type="checkbox"/>	<input type="checkbox"/>
PA4	I/O	<input type="checkbox"/>	<input type="checkbox"/>
PA5	I/O	<input type="checkbox"/>	<input type="checkbox"/>
PA6	I/O	<input type="checkbox"/>	<input type="checkbox"/>
PA7	I/O	<input type="checkbox"/>	<input type="checkbox"/>

Standard pins

**P40 to P45 are PMOS open-drain pins.**

B : With pull-up MOS

C : Without pull-up MOS

### 3. System Oscillator(OSC1,OSC2)

<input type="checkbox"/> Ceramic oscillator	f =	MHz
<input type="checkbox"/> Crystal oscillator	f =	MHz
<input type="checkbox"/> External clock	f =	MHz

### 4. Subsystem Oscillator (X1,X2)

<input type="checkbox"/> Used	f = 32.768 kHz
<input type="checkbox"/> Not used (X1 = Vcc)	

### 5. Package type

<input type="checkbox"/> DP-64S
<input type="checkbox"/> FP-64A