

Option List HD6432264(W),HD6432262(W)

Please check off the appropriate applications by , or within and enter the necessary information.

| | |
|----------------------------|--|
| Date of order | |
| Customer | |
| Department | |
| Name | |
| ROM code Name | |
| LSI number (Renesas entry) | |

(1) ROM size

| |
|--|
| <input type="checkbox"/> HD6432264(W) : 128-kbytes |
| <input type="checkbox"/> HD6432262(W) : 64-kbytes |

(2) System Oscillator(OSC1,OSC2)

| | | |
|---|-----|-----|
| <input type="checkbox"/> Crystal oscillator | f = | MHz |
| <input type="checkbox"/> External clock | f = | MHz |

(3) Power Supply Voltage/Maximum Operating Frequency

| |
|---|
| <input type="checkbox"/> Vcc = 2.7V ~ 5.5V (13.5MHz max, AVcc=2.7 ~ 5.5V, Step-Down Circuit used) for HD6432264(W)/HD6432262(W) |
| <input type="checkbox"/> Vcc = 4.0V ~ 5.5V (20.5MHz max, AVcc=4.0 ~ 5.5V, Step-Down Circuit used) for HD6432264(W)/HD6432262(W) |

Notes: 1.Please select the power supply voltage/operating frequency version according to the power supply voltage used.

Example: For use at Vcc = 2.7V to 3.6V/f = 6MHz
select Vcc = 2.7V to 3.6V(13.5MHz max.).

2.The power supply voltage and maximum operating frequency of the selected version should also be entered on the Single-Chip Microcomputer Ordering Specifications sheet.

(4) I²C Bus Option

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|--|
| <input type="checkbox"/> I ² C bus used |
| <input type="checkbox"/> I ² C bus not used |

Notes: 1. The "I²C bus used" option includes all cases where data transfer is performed via the SCL and SDA pins using the on-chip I²C bus interface function (hardware module). If the I²C bus interface function (hardware module) is used, various bus interface with different bus specifications and names are also included in "I²C bus used".

2. When "I²C bus not used" is selected, values cannot be set in registers relating to the I²C bus interface (ICCR,ICSR,ICDR,ICMR). These register always read H'FF. With emulators and F-ZTAT versions, the "I²C bus used" option is selected. If the "I²C bus not used" option is selected, it is essential to ensure that "I²C bus interface related registers are not specifications.

For the Microcomputer Family item in 1.Basic Specifications in the Single-Chip Microcomputer Ordering Specifications Sheet *, please specify the appropriate item from the table below according to the combination of items 1 and 4 above. If the "I²C bus used" option is selected, please also specify this in Special Specifications(Product Specifications, Mark Specifications, etc....) in 1.Basic Specifications.

* Please contact the relevant sales department for information on the Single-Chip Microcomputer Ordering Specifications Sheet

| | | |
|------------------|---------------------------|-------------------------------|
| I ² C | | |
| ROM Size | I ² C bus used | I ² C bus not used |
| 128Kbytes | H D 6 4 3 2 2 6 4 W | H D 6 4 3 2 2 6 4 |
| 64Kbytes | H D 6 4 3 2 2 6 2 W | H D 6 4 3 2 2 6 2 |