

Option List HD6432239

Please check off the appropriate applications by , or within and enter the necessary information.

(1) ROM size

HD6432239(W) : 384-kbytes

Date of order	
Customer	
Department	
Name	
ROM code Name	
LSI number (Renesas entry)	

(2) Power Supply Voltage / Maximum Operating Frequency / I²C Bus Option

Voltage/ Frequency	I ² C Bus	<u>I²C Bus used</u> HD6432239W	<u>I²C Bus not used</u> HD6432239
	V _{cc} = 2.2V to 3.6V f = 2MHz to 6.25MHz		—————
V _{cc} = 2.7V to 3.6V f = 2MHz to 16MHz		<input type="checkbox"/>	<input type="checkbox"/>
V _{cc} = 3.0V to 3.6V f = 2MHz to 20MHz		<input type="checkbox"/>	<input type="checkbox"/>

- Notes 1. The "I²C bus used" option includes all cases where data transfer is performed via the SCL and SDA pins using the on-chip I²C bus interface function (hardware module). If the I²C bus interface function (hardware module) is used, various bus interface with different bus specifications and names are also included in "I²C bus used".
2. When "I²C bus not used" is selected, values cannot be set in registers relating to the I²C bus interface (ICCR,ICSR,ICDR,ICMRetc). These registers are invalid value that reading. With emulators, F-ZTAT versions, the "I²C bus used" option is selected. If the "I²C bus not used" option is selected, it is essential to ensure that "I²C bus interface related registers are not accessed.

Notes: Please contact Renesas's sales office before using the product in an application that demands especially high quality and reliability ,such as automobile etc.

Because there is no product for high quality and reliability in the H8S/2239 series now.