Option List   HD6432238B(W),HD6432236B(W),HD6432238R(W),HD6432236R(W)

Please check off the appropriate applications
by ■ , ✕ or ✓ within □ and enter the
necessary information.

Date of order	
Customer	
Department	
Name	
ROM code Name	
LSI number (Renesas entry)	

(1) ROM size
□ HD6432238B(W),HD6432238R(W) : 256-kbytes
□ HD6432236B(W),HD6432236R(W) : 128-kbytes

(2) System Oscillator(OSC1,OSC2)
□ Crystal oscillator f =   MHz
□ External clock   f =   MHz

(3) Power Supply Voltage/Maximum Operating Frequency

□ Vcc   2.7V   5.5V   13.5MHz max, AVcc=3.6   5.5V, Step-Down Circuit used)
　　　　　　　　　　　　　for HD6432238B(W)／HD6432236B(W)

□ Vcc   2.7V   3.6V   13.5MHz max, AVcc=2.7   3.6V, Step-Down Circuit not used)
　　　　　　　　　　　　　for HD6432238R(W)／HD6432236R(W)

□ Vcc   2.2V   3.6V   6.25MHz max, AVcc=2.2   3.6V, Step-Down Circuit not used)
　　　　　　　　　　　　　for HD6432238R／HD6432236R

Notes: 1.Please select the power supply voltage/operating frequency version according to the power
              supply voltage used.
           Example: For use at Vcc = 2.7V to 3.6V/f = 6MHz
                       select Vcc = 2.7V to 3.6V(13.5MHz max.).
         2.The power supply voltage and maximum operating frequency of the selected version should
            also be entered on the Single-Chip Microcomputer Ordering Specifications sheet.

(4) I^2C Bus Option
□ I^2C bus used
□ I^2C bus not used

Notes: 1. The "I^2C bus used" option includes all cases where data transfer is performed via the SCL
             and SDA pins using the on-chip I^2C bus interface function (hardware module). If the I^2C
             bus interface function (hardware module) in used, various bus interface with different bus
             specifications and names are also included in "I^2C bus used".
         2. When "I^2C bus not used" is selected, values cannot be set in registers relating to the I^2C
             bus interface (ICCR,ICSR,ICDR,ICMR). These register always read H'FF. With emulators
             and F-ZTAT versions, the "I^2C bus used" option is selected. If the "I^2C bus not used"
             option is selected, it is essential to ensure that "I^2C bus interface related registers are not
             specifications.

For the Microcomputer Family item in 1.Basic Specifications in the Single-Chip Microcomputer
Ordering Specifications Sheet *, please specify the appropriate item from the table below according
to the combination of items 1 and 4 above. If the "I^2C bus used" option is selected, please also
specify this in Special Specifications(Product Specifications, Mark Specifications, etc....) in 1.Basic
accessed.

* Please contact the relevant sales department for information on the Single-Chip Microcomputer
  Ordering Specifications Sheet

I^2C ROM Size	I^2C bus used	I^2C bus not used
256Kbytes		
128Kbytes		