

## Option List HD6432127S,HD6432128S

Please check off the appropriate applications by ,  or  within  and enter the necessary information.

### (1) ROM size

- HD6432127S(W) : 64-kbytes  
 HD6432128S(W) : 128-kbytes

### (2) System Oscillator(EXTAL,XTAL)

- Crystal oscillator f = \_\_\_\_\_ MHz  
 External clock f = \_\_\_\_\_ MHz

|                               |  |
|-------------------------------|--|
| Date of order                 |  |
| Customer                      |  |
| Department                    |  |
| Name                          |  |
| ROM code Name                 |  |
| LSI number<br>(Renesas entry) |  |

### (3) Power Supply Voltage/Maximum Operating Frequency

- Vcc = 4.5V to 5.5V (20 MHz max.)  
 Vcc = 4.0V to 5.5V (16 MHz max.)  
 Vcc = 2.7V to 3.6V (10 MHz max.)
- under contemp

Notes 1. Please select the power supply voltage/operating frequency version according to the power supply voltage used.

Example: For use at Vcc = 4.5V to 5.5V/f = 10MHz  
select Vcc = 4.5V to 5.5V(20MHz max.).

2. The power supply voltage and maximum operating frequency of the selected version should also be entered on the Single-Chip Microcomputer Ordering Specifications sheet.

### (4) I<sup>2</sup>C Bus Option

- I<sup>2</sup>C bus used  
 I<sup>2</sup>C bus not used

Notes 1. The "I<sup>2</sup>C bus used" option includes all cases where data transfer is performed via the SCL and SDA pins using the on-chip I<sup>2</sup>C bus interface function ( hardware module ). If the I<sup>2</sup>C bus interface function ( hardware module ) is used, various bus interface with different bus specifications and names are also included in "I<sup>2</sup>C bus used".

2. When "I<sup>2</sup>C bus not used" is selected, values cannot be set in registers relating to the I<sup>2</sup>C bus interface (ICCR,ICSR,ICDR,ICMR etc). These registers are invalid value that reading. With emulators, F-ZTAT versions, the "I<sup>2</sup>C bus used" option is selected. If the "I<sup>2</sup>C bus not used" option is selected, it is essential to ensure that "I<sup>2</sup>C bus interface related registers are not accessed.

For the Microcomputer Family item in 1. Basic Specifications in the Single-Chip Microcomputer Ordering Specifications Sheet \*, please specify the appropriate item from the table below according to the combination of items 1 and 4 above. If the "I<sup>2</sup>C bus used" option is selected, please also specify this in Special Specifications(Product Specifications, Mark Specifications, etc....) in 1. Basic specifications.

\* Please contact the relevant sales department for information on the Single-Chip Microcomputer Ordering Specifications Sheet.

|            | I <sup>2</sup> C | I <sup>2</sup> C bus used | I <sup>2</sup> C bus not used |
|------------|------------------|---------------------------|-------------------------------|
| ROM Size   |                  |                           |                               |
| 64-kbytes  |                  | HD6432127SW               | HD6432127S                    |
| 128-kbytes |                  | HD6432128SW               | HD6432128S                    |