

LDOs decoupling: place one capacitor close to each pad group as below:

- VDD\_LDOx: 1, 2, 3, 4, 5
- VDD\_LDOx: 6, 7, 8, 9, 10, 11
- VSYS

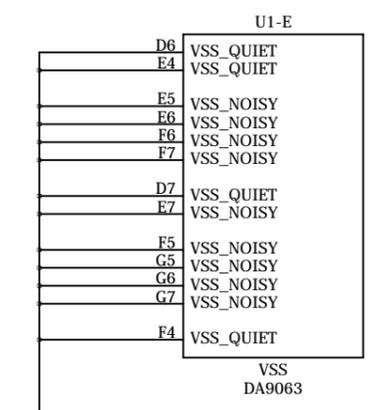
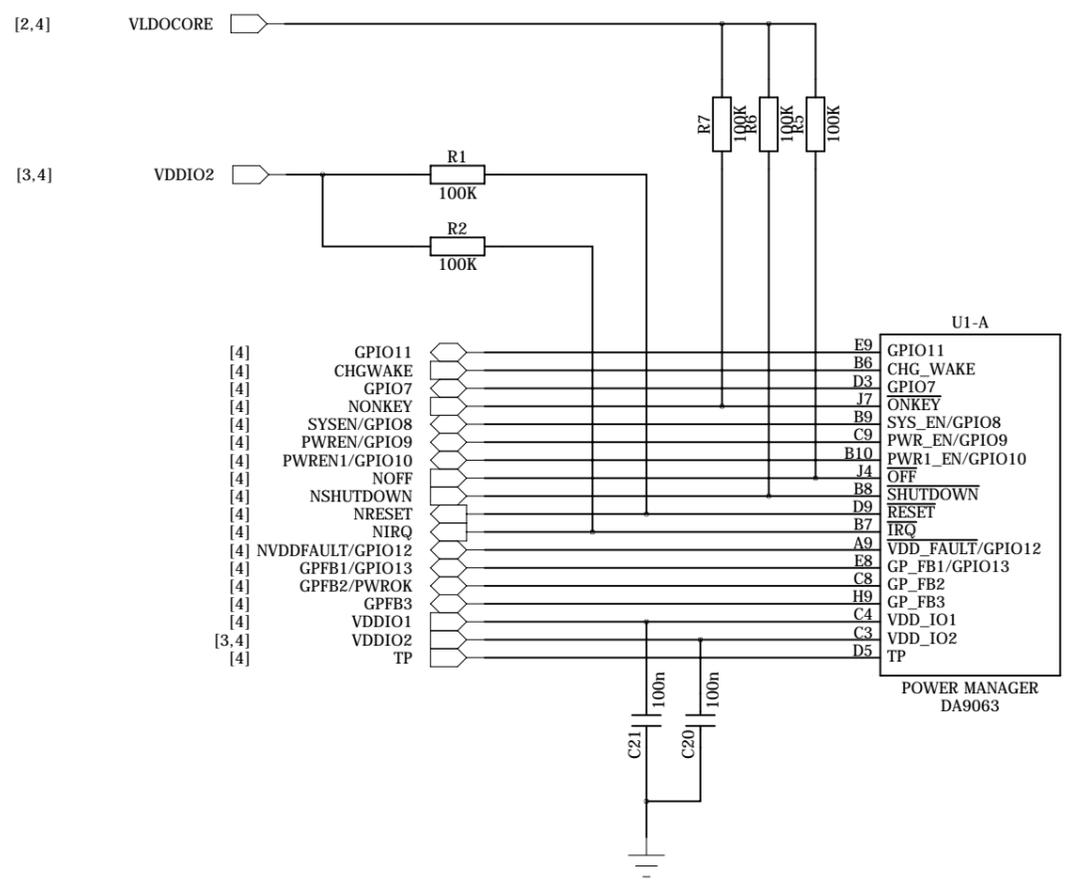
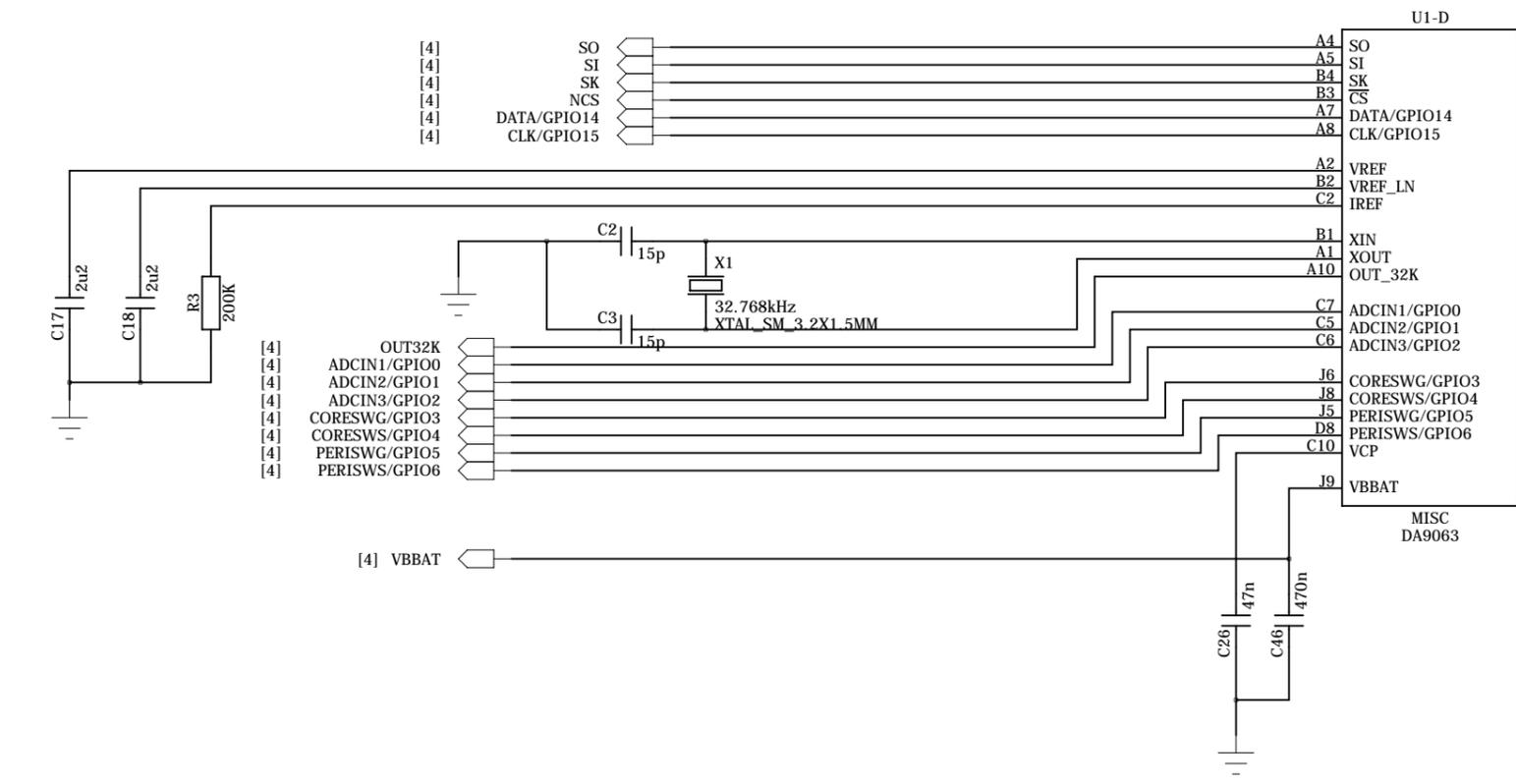
[4]	VLDO1	K3	VLDO1
		H3	VDD_LDO1
[4]	VLDO2	K2	VLDO2
		H2	VDD_LDO2
[4]	VLDO3	K1	VLDO3
		G3	VDD_LDO3
[4]	VLDO4	J1	VLDO4
		G4	VDD_LDO4
[4]	VLDO5	H1	VLDO5
		G2	VDD_LDO5
[4]	VLDO6	G1	VLDO6
		F3	VDD_LDO6
[4]	VLDO7	F2	VLDO7
[4]	VLDO8	F1	VLDO8
		E2	VDD_LDO7_8
[4]	VLDO9	E1	VLDO9
[4]	VLDO10	D1	VLDO10
		E3	VDD_LDO9_10
[4]	VLDO11	C1	VLDO11
		D2	VDD_LDO11
[3,4]	VDDCORE	A6	VDDCORE
		A3	VSYS

LDO DA9063

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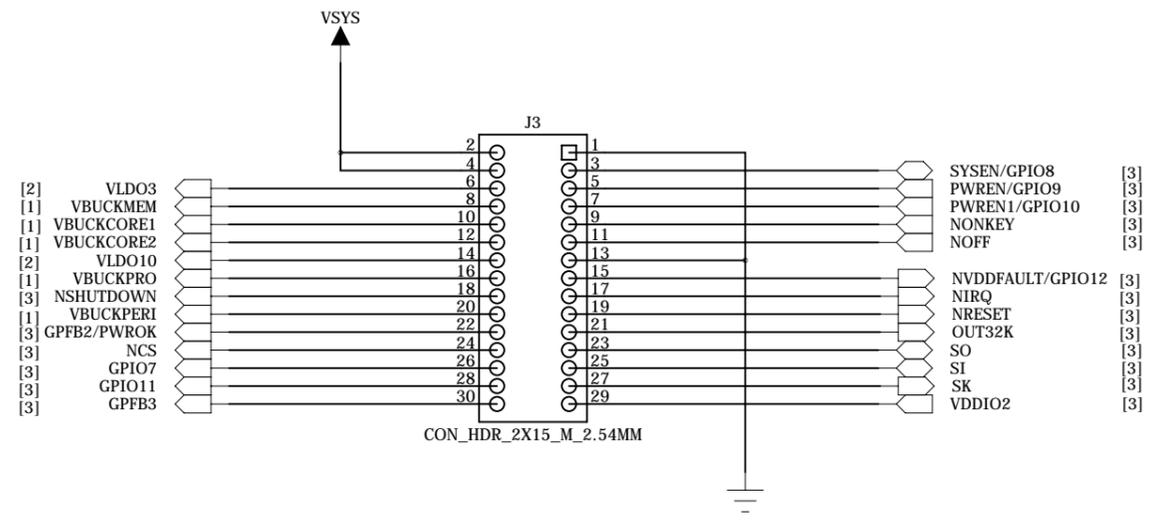
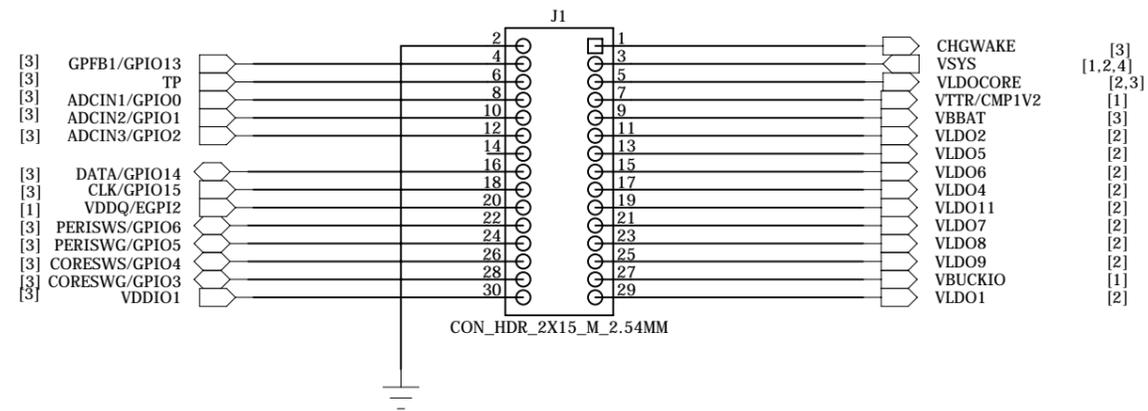
CONFIDENTIAL INFORMATION		TITLE: DA9063 PERFORMANCE BOARD	
PREPARED: FG	DATED: 19/11/2013	DRAWING NO: 176-11-A	REV: A
RESPONSIBLE: FG	DATED: <Approved Date>	SHEET NAME: LDOS	PAGE: 2 OF 5





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CONFIDENTIAL INFORMATION		TITLE: DA9063 PERFORMANCE BOARD		
PREPARED: FG	DATED: 19/11/2013	DRAWING NO: 176-11-A	REV: A	
RESPONSIBLE: FG	DATED: <Approved Date>	SHEET NAME: DIGITAL		PAGE: 3 OF 5



CONFIDENTIAL INFORMATION		TITLE: <b>DA9063 PERFORMANCE BOARD</b>		
PREPARED: <b>FG</b>	DATED: 19/11/2013	DRAWING NO: <b>176-11-A</b>	REV: <b>A</b>	
RESPONSIBLE: <b>FG</b>	DATED: <Approved Date>	SHEET NAME: <b>CONNECTORS</b>		
				PAGE: <b>4 OF 5</b>

REV	DATE	ENGINEER	NOTES
A	16 Oct 2012	FG	Initial version based on 44-179-176-07-A.
A1	19 Nov 2013	FG	J1 & J3 changed from SSQ-115-02-G-D to TSW-115-22-G-D

SCHEMATIC TEMPLATE REV PA2 (November 30, 2011) DIALOG DOCUMENT NUMBER 31-902-03

CONFIDENTIAL INFORMATION		TITLE: DA9063 PERFORMANCE BOARD		
PREPARED: FG	DATED: 19/11/2013	DRAWING NO: 176-11-A	REV: A	
RESPONSIBLE: FG	DATED: <Approved Date>	SHEET NAME: REVISION		PAGE: 5 OF 5