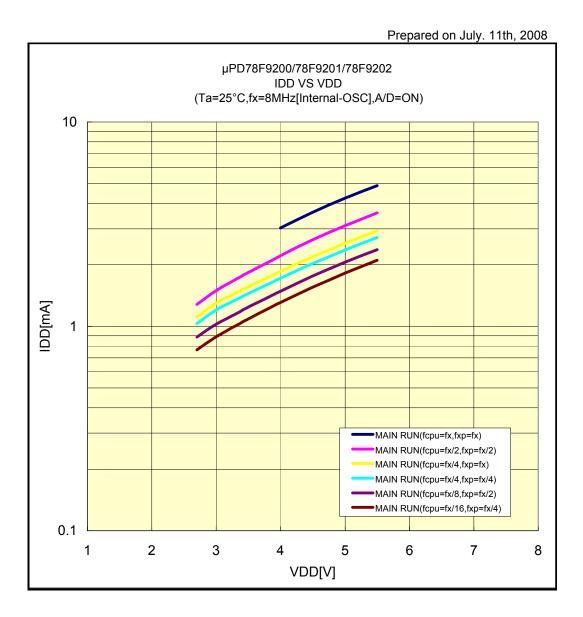
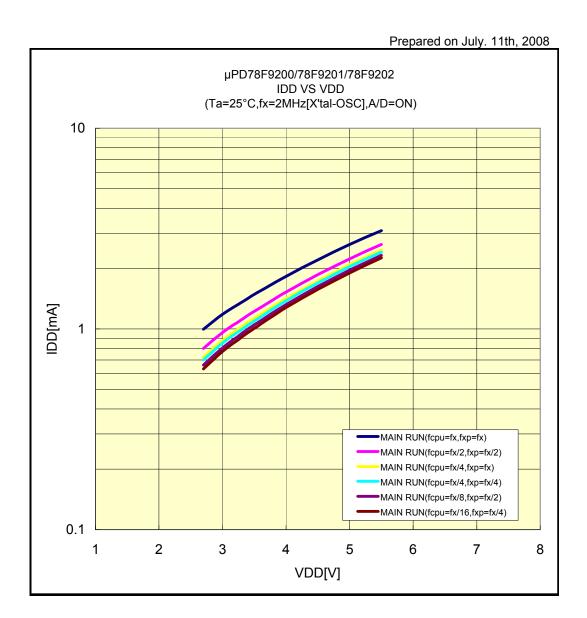
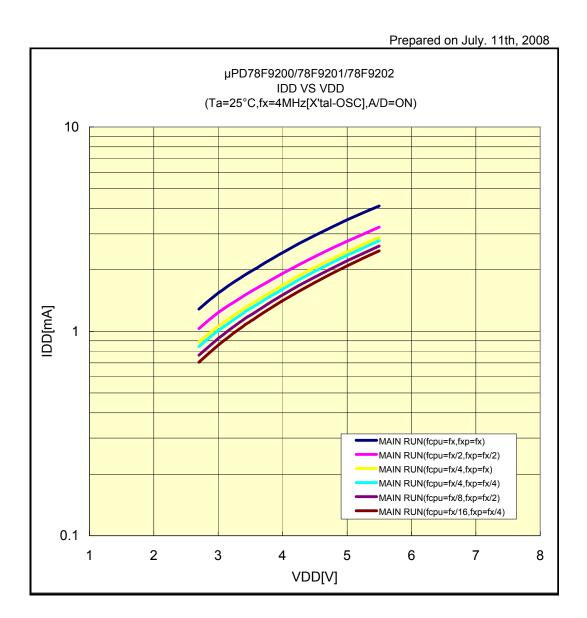
IDD VS VDD(25°C/8MHz[Internal-OSC]/AD=ON)



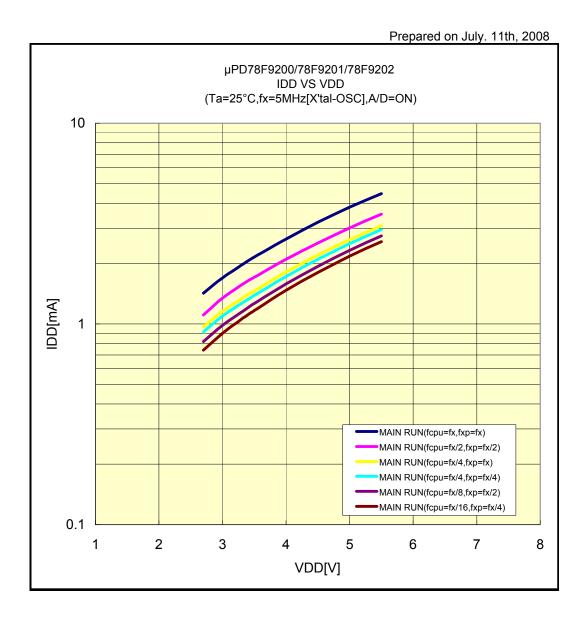
IDD VS VDD(25°C/2MHz[X'tal-OSC]/AD=ON)



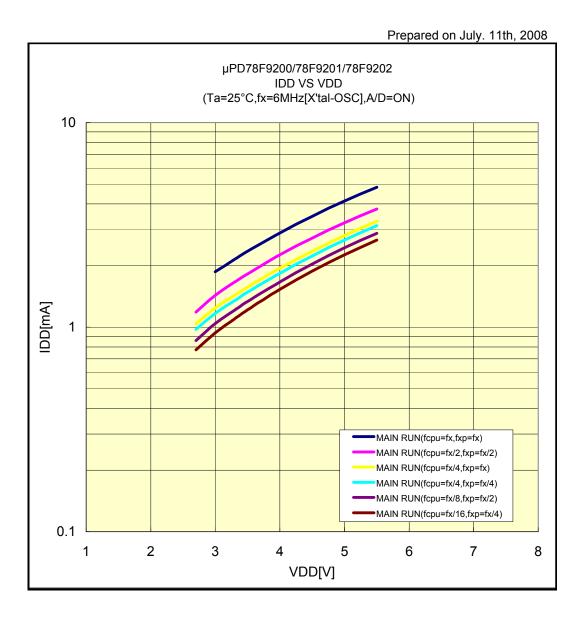
IDD VS VDD(25°C/4MHz[X'tal-OSC]/AD=ON)



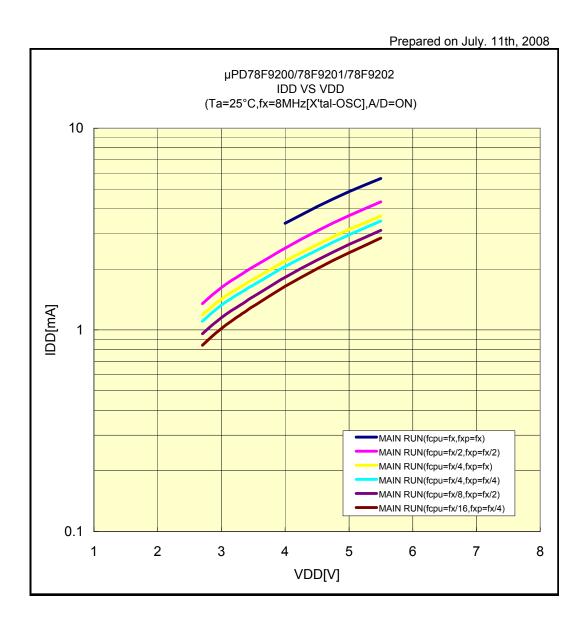
IDD VS VDD(25°C/5MHz[X'tal-OSC]/AD=ON)



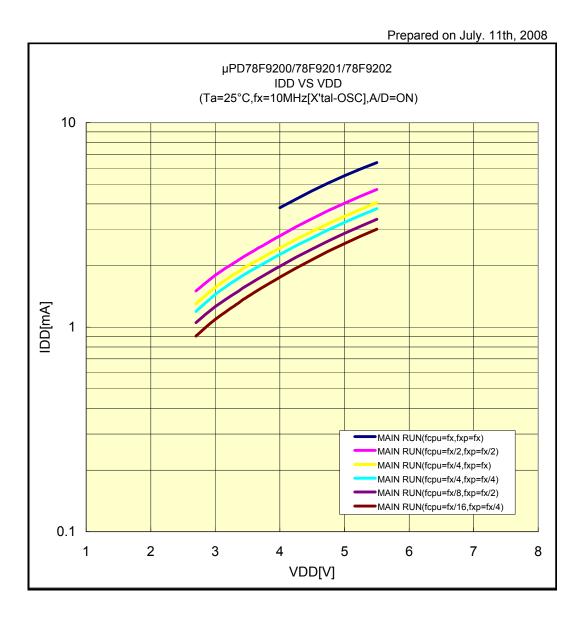
IDD VS VDD(25°C/6MHz[X'tal-OSC]/AD=ON)



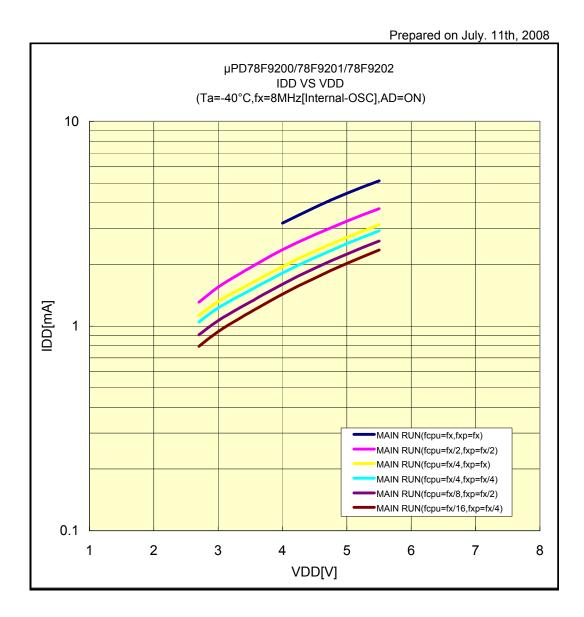
IDD VS VDD(25°C/8MHz[X'tal-OSC]/AD=ON)



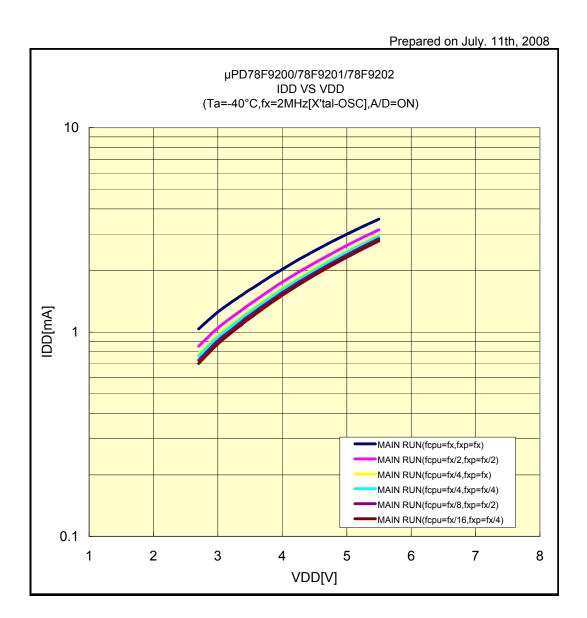
IDD VS VDD(25°C/10MHz[X'tal-OSC]/AD=ON)



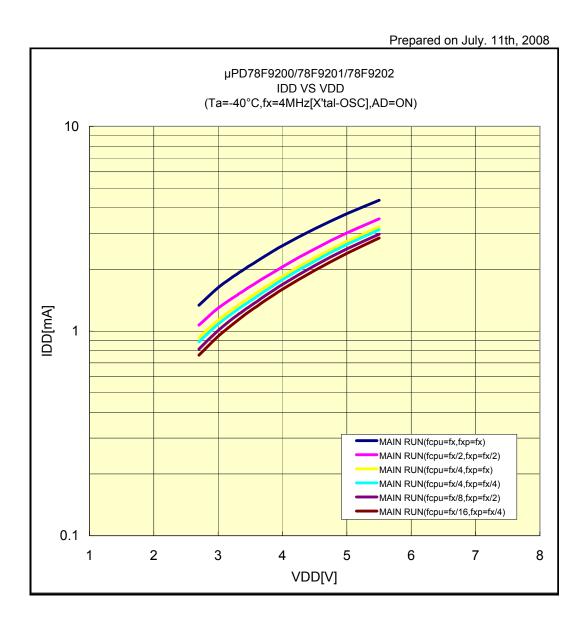
IDD VS VDD(-40°C/8MHz[Internal-OSC]/AD=ON)



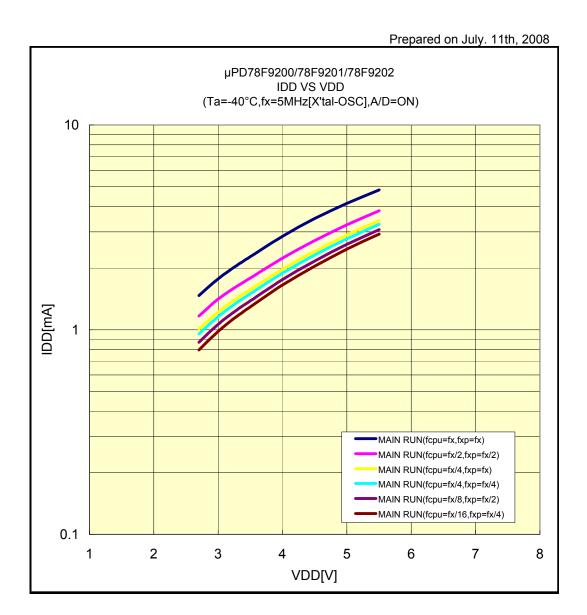
IDD VS VDD(-40°C/2MHz[X'tal-OSC]/AD=ON)



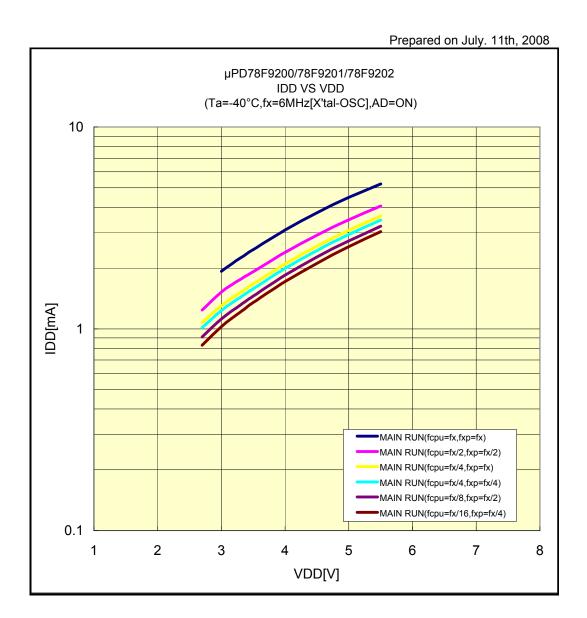
IDD VS VDD(-40°C/4MHz[X'tal-OSC]/AD=ON)



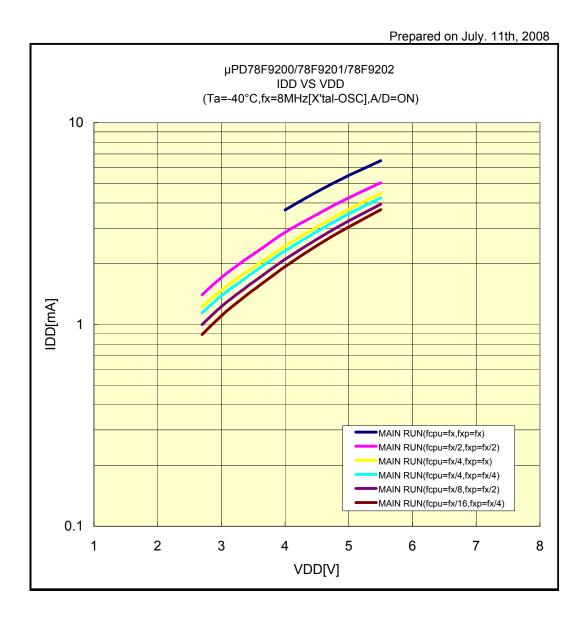
IDD VS VDD(-40°C/5MHz[X'tal-OSC]/AD=ON)



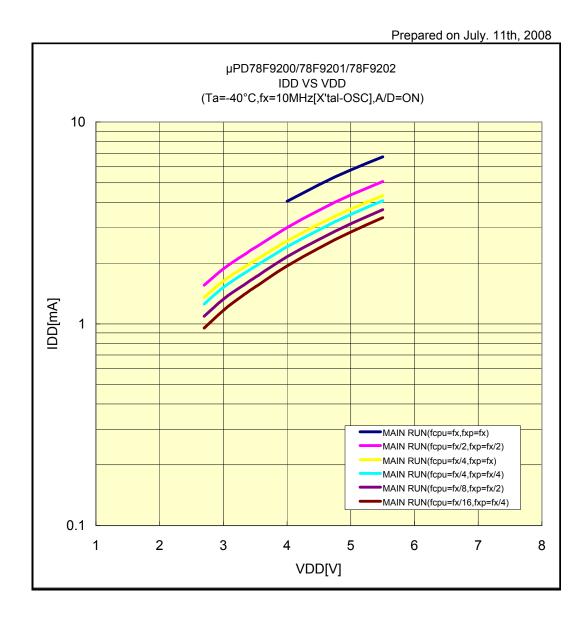
IDD VS VDD(-40°C/6MHz[X'tal-OSC]/AD=ON)



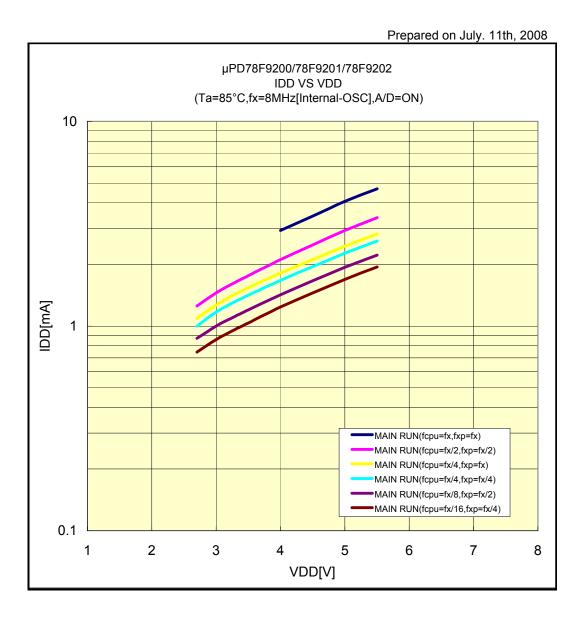
IDD VS VDD(-40°C/8MHz[X'tal-OSC]/AD=ON)



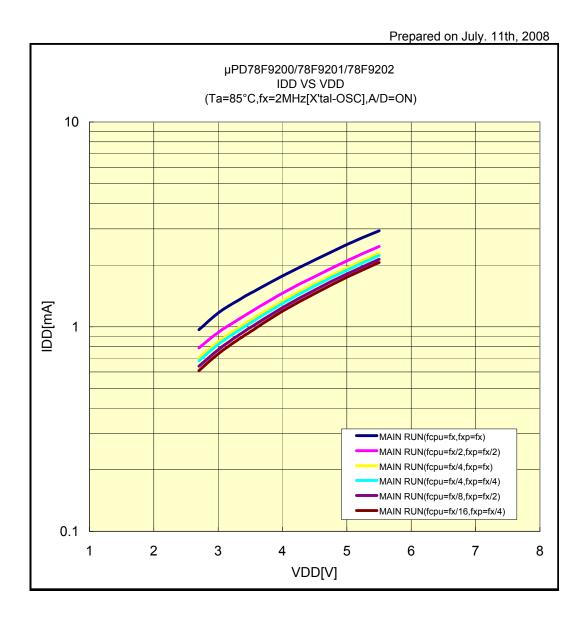
IDD VS VDD(-40°C/10MHz[X'tal-OSC]/AD=ON)



IDD VS VDD(85°C/8MHz[Internal-OSC]/AD=ON)

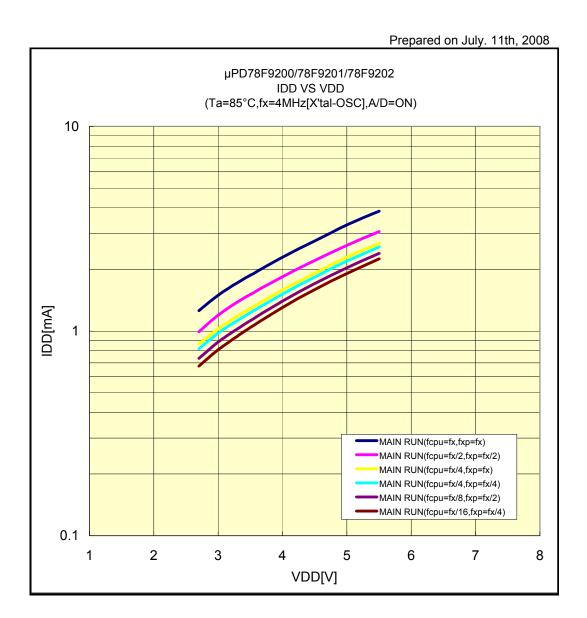


IDD VS VDD(85°C/2MHz[X'tal-OSC]/AD=ON)

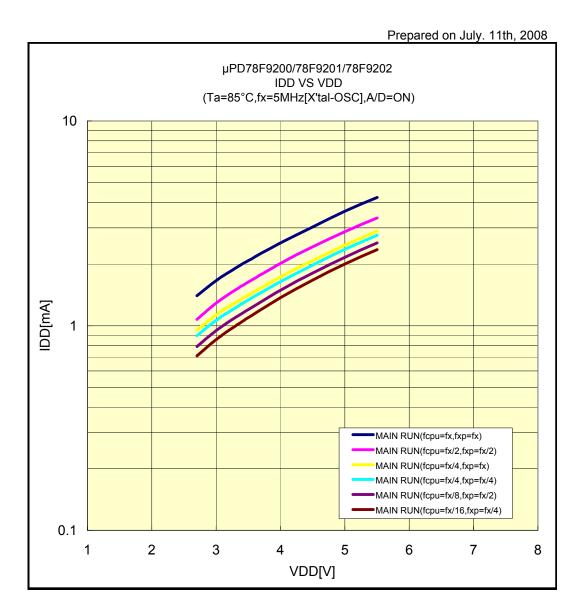


$\mu PD78F \mu PD78F9200/78F9201/78F9202$

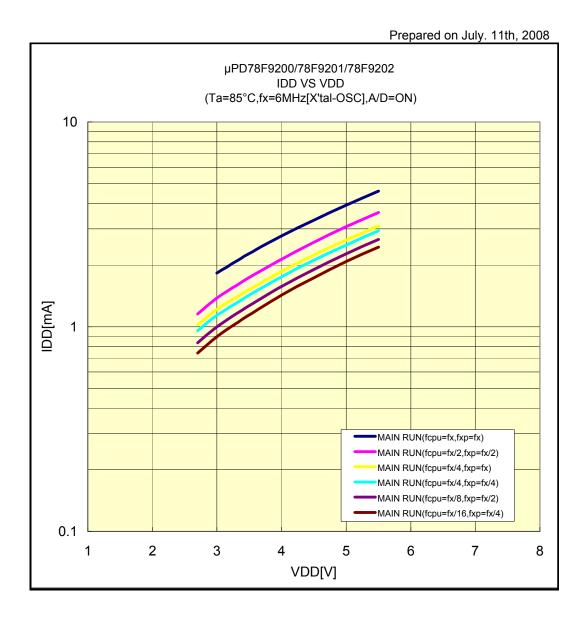
IDD VS VDD(85°C/4MHz[X'tal-OSC]/AD=ON)



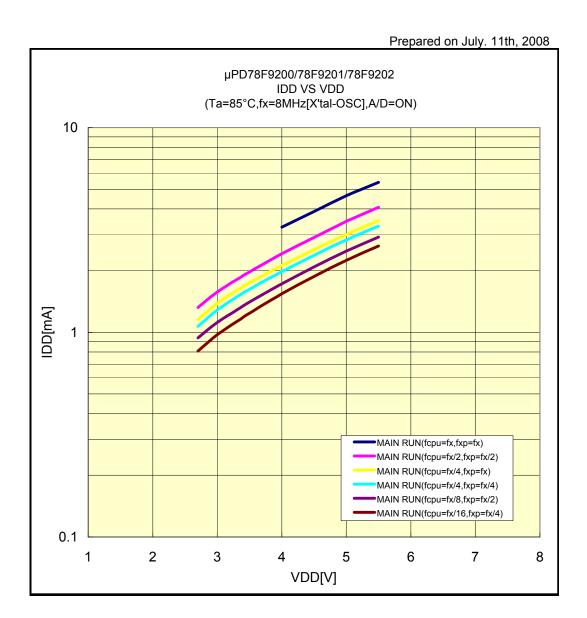
IDD VS VDD(85°C/5MHz[X'tal-OSC]/AD=ON)



IDD VS VDD(85°C/6MHz[X'tal-OSC]/AD=ON)



IDD VS VDD(85°C/8MHz[X'tal-OSC]/AD=ON)



IDD VS VDD(85°C/10MHz[X'tal-OSC]/AD=ON)

