The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/1MHz[Internal-OSC])
Normal Power Mode

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/1MHz[Internal-OSC])
Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011

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UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/1MHz[Internal-OSC])
Low Power Mode

Prepared on Oct. 11th, 2011

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/1MHz[Internal-OSC])
Low Power Mode(HALT)

Prepared on Oct. 11th, 2011

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mA
\( m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8 \)
\( m=1 : n=0, 2, 3, 5, 6, 8 \)

**IDD VS VDD(-40°C/2MHzCeramic-OSC])**

**Normal Power Mode**

<table>
<thead>
<tr>
<th>VDD [V]</th>
<th>IDD [mA]</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0.1</td>
</tr>
<tr>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>10</td>
</tr>
</tbody>
</table>

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The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mnA

- m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
- m=1 : n=0, 2, 3, 5, 6, 8

### IDD VS VDD(-40°C/4MHzCeramic-OSC])

**Normal Power Mode**

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mnA
m=0: n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1: n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/4MHzCeramic-OSC])
Normal Power Mode(HALT)

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/5MHzCeramic-OSC])
Normal Power Mode

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UPD78F15mnA  
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8  
m=1 : n=0, 2, 3, 5, 6, 8  

IDD VS VDD(-40°C/5MHzCeramic-OSC])  
Normal Power Mode(HALT)  

Prepared on Oct. 11th, 2011  
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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/6MHzCeramic-OSC])
Normal Power Mode

Prepared on Oct. 11th, 2011

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/8MHzCeramic-OSC])
Normal Power Mode

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/8MHzCeramic-OSC])
Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/10MHzCeramic-OSC])
Normal Power Mode

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/16MHzCeramic-OSC)]
AMPH=1,FSEL=1(HALT)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/16MHzCeramic-OSC])
AMPH=1,FSEL=0

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/16MHzCeramic-OSC])
AMPH=1,FSEL=0(HALT)

Prepared on Oct. 11th, 2011

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mnA
m=0: n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1: n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/20MHzCeramic-OSC])
AMPH=1,FSEL=1

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UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/20MHzCeramic-OSC])
AMPH=1, FSEL=1(HALT)

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD (-40°C/20MHz Ceramic-OSC)**
AMPH=1, FSEL=0

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mnA
m=0: n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1: n=0, 2, 3, 5, 6, 8

IDD VS VDD (-40°C/20MHz Ceramic-OSC])
AMPH=1, FSEL=0 (HALT)

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UPD78F15mnA
m=0: n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1: n=0, 2, 3, 5, 6, 8

**IDD VS VDD(25°C/8MHz[Internal-OSC])**
Normal Power Mode

Prepared on Oct. 11th, 2011

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/8MHz[Internal-OSC])
Normal Power Mode(HALT)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(25°C/1MHz[Internal-OSC])**
Normal Power Mode

Prepared on Oct. 11th, 2011

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
**UPD78F15mnA**

\[ m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8 \]
\[ m=1 : n=0, 2, 3, 5, 6, 8 \]

**IDD VS VDD(25°C/1MHz[Internal-OSC])**

**Normal Power Mode(HALT)**

Prepared on Oct. 11th, 2011

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UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(25°C/8MHz[Internal-OSC])**
Low Power Mode

Prepared on Oct. 11th, 2011

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/8MHz[Internal-OSC])
Low Power Mode(HALT)

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(25°C/1MHz[Internal-OSC])**
Low Power Mode(HALT)

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/2MHzCeramic-OSC])
Normal Power Mode(HALT)

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD (25°C/4MHz Ceramic-OSC)**
Normal Mode

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UPD78F15mA
m=0: n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1: n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/4MHzCeramic-OSC])
Normal Power Mode(HALT)

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**UPD78F15mnA**

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8  
m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD (25°C/6MHz Ceramic-OSC)**  
Normal Power Mode (HALT)

Prepared on Oct. 11th, 2011

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/8MHzCeramic-OSC)]
Normal Power Mode

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UPD78F15mA
\( m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8 \)
\( m=1 : n=0, 2, 3, 5, 6, 8 \)

**IDD VS VDD(25°C/10MHzCeramic-OSC])**
*Normal Power Mode*

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UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD (25°C/16MHzCeramic-OSC)**
AMPH=1,FSEL=1 (HALT)

Prepared on Oct. 11th, 2011

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UPD78F15mA
\(m=0: n=0, 1, 2, 3, 4, 5, 6, 7, 8\)
\(m=1: n=0, 2, 3, 5, 6, 8\)

**IDD VS VDD(25°C/16MHzCeramic-OSC])**
AMPH=1,FSEL=0

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UPD78F15mnA
m=0: n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1: n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/20MHzCeramic-OSC])
AMPH=1,FSEL=1(HALT)

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UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD (25°C/20MHz Ceramic-OSC)
AMPH=1, FSEL=0

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD (25°C/32.768KHz[X'tal-OSC])

Notes
1. The purple line (SUBHALT_WATCH(SDIV=0)) overlaps with the turquoise line (SUBHALT_WATCH(SDIV=1)).
2. The orange line (SUBHALT_WATCH(SDIV=0,RTC=ON)) overlaps with the coral line (SUBHALT_WATCH(SDIV=1,RTC=ON)).

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UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/1MHz[Internal-OSC])
Normal Power Mode

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/1MHz[Internal-OSC])
Normal Power Mode(HALT)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.
UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/8MHz[Internal-OSC])
Low Power Mode

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/1MHz[Internal-OSC])
Low Power Mode

Prepared on Oct. 11th, 2011

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UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/1MHz[Internal-OSC])
Low Power Mode(HALT)

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UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/2MHz[Ceramic-OSC])
Normal Power Mode(HALT)

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/4MHz[Ceramic-OSC])
Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011

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UPD78F15mA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(85°C/6MHz[Ceramic-OSC])**
*Normal Power Mode*

Prepared on Oct. 11th, 2011

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD (25°C/6MHz[Ceramic-OSC])
Normal Power Mode (HALT)

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UPD78F15mnA

m=0: n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1: n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/8MHzCeramic-OSC])
Normal Power Mode(HALT)

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/16MHz[Ceramic-OSC])
AMPH=1,FSEL=1

Prepared on Oct. 11th, 2011

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/16MHz[Ceramic-OSC])
AMPH=1,FSEL=1(HALT)

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(85°C/16MHz[Ceramic-OSC])**
AMPH=1, FSEL=0

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/20MHz[Ceramic-OSC])
AMPH=1,FSEL=1(HALT)

![Graph showing IDD vs VDD](image)

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UPD78F15mnA
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8
m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/32.768KHz[X'tal-OSC])

Prepared on Oct. 11th, 2011

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